Triacs BT137X series

GENERAL DESCRIPTION

Glass passivated triacs in a full pack plastic envelope, intended for use in applications requiring high bidirectional transient and blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

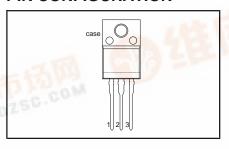
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
- 松岡	BT137X- BT137X- BT137X-	500 500F 500G	600 600F 600G	800 800F 800G	
V_{DRM}	Repetitive peak off-state voltages	500	600	800	V
I _{T(RMS)} I _{TSM}	RMS on-state current Non-repetitive peak on-state current	8 65	8 65	8 65	A A

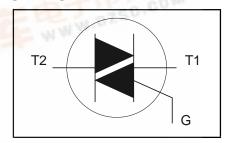
PINNING - SOT186A

PIN	DESCRIPTION
1	main terminal 1
2	main terminal 2
3	gate
case	isolated

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	THE.	MAX.		UNIT
V_{DRM}	Repetitive peak off-state voltages	- 12 MI 6 19 2 E	13	-500 500 ¹	-600 600 ¹	-800 800	V
$\mathbf{I}_{T(RMS)}$	RMS on-state current Non-repetitive peak on-state current	full sine wave; $T_{hs} \le 73$ °C full sine wave; $T_j = 25$ °C prior to surge	-		8		A
	HI LOL W.	t = 20 ms	-		65		A
l²t dl _⊤ /dt	I ² t for fusing Repetitive rate of rise of on-state current after	t = 16.7 ms t = 10 ms $I_{TM} = 12 \text{ A}; I_G = 0.2 \text{ A};$ $dI_G/dt = 0.2 \text{ A}/\mu\text{s}$	=		71 21		A A ² s
	triggering	T2+ G+ T2+ G- T2- G- T2- G+	TE.	WW	50 50 50 10		Α/μs Α/μs Α/μs Α/μs
I_{GM} V_{GM} P_{GM}	Peak gate current Peak gate voltage Peak gate power	TIME OF THE PROPERTY OF THE PR	-		2 5 5		V W
$\begin{array}{c} P_{G(AV)} \\ T_{stg} \\ T_{j} \end{array}$	Average gate power Storage temperature Operating junction temperature	over any 20 ms period	-40 -		0.5 150 125		,C ,C

1 Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 6 A/μs.

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ISOLATION LIMITING VALUE & CHARACTERISTIC

 $T_{hs} = 25$ °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{isol}	R.M.S. isolation voltage from all three terminals to external heatsink	f = 50-60 Hz; sinusoidal waveform; R.H. ≤ 65%; clean and dustfree	ı		2500	>
C _{isol}	Capacitance from T2 to external heatsink	f = 1 MHz	ı	10	1	pF

THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{\text{th j-hs}}$ $R_{\text{th j-a}}$	Thermal resistance junction to heatsink Thermal resistance	full or half cycle with heatsink compound without heatsink compound in free air	- - -	- - 55	4.5 6.5 -	K/W K/W K/W
, a	junction to ambient					

STATIC CHARACTERISTICS

T_i = 25 °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.		MAX.		UNIT
I _{GT}	Gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$:	F	G	
61	33	T2+ G+ T2+ G-	-	5 8	35 35	25 25	50 50	mA mA
		T2- G- T2- G+	-	11 30	35 70	25 70	50 100	mA mA
I _L	Latching current	$V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$ T2+ G+		7		30	45	
		T2+ G-	-	16	30 45	45	60	mA mA
		T2- G- T2- G+	-	5 7	30 45	30 45	45 60	mA mA
I _H	Holding current	$V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$	-	5	20	20	40	mA
V _T	On-state voltage	$I_{T} = 10 \text{ A}$	-	1.3		1.65		V
V _{GT}	Gate trigger voltage	$\dot{V}_D = 12 \text{ V}; I_T = 0.1 \text{ A}$ $\dot{V}_D = 400 \text{ V}; I_T = 0.1 \text{ A};$	- 0.25	0.7 0.4		1.5 -		V V
I _D	Off-state leakage current	$T_{j} = 125 °C$ $V_{D} = V_{DRM(max)};$ $T_{j} = 125 °C$	-	0.1		0.5		mA

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DYNAMIC CHARACTERISTICS

T_i = 25 °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS		MIN.		TYP.	MAX.	UNIT
dV _D /dt	Critical rate of rise of off-state voltage	BT137X- $V_{DM} = 67\% V_{DRM(max)};$ $T_i = 125 °C;$ exponential	 100	F 50	G 200	250	-	V/μs
dV _{com} /dt	Critical rate of change of commutating voltage	waveform; gate open circuit $V_{DM} = 400 \text{ V}; T_j = 95 ^{\circ}\text{C};$ $I_{T(RMS)} = 8 \text{ A};$ $dI_{com}/dt = 3.6 \text{ A/ms}; gate$	-	-	10	20	-	V/μs
t _{gt}	Gate controlled turn-on time	open circuit $I_{TM} = 12 \text{ A}$; $V_D = V_{DRM(max)}$; $I_G = 0.1 \text{ A}$; $dI_G/dt = 5 \text{ A}/\mu \text{s}$	-	-	-	2	-	μs

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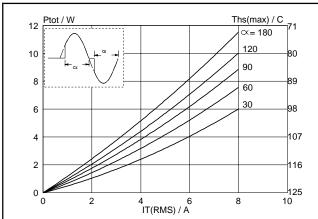


Fig.1. Maximum on-state dissipation, P_{tot} , versus rms on-state current, $I_{T(RMS)}$, where α = conduction angle.

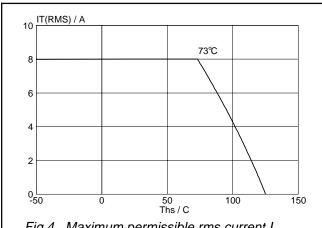


Fig.4. Maximum permissible rms current $I_{T(RMS)}$, versus heatsink temperature T_{hs} .

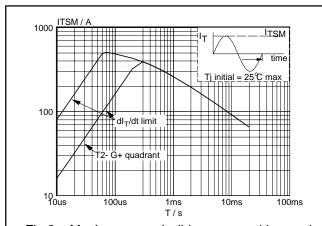


Fig.2. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus pulse width t_p , for sinusoidal currents, $t_p \le 20$ ms.

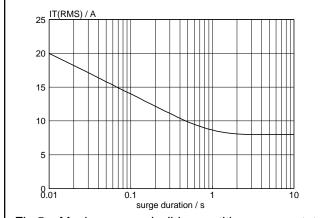


Fig.5. Maximum permissible repetitive rms on-state current $I_{T(RMS)}$, versus surge duration, for sinusoidal currents, f = 50 Hz; $T_{hs} \le 73$ °C.

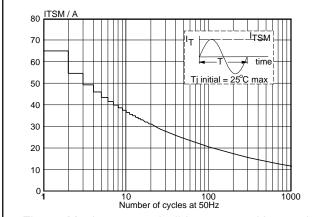


Fig.3. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus number of cycles, for sinusoidal currents, f = 50 Hz.

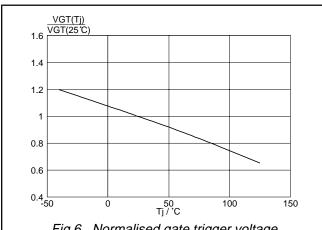
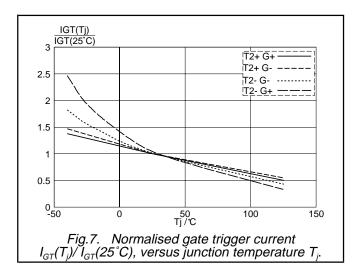


Fig.6. Normalised gate trigger voltage $V_{GT}(T_j)/V_{GT}(25^{\circ}C)$, versus junction temperature $T_{j\cdot}$

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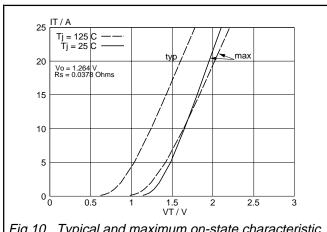
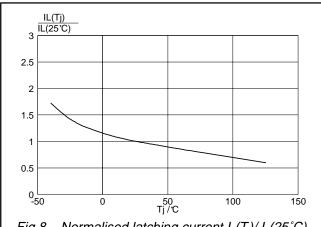


Fig. 10. Typical and maximum on-state characteristic.



Normalised latching current $I_L(T_j)/I_L(25^{\circ}C)$, versus junction temperature T

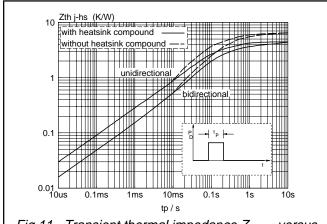


Fig.11. Transient thermal impedance $Z_{th j-hs}$, versus pulse width to.

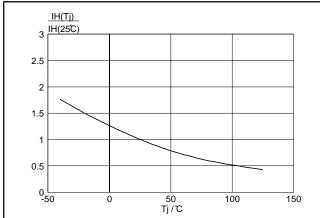


Fig.9. Normalised holding current $I_H(T_i)/I_H(25^{\circ}C)$, versus junction temperature T_i .

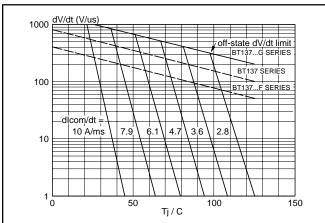
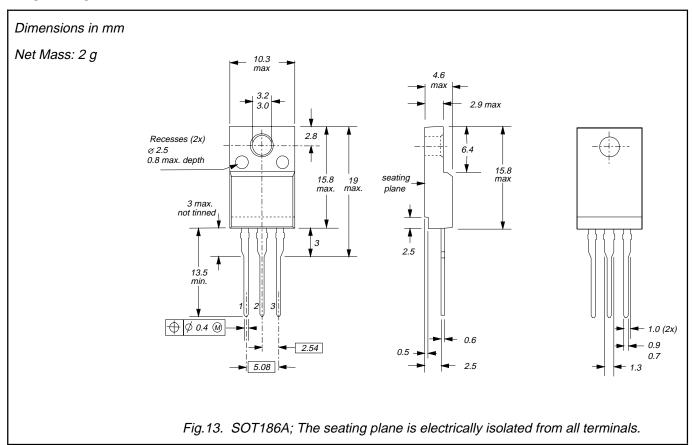


Fig.12. Typical commutation dV/dt versus junction temperature, parameter commutation dl_T/dt. The triac should commutate when the dV/dt is below the value on the appropriate curve for pre-commutation dl_r/dt.

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MECHANICAL DATA



- Notes
 1. Refer to mounting instructions for F-pack envelopes.
 2. Epoxy meets UL94 V0 at 1/8".

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DEFINITIONS

Data sheet status							
Objective specification	This data sheet contains target or goal specifications for product development.						
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.						
Product specification	This data sheet contains final product specifications.						
1 reduct op comeditors	The data check contains into product opening and in						

Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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