

# **CXA1211M**

# **Electronic Volume**

### **Description**

The CXA1211M is a wide band general-purpose VCA. This bipolar IC incorporates 2 chnannels.

#### **Features**

- Wide band frequency characteristics: 100kHz to 20MHz (–0.5dB)
- Wide dynamic range
- · Low noise, low distortion
- Low power consumption



Video signals and other wide band VCA

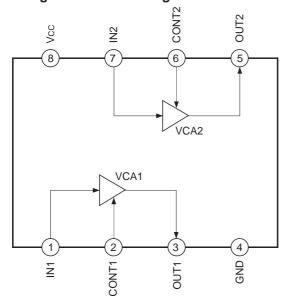
# **Operating Conditions**

Supply voltage Vcc 4.50 to 5.50 V

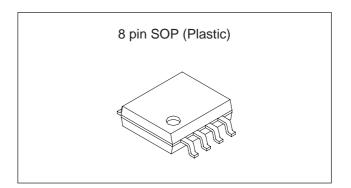
#### **Absolute Maximum Ratings**

Supply voltage Vcc 14 V
 Operating temperature Topr -20 to +75 °C
 Storage temperature Tstg -65 to +150 °C
 Allowable power dissipation
 PD 510 mW

#### **Block Diagram and Pin Configuration**



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# **Pin Description**

Pin			I/O	Equivalent circuit	Description		
No.	Symbol	DC	AC	resistance	Equivalent circuit	Description	
1	IN1	2.5V	1.0Vp-p*	40kΩ	1 3k 3k 40k W F 7777 GND	Signal input pin (CH1)	
2	CONT1	2.7V*	_		24k 5k 7/// 100µ 1µ 100µ GND	Input pin of gain control signal (CH1). At "Low" power save is possible. Low: 1V and below	
3	OUT1	1.9V	1.0Vp-p		Vcc \$129	Signal output pin (CH1)	
4	GND	0V*	_	_	_	GND pin	
5	OUT2	1.9V	1.0Vp-p		Vcc \$129	Signal output pin (CH2)	

<sup>\*</sup> External input

Pin	Symbol	Volt		I/O	Equivalent circuit	Description	
No.	Cymbol	DC	AC	resistance	Equivalent enedit	Doscription	
6	CONT2	2.7V*	_		6 24k 5k W 100µ 1µ 100µ GND	Input pin of gain control signal (CH2). At "Low" power save is possible. Low: 1V and below	
7	IN2	2.5V	1.0Vp-p*	40kΩ	7 3k 3k 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7	Signal input pin (CH2)	
8	Vcc	5V*	_	_	_	Vcc pin	

<sup>\*</sup> External input

#### **Notes on Operation**

Do not fail to take the following precautions upon usage of the CXA1211M.

1. Countermeasure to cross talk between channels

Fix a bypass capacitor to Pins CONT1 and CONT2 that control the amplifier gain. When the impedance of the control voltage source is visible, depending on the package volume and others, cross talk between channels is easily generated.

2. Input signal dynamic range

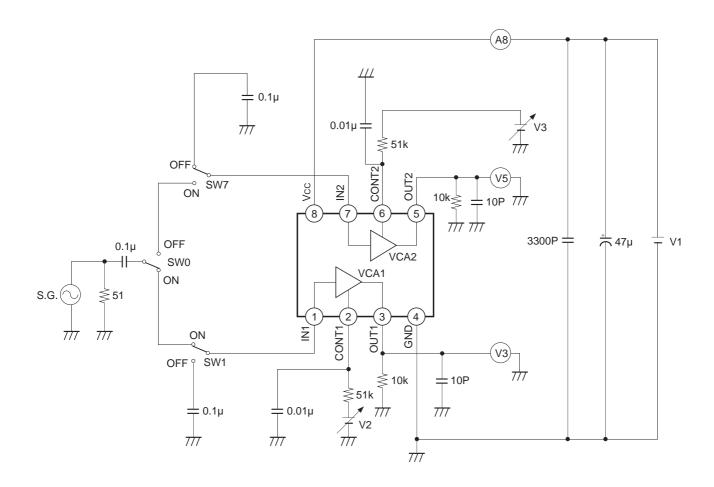
The input dynamic range is at a max of 1.4Vp-p. When the input signal exceeds 1.4Vp-p, the waveform may be clipped and deformed.

# **Electrical Characteristics**

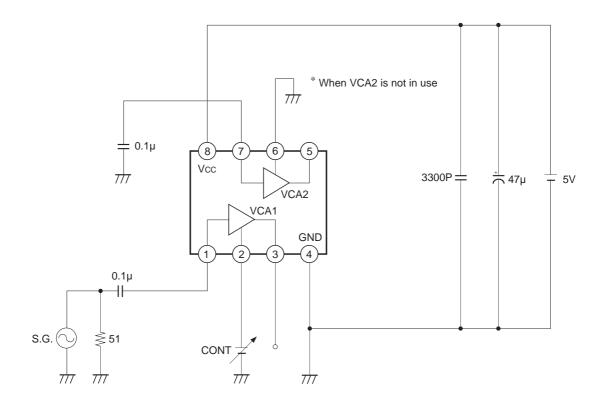
(Ta = 25°C, Vcc = 5.0V, See Electrical Characteristics Test Circuit.)

Item			Test conditions					point					
		Symbol	Input conditions			CONT	SW set	st po	Test method	Min.	Тур.	Max.	Unit
			IN	Level	Freq	voltage (V)	to ON	Test					
Current	1-ch	Ілсн				0	0	A8	When only 1ch is used, ch used is set to 2.87V.	2.0	4.0	6.0	m A
consumption	Both-ch	І2СН				2.87				4.5	7.5	12	mA
Moy goin	CH1	G1MAX	1	500	300	5.0	0, 1	3		3.0	5.0	7.0	dB
Max. gain	CH2	G2MAX	7	mVp-p	kHz		7	5		3.0	3.0	7.0	ub
Min. gain	CH1	G <sub>1</sub> MIN	1	500 mVp-p	300 kHz	1.8	0, 1	3		-7.0	-5.0	-3.0	dB
iviiii. yaiii	CH2	G <sub>2</sub> MIN	7				7	5		-7.0	-5.0	-3.0	ub
Frequency	CH1	V <sub>F1</sub>	1	500 mVp-p 300k to 20M		2.87	0, 1	3	20MHz level	-1.0		+1.3	dB
characteristics	CH2	VF2	7		2.07	7	5	300kHz level	1.0		71.3	ub	
Distortion 1	CH1	D <sub>1</sub> CH	1	1.0	5.0		0, 1	3	Adjust CONT to		<b>-40</b>		dB
Distortion	CH2	D <sub>2</sub> CH	7	Vp-p MHz			7	5	obtain 1.0Vp-p at output		<del>-4</del> 0		ub
S/N	CH1	N <sub>1</sub> CH	1	0.5 100k to	1.8	0, 1	3	Test at no signal		61		dB	
G/IN	CH2	<b>N</b> 2CH	7	Vp-p	4.2M	1.0	7	5	conditon		01		uБ

## **Electrical Characteristics Test Circuit**



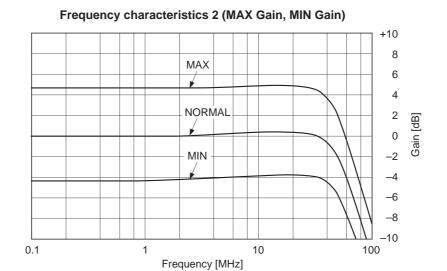
# **Application Circuit**



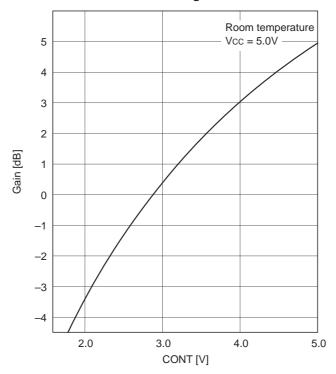
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#### Frequency characteristics 1 (When Gain = 0dB) 150° 2.0 100° 0 Gain 50° -2 0° -4 Phase [degree] –50° -6 Phase -100° -8 -150° -10 -200° -12 –250° -14 -300° -16 -350° 0.1 10

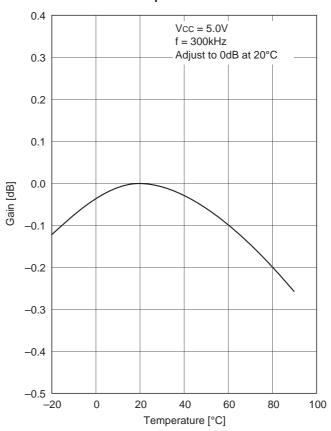
Frequency [MHz]



Gain vs. CONT voltage characteristics

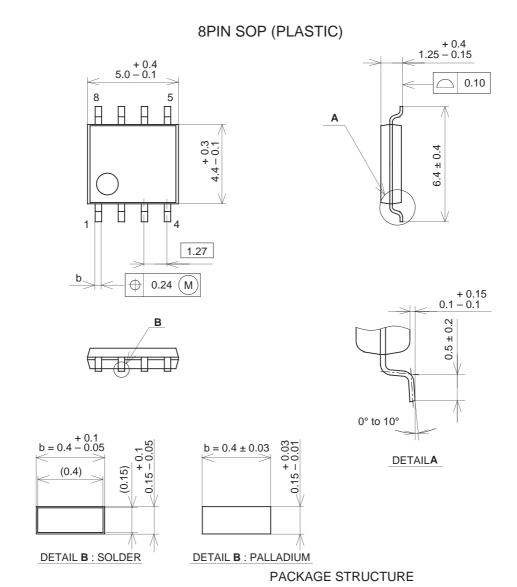


Gain vs. Temperature characteristics



SONY

## Package Outline Unit: mm



SONY CODE	SOP-8P-L03
EIAJ CODE	SOP008-P-0225
JEDEC CODE	

PACKAGE MATERIAL	EPOXY RESIN
LEAD TREATMENT	SOLDER/PALLADIUM PLATING
LEAD MATERIAL	42/COPPER ALLOY
PACKAGE MASS	0.1g

**NOTE: PALLADIUM PLATING** 

This product uses S-PdPPF (Sony Spec.-Palladium Pre-Plated Lead Frame).