

YOU DA INTEGRATED CIRCUIT

YD5954

**ACTUATOR DRIVER WITH CURRENT FEEDBACK
FOR CD – ROM/DVD—YD5954**

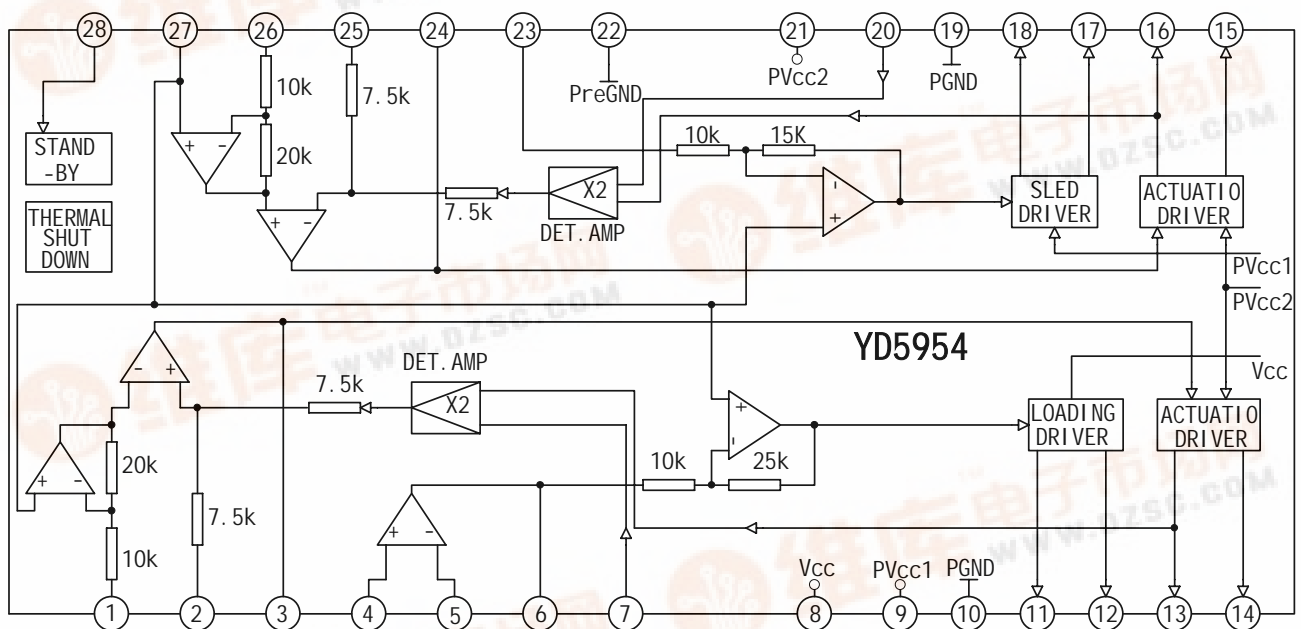
DESCRIPTION

The YD5954 is a four-channel BTL driver IC for driving the motors and actuators in products such as CD-ROM/DVD drivers. Two of the channels use current feedback to minimize the current phase shift caused by the influence of load inductance.

FEATURES

- *Wide dynamic range(4.0V(typ.) when $V_{CC}=12V$, $PV_{CC}=5V$, and $R_L=8 \Omega$).
- *Level shift circuit on chip.
- *Thermal shutdown circuit on chip.

BLOCK DIAGRAM



WuXi YouDa Electronics Co., Ltd

Add: No.5 Xijin Road, National Hi-Tech Industrial Development Zone, Wuxi Jiangsu China

Tel: 86-510-5205117 86-510-5205108

Fax: 86-510-5205110

Website: www.e-youda.com

SHENZHEN OFFICE Tel : 86-755-83740369

13823533350

Fax : 86-755-83741418



ABSOLUTE MAXIMUM RATINGS (Tamb=25)

PARAMETER	SYMBOL	VALUE	UNIT
Power Supply Voltage	V _{CC} , PV _{CC1} /2	13.5	V
Power Dissipation	P _D	1.7	W
Operating Temperature	Topr	-34 to +85	
Storage Temperature	Tstg	-55 to +150	

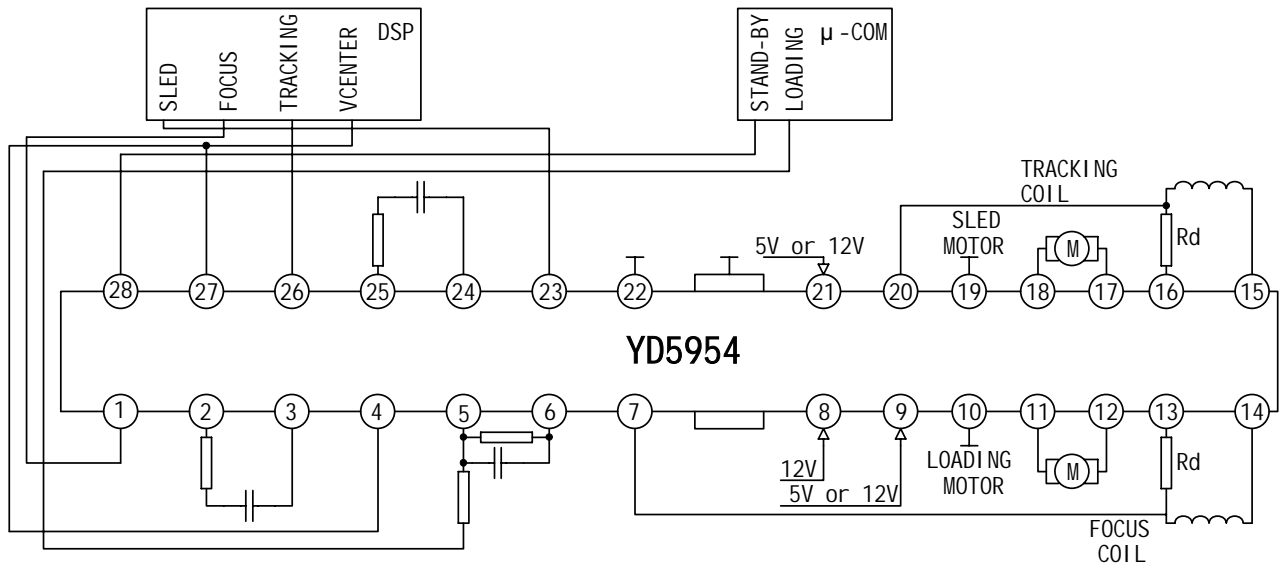
ELECTRICAL CHARACTERISTICS

(Unless other specified, Tamb=25 , Vcc=12V, Pvcc1=Pvcc2=5V, BIAS=2.5V, R_L=8., Rd=0.5, C= 100pF)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Quiescent Current	I _{CC}			18	27	mA
Actuator Driver						
Output Offset Current	I _{OOF}		-6		6	mA
Maximum Output Amplitude	V _{OM}		3.6	4.0		V
Transmission Gain	G _M	V _{IN} =BIAS ± 0.2V	1.3	1.5	1.7	A/V
Loading Driver						
Input op-amp Same Phase Input Range	V _{ICM}		-0.3		11.0	V
Input Bias Current (outflow current)	I _{BOP}			30	300	nA
Output High Level Voltage	V _{OHO}	No load	10.8	11.1		V
Output Low Level Voltage	V _{OLO}	No load		0.8	1.1	V
Output Offset Voltage	V _{OOFD}		-100	0	100	mV
Maximum Output Amplitude	V _{OMD}		7.5	9.0		V
Closed-circuit Voltage Gain	G _{VLD}	V _{IN} =BIAS ± 0.2V	18.0	20.0	22.0	dB
Sled Driver						
Offset Voltage	V _{OOFSL}		-50	0	50	mV

Maximum Output Amplitude	V_{OMSL}		3.6	4.0		V
Voltage Gain	G_{VSL}	$V_{IN}=BIAS \pm 0.2V$	13.5	15.5	17.5	dB
F/R Gain Differential	ΔG_{VSL}	$V_{IN}=BIAS \pm 0.2V$	0	1	2	dB
STBY Logic						
STBY On Voltage	V_{STBY1}	All Channels Off	0		0.5	V
STBY Off Voltage	V_{STBY2}	All Channels On	2.0			V

APPLICATION CIRCUIT



OUTLINE DRAWING

