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HIGH TEMPERATURE ADJUSTABLE PRECISION ZENER SHUNT REGULATOR

ZHT2431

ISSUE 1 JULY 1997

DEVICE DESCRIPTION

The ZHT2431 is a three terminal adjustable shunt regulator offering excellent temperature stability and output current handling capability up to 25mA. The output voltage may be set to any chosen voltage between 1.24 and 15 volts by selection of two external divider resistors.

The devices can be used as a replacement for zener diodes in many applications requiring an improvement in zener performance.

The ZHT2431 is particularly used in the feedback control loop of switch mode power supplies. In this application the device 1.24 volt reference enables the generation of low voltage supplies, typically 3.3 volts or 3 volts.

The ZHT2431 is intended for use over the extended operating range of -55°C to 125°C.

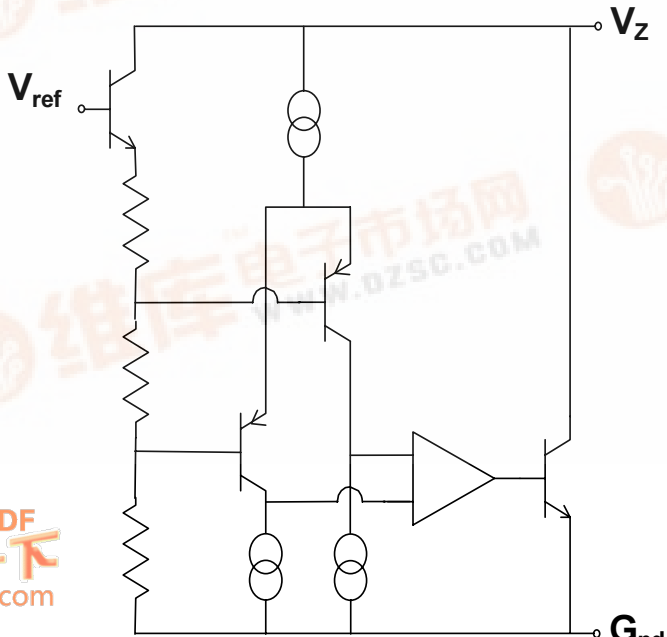
FEATURES

- Surface mount SOT23 package
- TO92 package
- 2.5% tolerance
- Average temperature coefficient 50 ppm/°C
- Temperature compensated for operation over the full temperature range
- Programmable output voltage
- 100µA to 25mA current sink capability
- Low output noise
- -55° to 125° C operation

APPLICATIONS

- Shunt regulator
- Series regulator
- Voltage monitor
- Over voltage/ under voltage protection
- Switch mode power supplies

SCHEMATIC DIAGRAM



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ZHT2431

ABSOLUTE MAXIMUM RATING

Cathode Voltage (V_Z) 15V
Cathode Current 50mA
Operating Temperature -55 to 125°C
Storage Temperature -55 to 125°C

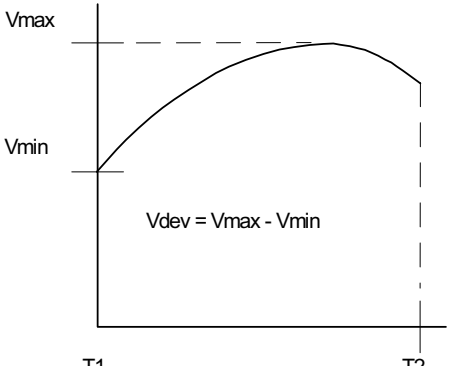
Power Dissipation (T_{amb}=25°C, T_{jmax}=150°C)
SOT23 330mW
TO92 780mW

Recommended Operating Conditions
Cathode Voltage (min)V_{REF} to (max) 15V
Cathode Current (min) 100µA to (max) 25mA

ELECTRICAL CHARACTERISTICS
TEST CONDITIONS (Unless otherwise stated):T_{amb}=25°C

PARAMETER	SYMBOL	VALUE			UNITS	CONDITIONS
		MIN	TYP	MAX		
Reference Voltage 2.5%	V_{ref}	1.21	1.24	1.27	V	I_L=10mA (Fig1), V_Z=V_{ref}
Deviation of Reference Input Voltage over Temperature	V_{dev}		5.0	15.0	mV	I_L=10mA, V_Z=V_{ref} T_a=full range (Fig1)
Ratio of the change in Reference Voltage to the Change in Cathode Voltage	$\frac{\Delta V_{ref}}{\Delta V_Z}$		0.5	2.0	mV/V	V_Z from V_{ref} to 10V I_Z=10mA (Fig2)
Reference Input Current	I_{ref}	0.02	0.11	0.4	µA	R1=10K, R2=O/C, I_L=10mA (Fig2)
Deviation of Reference Input Current over Temperature	ΔI_{ref}		0.02	0.2	µA	R1=10K, R2=O/C, I_L=10mA T_a=full range (Fig2)
Minimum Cathode Current for Regulation	I_{Zmin}		30	100	µA	†
Off-state Current	I_{Zoff}		10	30	µA	V_Z=15V, V_{ref} =0V (Fig3)
Dynamic Output Impedance	R_Z		0.25	2	Ω	V_Z=V_{ref} (Fig1), f=0Hz, I_L=10mA

Deviation of reference input voltage, V_{dev}, is defined as the maximum variation of the reference input voltage over the full temperature range.
The average temperature coefficient of the reference input voltage, V_{ref} is defined as:



$$V_{ref}(ppm/°C) = \frac{V_{dev} \times 1000000}{V_{ref}(T1 - T2)}$$

The dynamic output impedance, Rz, is defined as:

$$R_Z = \frac{\Delta V_Z}{\Delta I_Z}$$

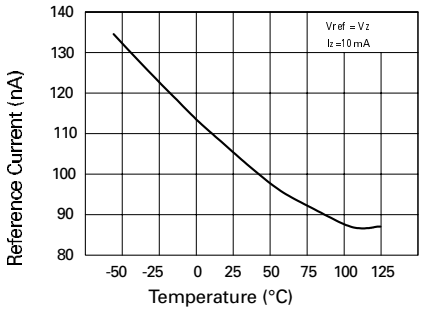
When the device is programmed with two external resistors, R1 and R2, (fig 2) , the dynamic output impedance of the overall circuit, R', is defined as:

$$R' = R_z\left(1 + \frac{R1}{R2}\right)$$

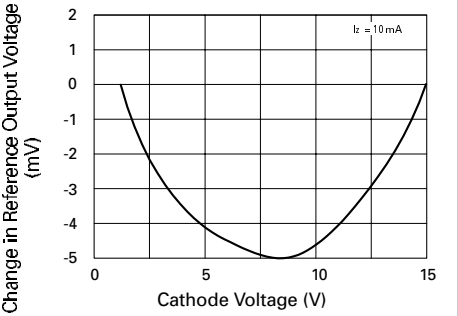
† With a capacitance of greater than 100pF between cathode and anode, minimum cathode current must be 0.2mA.

ZHT2431

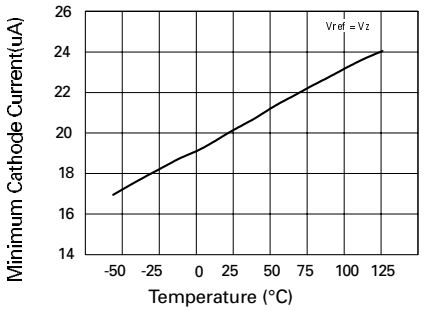
TYPICAL CHARACTERISTICS



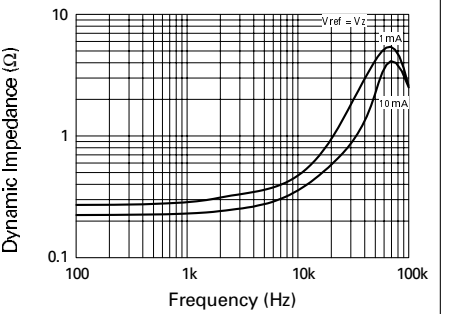
Reference Current v Temperature



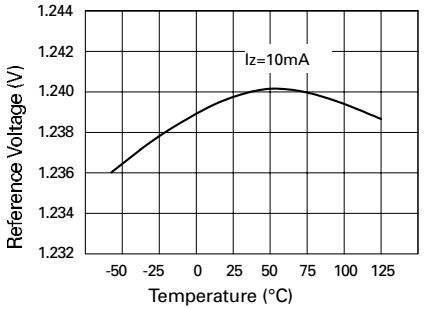
Change in V_{ref} v Cathode Voltage



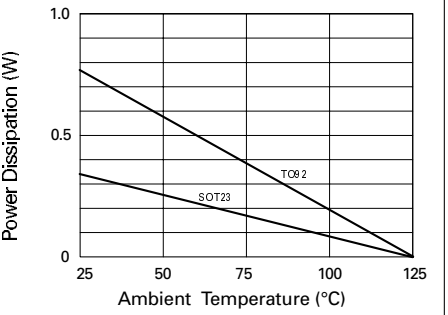
Cathode Current v Temperature



Dynamic Impedance vs. Frequency

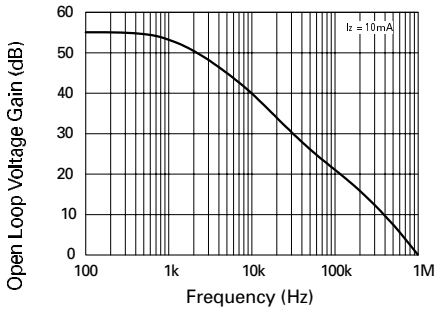


Reference Voltage v Temperature

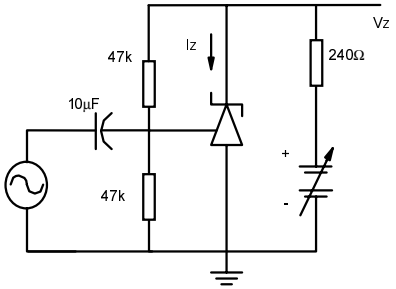


Power Dissipation Derating

TYPICAL CHARACTERISTICS

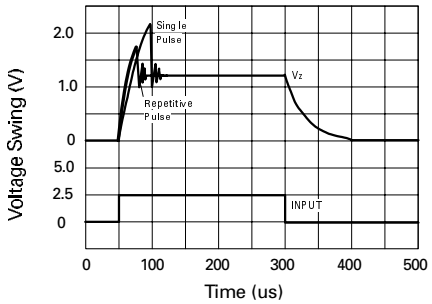


Gain v Frequency

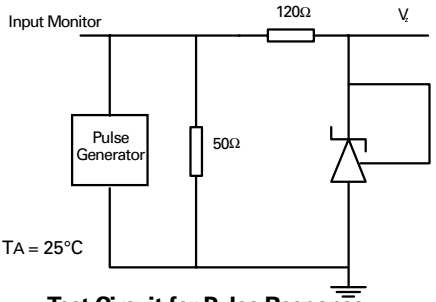


IZ = 10mA, TA = 25°C

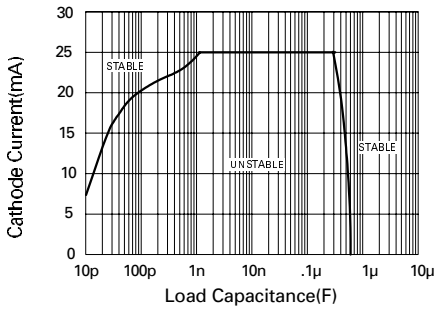
Test Circuit for Open Loop Voltage Gain



Pulse Response

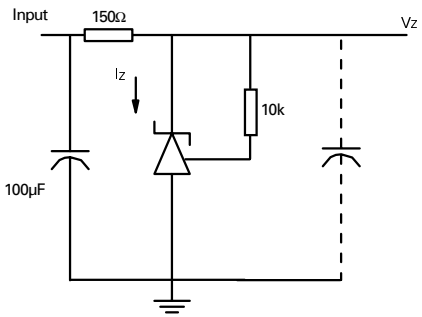


Test Circuit for Pulse Response



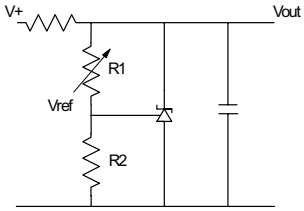
Stablity Boundary Conditions

Vref < VZ < 20, IZ = 10mA, TA = 25°C



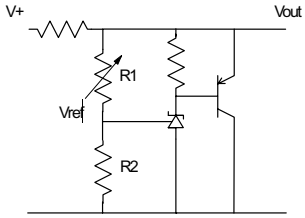
Test Circuit for Stability Boundary Conditions

APPLICATION CIRCUITS



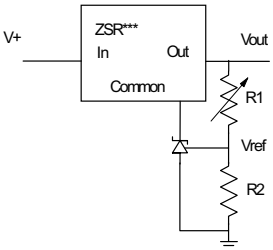
$$V_{out} = \left(1 + \frac{R1}{R2}\right) V_{ref}$$

SHUNT REGULATOR



$$V_{out} = \left(1 + \frac{R1}{R2}\right) V_{ref}$$

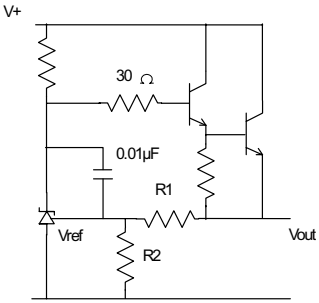
HIGHER CURRENT SHUNT REGULATOR



$$V_{out_{MIN}} = V_{ref} + V_{reg}$$

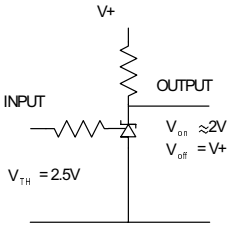
$$V_{out} = \left(1 + \frac{R1}{R2}\right) V_{ref}$$

OUTPUT CONTROL OF A
THREE TERMINAL FIXED REGULATOR

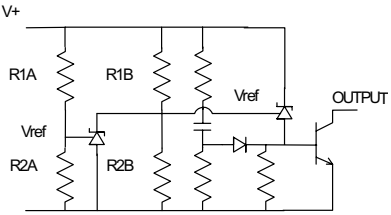


$$V_{out} = \left(1 + \frac{R1}{R2}\right) V_{ref}$$

SERIES REGULATOR



SINGLE SUPPLY COMPARATOR
WITH TEMPERATURE
COMPENSATED THRESHOLD



$$\text{Low limit} = \left(1 + \frac{R1B}{R2B}\right) V_{ref}$$

$$\text{High limit} = \left(1 + \frac{R1A}{R2A}\right) V_{ref}$$

OVER VOLTAGE / UNDER VOLTAGE

DC Test Circuits

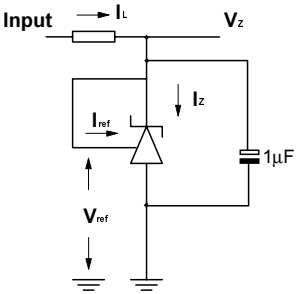


Fig 1 – Test Circuit for V_z=V_{ref}

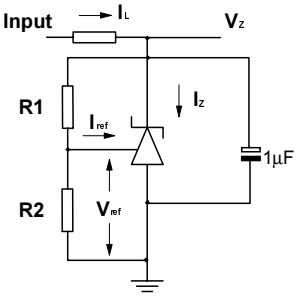


Fig 2 – Test Circuit for V_z>V_{ref}

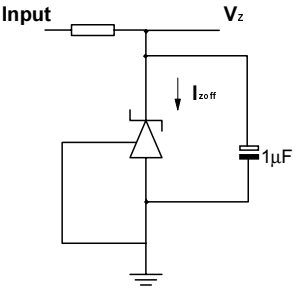
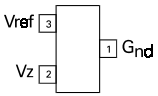


Fig 3 – Test Circuit for Off State current

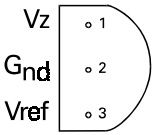
Voltage Regulator Connection Diagrams

SOT23 Package Suffix – F



Top View

TO92 Package Suffix – C



Bottom View

Ordering Information

Part Number	Package	Part Mark
ZHT2431F02	SOT23	24F
ZHT2431C02	TO92	ZHT243102