

SOT223 N-CHANNEL ENHANCEMENT MODE VERTICAL DMOS FET

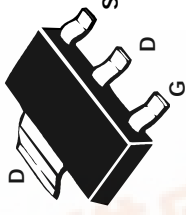
ZVN2120G

ISSUE 2 - FEBRUARY 1996

FEATURES:

- * $V_{DS} = 200V$
- * $R_{DS(ON)} = 10\Omega$

PARTMARKING DETAIL - ZVN2120



查询ZVN2120G供应商

捷多邦, 专业PCB打样工厂, 24小时加急出货

ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	VALUE	UNIT
Drain-Source Voltage	V_{DS}	200	V
Continuous Drain Current at $T_{amb}=25^{\circ}C$	I_D	320	mA
Pulsed Drain Current	I_{DM}	2	A
Gate-Source Voltage	V_{GS}	± 20	V
Power Dissipation at $T_{amb}=25^{\circ}C$	P_{tot}	2	W
Operating and Storage Temperature Range	T_j, T_{stg}	-55 to +150	$^{\circ}C$

ELECTRICAL CHARACTERISTICS (at $T_{amb} = 25^{\circ}C$ unless otherwise stated).

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	CONDITIONS.
Drain-Source Breakdown Voltage	BV_{DSS}	200		V	$I_D = 1mA, V_{GS} = 0V$
Gate-Source Threshold Voltage	$V_{GS(th)}$	1	3	V	$I_D = 1mA, V_{DS} = V_{GS}$
Gate-Body Leakage	I_{GSS}		20	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
Zero Gate Voltage Drain Current	I_{DSS}		10 100	μA μA	$V_{DS} = 200V, V_{GS} = 0V$ $V_{DS} = 160V, V_{GS} = 0V,$ $T = 125^{\circ}C(2)$
On-State Drain Current(1)	$I_{D(on)}$	500		mA	$V_{DS} = 25V, V_{GS} = 10V$
Static Drain-Source On-State Resistance (1)	$R_{DS(on)}$		10	Ω	$V_{GS} = 10V, I_D = 250mA$
Forward Transconductance(1)(2)	g_{fs}	100		mS	$V_{DS} = 25V, I_D = 250mA$
Input Capacitance (2)	C_{iss}		85	pF	
Common Source Output Capacitance (2)	C_{oss}		20	pF	$V_{DS} = 25V, V_{GS} = 0V, f = 1MHz$
Reverse Transfer Capacitance (2)	C_{rss}		7	pF	
Turn-On Delay Time (2)(3)	$t_{d(on)}$		8	ns	
Rise Time (2)(3)	t_r		8	ns	
Turn-Off Delay Time (2)(3)	$t_{d(off)}$		20	ns	
Fall Time (2)(3)	t_f		12	ns	$V_{DD} = 25V, I_D = 250mA$

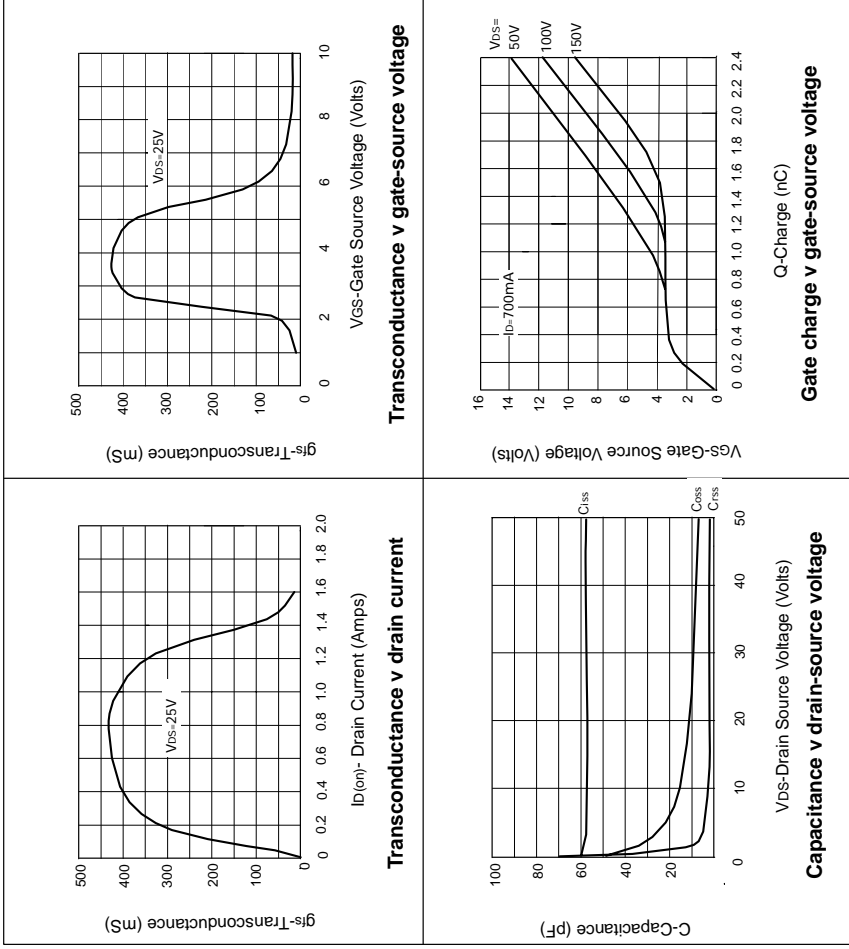
- (1) Measured under pulsed conditions. Width=300 μs . Duty cycle $\leq 2\%$ (2) Sample test.
 (3) Switching times measured with 50 Ω source impedance and <5ns rise time on a pulse generator



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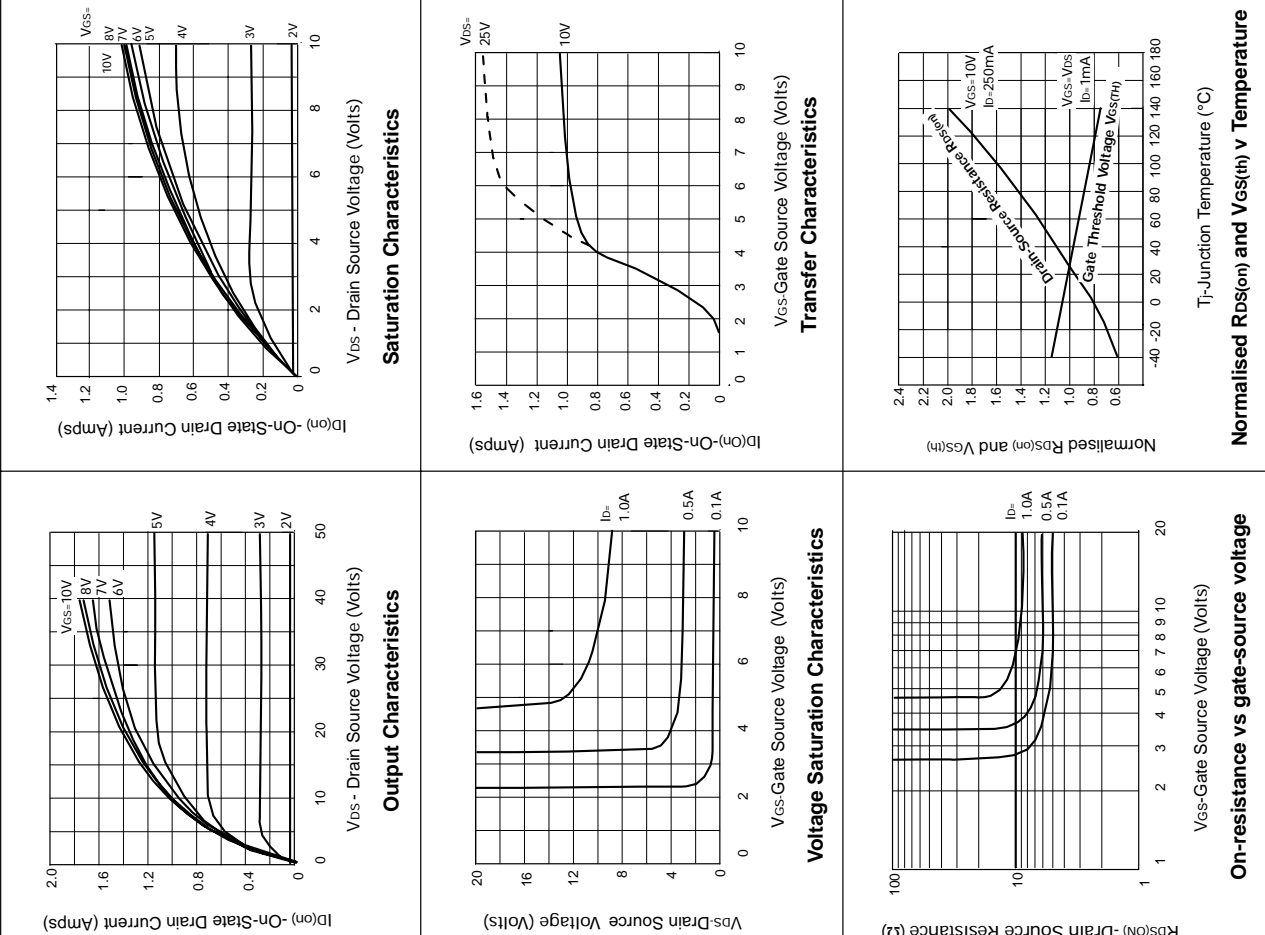
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TYPICAL CHARACTERISTICS



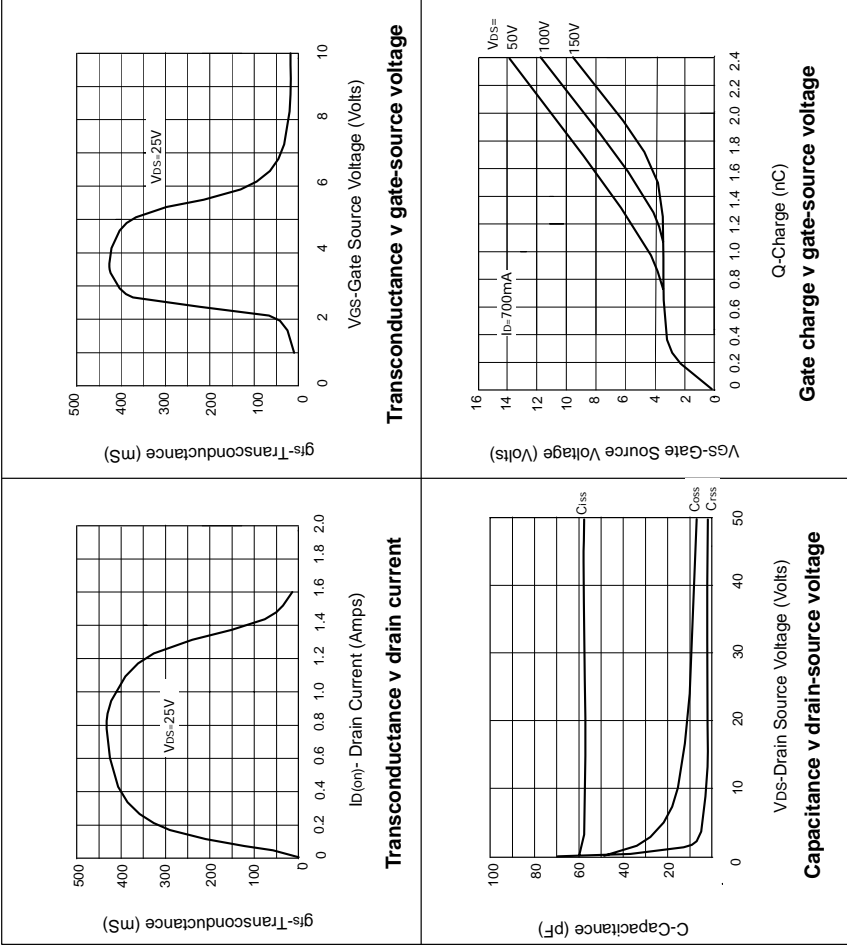
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