



SDU/D20N03L

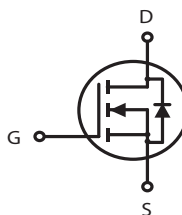
N-Channel Logic Level Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY

VDSS	ID	RDS(ON) (m Ω) Max
30V	35A	15 @ VGS = 10V
		32 @ VGS = 4.5V

FEATURES

- Super high dense cell design for low Rds(ON).
- Rugged and reliable.
- TO-252 and TO-251 Package.



ABSOLUTE MAXIMUM RATINGS (Tc=25 C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	30	V
Gate-Source Voltage	VGS	±20	V
Drain Current-Continuous @TJ=125 C ° -Pulsed ^a	ID	35	A
	IDM	87	A
Drain-Source Diode Forward Current	IS	20	A
Maximum Power Dissipation @Tc=25 C°	PD	50	W
Operating and Storage Temperature Range	TJ, TSTG	-55 to 175	°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Case	R θ C	3	°C/W
Thermal Resistance, Junction-to-Ambient	R θ A	50	°C/W

S DU/D20N03L

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ELECTRICAL CHARACTERISTICS (Tc=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = 250uA	30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24V, V _{GS} = 0V			10	uA
Gate-Body Leakage	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
ON CHARACTERISTICS ^a						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250uA	1	1.5	3	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D = 10A		13.5	15	m ohm
		V _{GS} = 4.5V, I _D = 8A		23	32	m ohm
On-State Drain Current	I _{D(ON)}	V _{DS} = 10V, V _{GS} = 10V	55			A
Forward Transconductance	g _{FS}	V _{DS} = 10V, I _D = 20A		25		S
DYNAMIC CHARACTERISTICS ^b						
Input Capacitance	C _{ISS}	V _{DD} = 15V, V _{GS} = 0V f = 1.0MHz		930		pF
Output Capacitance	C _{OSS}			400		pF
Reverse Transfer Capacitance	C _{RSS}			120		pF
SWITCHING CHARACTERISTICS ^b						
Turn-On Delay Time	t _{D(ON)}	V _{DD} = 15V I _D = 1A V _{GS} = 10V V _{GEN} = 6 ohm		30		ns
Rise Time	t _r			20		ns
Turn-Off Delay Time	t _{D(OFF)}			34		ns
Fall time	t _f			10		ns
Total Gate Charge	Q _g	V _{DS} = 15V, I _D = 20A, V _{GS} = 10V		26.5		nC
		V _{DS} = 15V, I _D = 20A, V _{GS} = 4.5V		13.5		nC
Gate-Source Charge	Q _{gs}	V _{DS} = 15V, I _D = 20A V _{GS} = 10V		4.8		nC
Gate-Drain Charge	Q _{gd}			5.4		nC

S DU/D20N03L

ELECTRICAL CHARACTERISTICS (T_c=25℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS ^a						
Diode Forward Voltage	V _{SD}	V _{GS} = 0V, I _s = 20A			1.3	V

Notes

- a.Pulse Test:Pulse Width ≤ 300us, Duty Cycle ≤ 2%.
- b.Guaranteed by design, not subject to production testing.

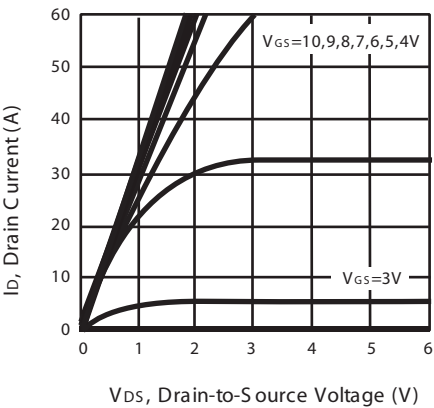


Figure 1. Output Characteristics

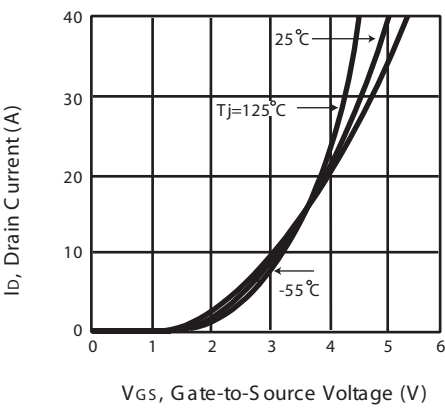


Figure 2. Transfer Characteristics

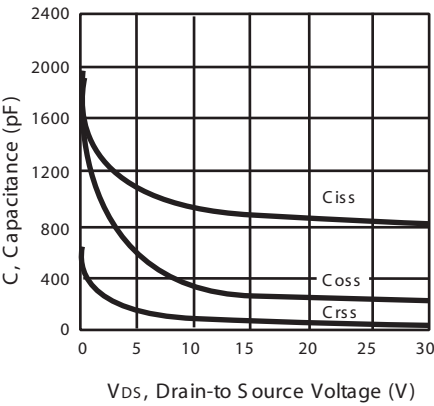


Figure 3. Capacitance

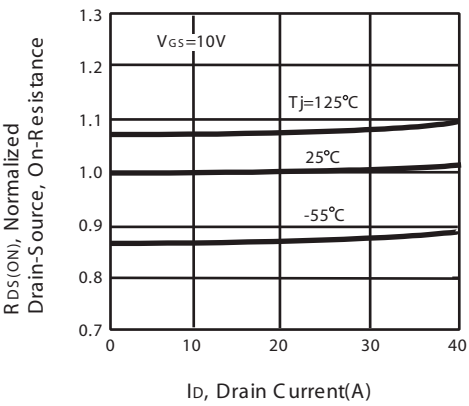


Figure 4. On-Resistance Variation with Drain Current and Temperature

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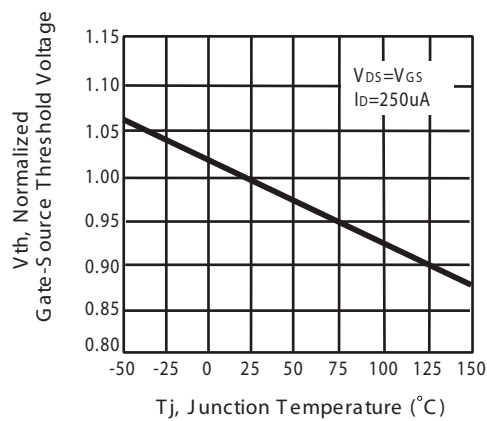


Figure 5. Gate Threshold Variation with Temperature

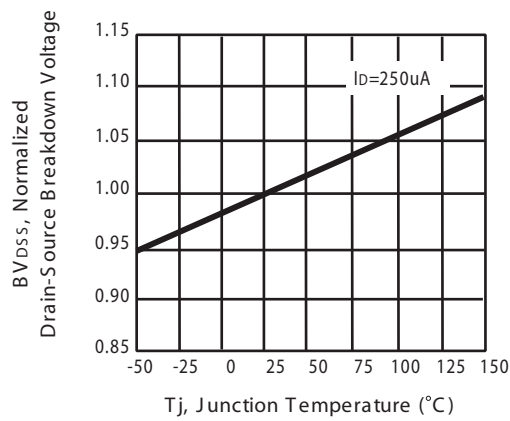


Figure 6. Breakdown Voltage Variation with Temperature

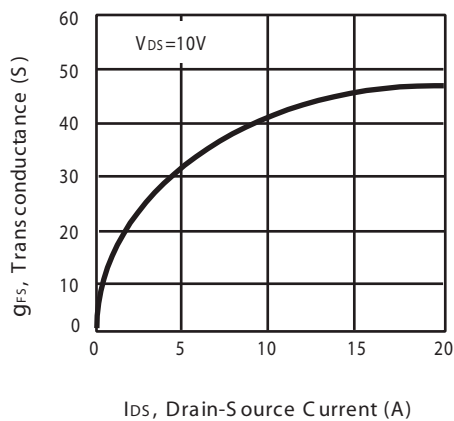


Figure 7. Transconductance Variation with Drain Current

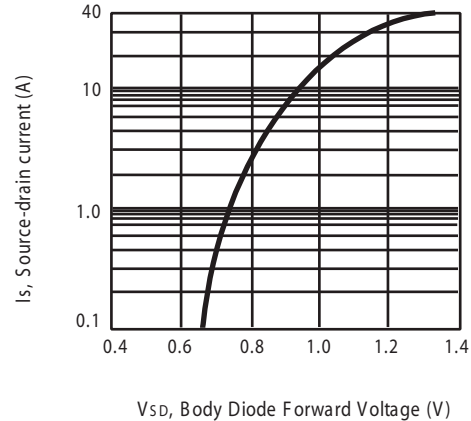


Figure 8. Body Diode Forward Voltage Variation with Source Current

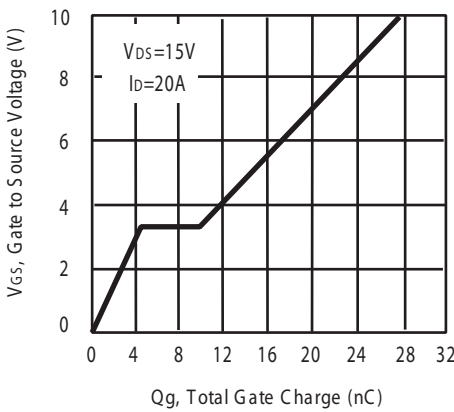


Figure 9. Gate Charge

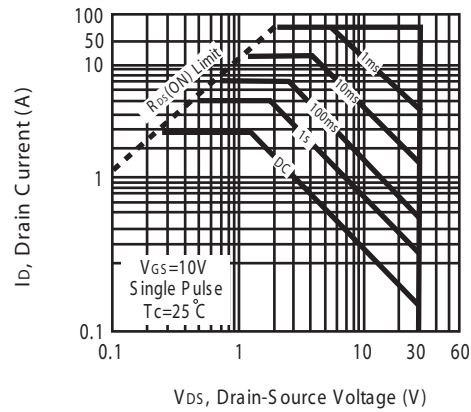


Figure 10. Maximum Safe Operating Area

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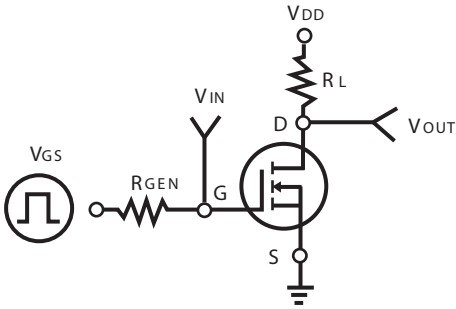


Figure 11. S switching Test Circuit

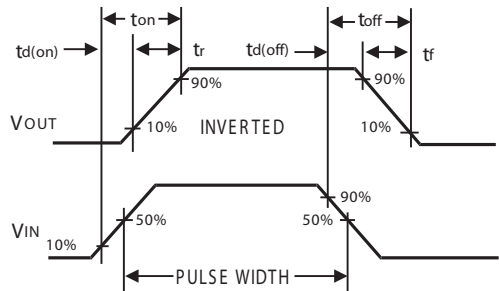


Figure 12. S switching Waveforms

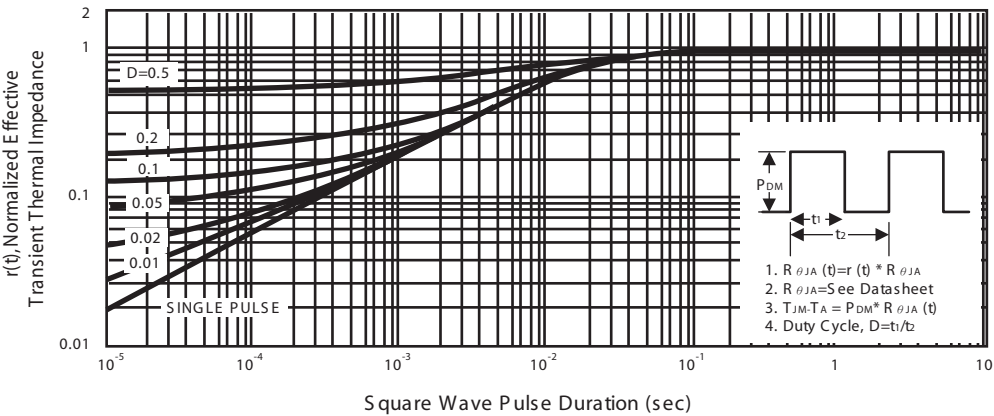


Figure 13. Normalized Thermal Transient Impedance Curve

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