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	SHEET	1	2	3	4	5	6	7	8	9	10		

PMIC N/A	PREPARED BY <i>Larry T. Hauder</i>	DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444	
STANDARDIZED MILITARY DRAWING THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE	CHECKED BY <i>Ray Monnin</i>	MICROCIRCUIT, DIGITAL, BIPOLAR, ADVANCED LOW POWER SCHOTTKY, TTL, 8-BIT ADDRESSABLE LATCHES, MONOLITHIC SILICON	
	APPROVED BY <i>[Signature]</i>		
	DRAWING APPROVAL DATE 30 NOVEMBER 1988	SIZE A	CAGE CODE 67268
AMSC N/A	REVISION LEVEL	SHEET 1 OF 10	

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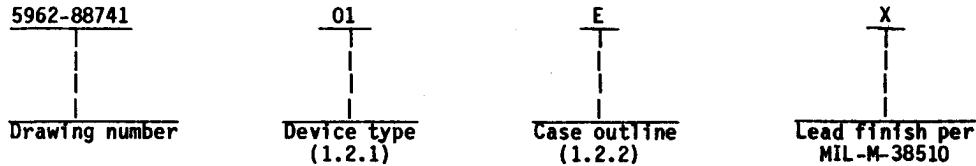
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5962-1082-1

1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:



1.2.1 Device type. The device type shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	54ALS259	8-Bit addressable latches

1.2.2 Case outline. The case outline shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
E	D-2 (16-Lead, .840" x .310" x .200"), dual-in-line package
F	F-5 (16-Lead, .440" x .285" x .085"), flat package
2	C-2 (20-terminal, .358" x .358" x .100"), square chip carrier package

1.3 Absolute maximum ratings.

Supply voltage range	-0.5 V dc minimum to +7.0 V dc maximum
Input voltage range	-1.5 V dc at -18 mA to +7.0 V dc
Storage temperature range	-65°C to +150°C
Lead temperature (soldering, 10 seconds)	+300°C
Junction temperature (T _J)	+175°C
Maximum power dissipation (P _D) ^{1/}	121 mW
Thermal resistance, junction to case (θ _{JC})	See MIL-M-38510, appendix C

1.4 Recommended operating conditions.

Supply voltage range (V _{CC})	+4.5 V dc minimum to +5.5 V dc maximum
Minimum high level input voltage (V _{IH})	2.0 V dc
Maximum low level input voltage (V _{IL})	0.7 V dc
Maximum high level output current (I _{OH})	-0.4 mA
Maximum low level output current (I _{OL})	4 mA
Pulse duration (t _w):	
G low	20 ns
CLR low	10 ns
Setup time (t _{su}):	
Data before G ⁺	20 ns
Address before G ⁺	20 ns

^{1/} Maximum power dissipation is defined as V_{CC} x I_{CC}, and must withstand the added P_D due to short circuit test; e.g., I_O.

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Hold time (t_h):
 Data after G^+ - - - - - 0 ns
 Address after G^+ - - - - - 0 ns
 Case operating temperature range (T_C) - - - - - -55°C to $+125^\circ\text{C}$

2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth table. The truth table shall be as specified on figure 2.

3.2.3 Test circuit and switching waveforms. The test circuit and switching waveforms shall be as specified on figure 3.

3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full case operating temperature range.

3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions -55°C < T _C < +125°C unless otherwise specified	1/ Group A subgroups	Limits		Unit
				Min	Max	
High level output voltage	V _{OH}	V _{CC} = 4.5 V, V _{IH} = 2.0 V I _{OH} = -0.4 mA V _{IL} = 0.7 V <u>2/</u>	1, 2, 3	2.5		V
Low level output voltage	V _{OL}	V _{CC} = 4.5 V, V _{IH} = 2.0 V I _{OL} = 4 mA, V _{IL} = 0.7 V <u>2/</u>	1, 2, 3		0.4	V
Input clamp voltage	V _{IC}	V _{CC} = 4.5 V I _{IN} = -18 mA	1, 2, 3		-1.5	V
Low level input current	I _{IL}	V _{CC} = 5.5 V V _{IN} = 0.4 V All unused inputs ≥ 4.5 V	1, 2, 3		-0.1	mA
High level input current	I _{IH1}	V _{CC} = 5.5 V V _{IN} = 2.7 V All unused inputs = 0.0 V	1, 2, 3		20	μA
	I _{IH2}	V _{CC} = 5.5 V V _{IN} = 7.0 V All unused inputs = 0.0 V	1, 2, 3		0.1	mA
Output current	I _O	V _{CC} = 5.5 V <u>3/</u> V _{OUT} = 2.25 V	1, 2, 3	-30	-112	mA
Supply current	I _{CC}	V _{CC} = 5.5 V	1, 2, 3		22	mA
Functional tests		See 4.3.1c <u>4/</u>	7, 8			

See footnotes at end of table.

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TABLE 1. Electrical performance characteristics - Continued.

Test	Symbol	Conditions -55°C < T _C < +125°C unless otherwise specified	1/ Group A subgroups	Limits		Unit	
				Min	Max		
Propagation delay time, from clear to any Q	t _{PHL1}	V _{CC} = 4.5 to 5.5 V C _L = 50 pF R _L = 500Ω 5/ See figure 3	9,10,11	2	15	ns	
Propagation delay time, from data to any Q	t _{PLH2}			9,10,11	4		22
	t _{PHL2}				2	15	
Propagation delay time, from address to any Q	t _{PLH3}		9,10,11	9,10,11	4	26	ns
	t _{PHL3}					2	
Propagation delay time, from enable to any Q	t _{PLH4}		9,10,11	9,10,11	4	22	ns
	t _{PHL4}				2	16	

- 1/ Unused inputs that do not directly control the pin under test must be > 2.5 V or < 0.4 V. No unused inputs shall exceed 5.5 V or go less than 0.0 V. No inputs shall be floated.
- 2/ All outputs must be tested. In the case where only one input at V_{IL} maximum or V_{IH} minimum produces the proper output state, the test must be performed with each input being selected as the V_{IL} maximum or V_{IH} minimum input.
- 3/ The output conditions have been chosen to produce a current that closely approximates one half of the true short circuit output current, I_{OS}. Not more than one output will be tested at one time and the duration of the test shall not exceed 1 second.
- 4/ Functional tests shall be conducted at input test conditions of GND ≤ V_{IL} ≤ V_{OL} and V_{OH} ≤ V_{IH} ≤ V_{CC}.
- 5/ Propagation delay limits are based on single output switching. Unused inputs = 3.5 V or < 0.3 V.

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Case outlines	E and F	2
Terminal number	Terminal symbol	
1	S0	NC
2	S1	S0
3	S2	S1
4	Q0	S2
5	Q1	Q0
6	Q2	NC
7	Q3	Q1
8	GND	Q2
9	Q4	Q3
10	Q5	GND
11	Q6	NC
12	Q7	Q4
13	D	Q5
14	\bar{G}	Q6
15	CLR	Q7
16	VCC	NC
17	---	D
18	---	\bar{G}
19	---	CLR
20	---	VCC

NC = No connection

FIGURE 1. Terminal connections.

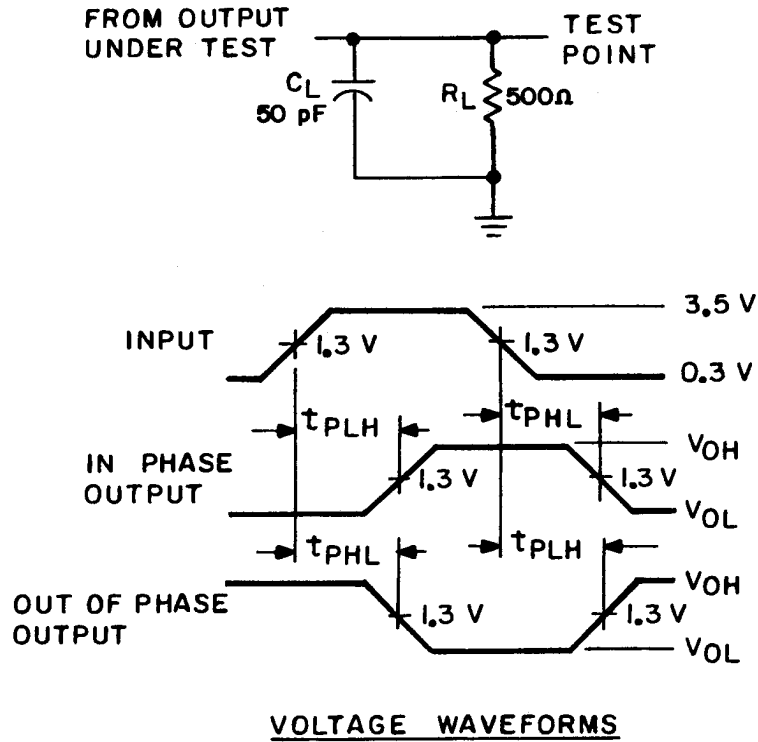
Select inputs			Latch addressed
S2	S1	S0	
L	L	L	0
L	L	H	1
L	H	L	2
L	H	H	3
H	L	L	4
H	L	H	5
H	H	L	6
H	H	H	7

Inputs		Output of addressed latch	Each Other Output	Function
CLR	\bar{G}			
H	L	D	Q _{i0}	Addressable Latch Memory
H	H	Q _{i0}	Q _{i0}	
L	L	D	L	8-bit Demultiplexer Clear
L	H	L	L	

D = the level at the data input
 Q_{i0} = the level of Q_i (i = 0, 1 ... 7, as appropriate) before the indicated steady-state input conditions were established.
 H = high level voltage
 L = low level voltage

FIGURE 2. Truth table

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NOTES:

1. C_L includes probe and jig capacitance.
2. All input pulses have the following characteristics: $PRR \leq 10$ MHz, duty cycle = 50%, $t_r = t_f = 3$ ns ± 1 ns.
3. The outputs are measured one at a time with one input transition per measurement.

FIGURE 3. Test circuit and switching waveforms.

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3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

- a. Burn-in test, method 1015 of MIL-STD-883.
 - (1) Test condition A or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^\circ\text{C}$, minimum.
- b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

- a. Tests shall be as specified in table II herein.
- b. Subgroups 4, 5, and 6 in table I, method 5005 of MIL-STD-883 shall be omitted.
- c. Subgroups 7 and 8 shall verify the truth table as specified on figure 2.

4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test conditions, method 1005 of MIL-STD-883.
 - (1) Test condition A or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^\circ\text{C}$, minimum.
 - (3) Test duration: 1,000 hours except as permitted by method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*,2,3,7,8,9, 10,11
Group A test requirements (method 5005)	1,2,3,7,8,9, 10,11
Group C and D end-point electrical parameters (method 5005)	1,2,3

* PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

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6.4 Approved source of supply. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed herein has agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor CAGE number	Vendor similar part number 1/
5962-8874101EX	01295	SNJ54ALS259J
5962-8874101FX	01295	SNJ54ALS259W
5962-88741012X	01295	SNJ54ALS259FK

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

01295

Vendor name and address

Texas Instruments, Incorporated
P.O. Box 6448
Midland, TX 79711

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