

PROCESS CP592

Small Signal Transistor

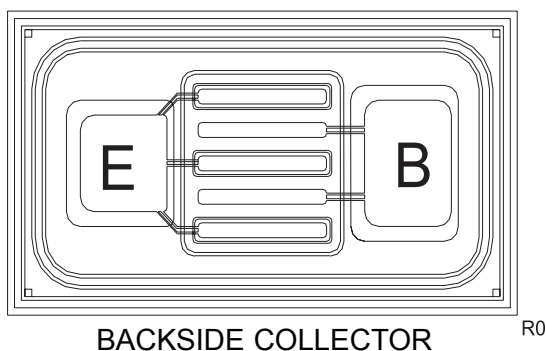
[查询"CP592"供应商](#) PNP Amplifier Switch Transistor Chip

CentralTM
Semiconductor Corp.

PROCESS DETAILS

Process	EPITAXIAL PLANAR
Die Size	12 x 20 MILS
Die Thickness	9.0 MILS
Base Bonding Pad Area	3.6 X 3.6 MILS
Emitter Bonding Pad Area	3.6 X 3.6 MILS
Top Side Metalization	Al - 30,000Å
Back Side Metalization	Au - 18,000Å

GEOMETRY



GROSS DIE PER 4 INCH WAFER

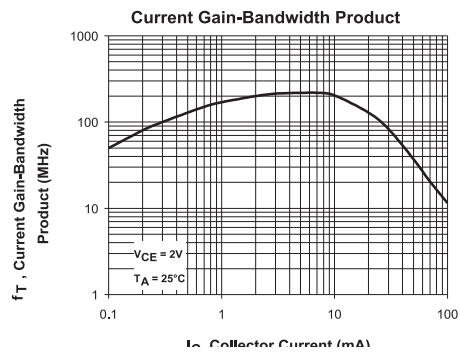
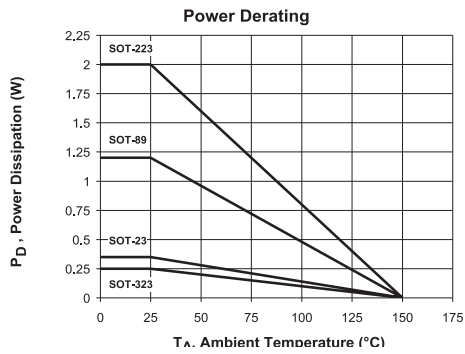
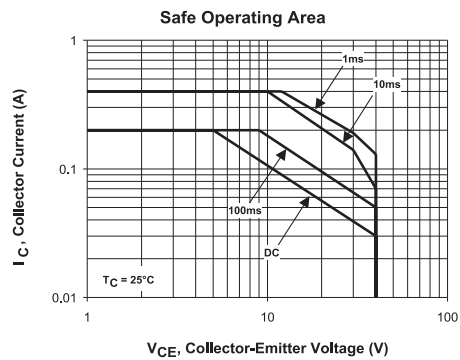
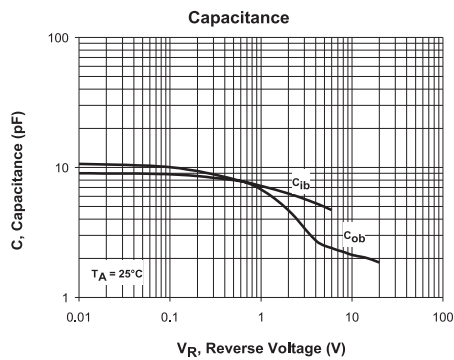
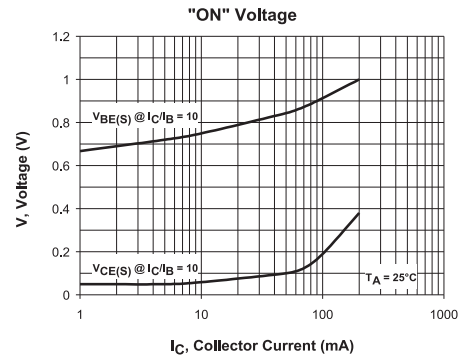
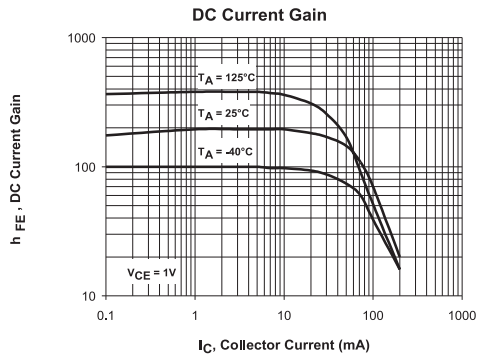
47,150

PRINCIPAL DEVICE TYPES

2N3906
CMPT3906
CMST3906
CXT3906
CZT3906

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R2 (1-August 2002)



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