VLRK31..



Vishay Semiconductors

Reverse Gullwing SMD LED Red



DESCRIPTION

This device has been designed to meet the increasing demand for AllnGaP technology.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

LED is mounted top down and emits through the PCB.

PRODUCT GROUP AND PACKAGE DATA

- Product group: LED
- Package: SMD reverse gullwing
- Product series: standard
- WWW.DZSC.COM Angle of half intensity: ± 60°

FEATURES

- SMD LED with exceptional brightness
- Luminous intensity categorized
- Compatible with automatic placement equipment
- EIA and ICE standard package
- Compatible with IR reflow, vapor phase and wave solder processes according to CECC 00802 and J-STD-020C
- Available in 12 mm tape
- Low profile package
- Non-diffused lens: excellent for coupling to light ٠ pipes and backlighting
- Low power consumption
- Luminous intensity ratio in one packaging unit $I_{Vmax}/I_{Vmin} > 1.6$
- Lead (Pb)-free device
- . Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC
- Preconditioning acc. to JEDEC level 2a
- · ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- Qualified according Vishay automotive requirement

APPLICATIONS

- Automotive: backlighting in dashboards and switches
- Telecommunication: indicator and backlighting in telephone and fax
- Indicator and backlight for audio and video equipment
- Indicator and backlight in office equipment
- Flat backlight for LCDs, switches and symbols
- General use

COLOR, LUMINOUS INTENSITY	TECHNOLOGY
Red, I _V = (112 to 285) mcd	AllnGaP on GaAs
Red, I _V = (112 to 285) mcd	AllnGaP on GaAs
Red, I _V = (71 to 180) mcd	AllnGaP on GaAs
Red, I _V = (71 to 180) mcd	AllnGaP on GaAs
Red, I _V = (112 to 180) mcd	AllnGaP on GaAs
Red, I _V = (112 to 180) mcd	AllnGaP on GaAs
Red, I _V = (90 to 140) mcd	AllnGaP on GaAs
Red, I _V = (90 to 140) mcd	AllnGaP on GaAs
	Red, $I_V = (112 \text{ to } 285) \text{ mcd}$ Red, $I_V = (112 \text{ to } 285) \text{ mcd}$ Red, $I_V = (112 \text{ to } 285) \text{ mcd}$ Red, $I_V = (71 \text{ to } 180) \text{ mcd}$ Red, $I_V = (71 \text{ to } 180) \text{ mcd}$ Red, $I_V = (112 \text{ to } 180) \text{ mcd}$ Red, $I_V = (112 \text{ to } 180) \text{ mcd}$ Red, $I_V = (112 \text{ to } 180) \text{ mcd}$ Red, $I_V = (90 \text{ to } 140) \text{ mcd}$





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ABSOLUTE MAXIMUM RATINGS ¹⁾ VLRK31				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage 2)		V _R	5	V
DC Forward current	$T_{amb} \le 85 \ ^{\circ}C$	١ _F	30	mA
Surge forward current	$t_p \le 10 \ \mu s$	I _{FSM}	1	А
Power dissipation		P _V	75	mW
Junction temperature		Тj	125	°C
Operating temperature range		T _{amb}	- 40 to + 100	°C
Storage temperature range		T _{stg}	- 40 to + 100	°C
Thermal resistance junction/ ambient	mounted on PC board (pad size > 16 mm ²)	R _{thJA}	400	K/W

Note:

¹⁾ $T_{amb} = 25 \text{ °C}$, unless otherwise specified

²⁾ Driving LED in reverse direction is suitable for short term application

OPTICAL AND ELECTRICAL CHARACTERISTICS ¹⁾ VLRK31, RED							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
		VLRK31R1S2	Ι _V	112		285	mcd
	I _F = 20 mA	- 20 m A VLRK31Q1R2	Ι _V	71		180	mcd
Luminous intensity ²⁾	sity $r_{\rm F} = 20$ mA	VLRK31R1R2	Ι _V	112		180	mcd
		VLRK31Q2R1	Ι _V	90		140	mcd
Dominant wavelength 4)	I _F = 20 mA		λ_d	620	630	635	nm
Peak wavelength	I _F = 20 mA		λ _p		643		nm
Angle of half intensity	I _F = 20 mA		φ		± 60		deg
Forward voltage 3)	I _F = 20 mA		V _F		2.1	2.3	V
Reverse voltage	I _R = 10 μA		V _R	5			V
Junction capacitance	V _R = 0, f = 1 MHz		Cj		15		pF

Note:

¹⁾ $T_{amb} = 25 \,^{\circ}C$, unless otherwise specified

²⁾ In one packing unit $I_{Vmax}/I_{Vmin} > 1.6$ ³⁾ Forward voltages are tested at a current pulse duration of 1 ms and a tolerance of ± 0.05 V

 $^{4)}$ Wavelengths are tested at a current pulse duration of 25 ms and an accuracy of ± 1 nm

LUMINOUS INTENSITY CLASSIFICATION			
GROUP	LIGHT INTENSITY (MCD)		
STANDARD	OPTIONAL	MIN.	MAX.
В	1	112	140
n	2	140	180
S	1	180	224
5	2	224	285

Note:

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 11 %.

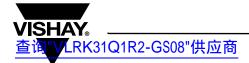
The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).

In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.

In order to ensure availability, single wavelength groups will not be orderable.

CROSSING TABLE		
VISHAY	OSRAM	
VLRK31R1S2	LST776-R1S2	
VLRK31Q1R2	LST776-Q1R2	
VLRK31R1R2	LST776-R1R2	
VLRK31Q2R1	LST776-Q2R1	
VLRK3TQ2RT	LS1776-Q2R1	



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TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

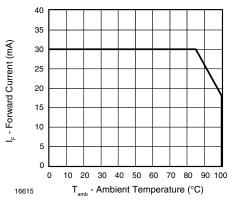


Figure 1. Forward Current vs. Ambient Temperature

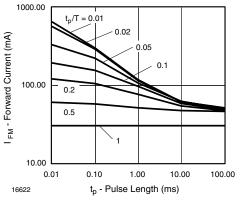


Figure 2. Forward Current vs. Pulse Length

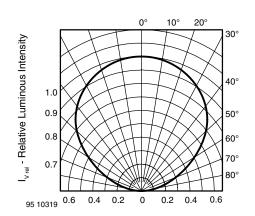
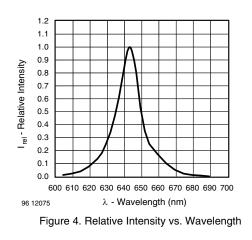


Figure 3. Rel. Luminous Intensity vs. Angular Displacement



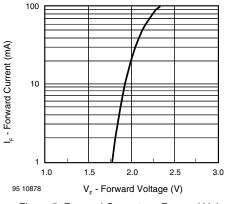


Figure 5. Forward Current vs. Forward Voltage

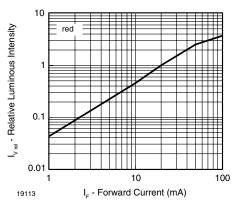


Figure 6. Relative Luminous Intensity vs. Forward Current

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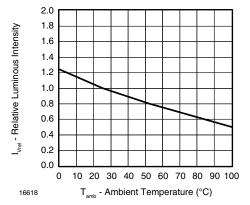


Figure 7. Rel. Luminous Intensity vs. Ambient Temperature

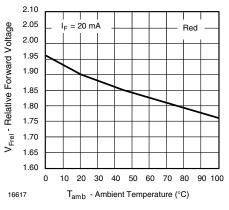
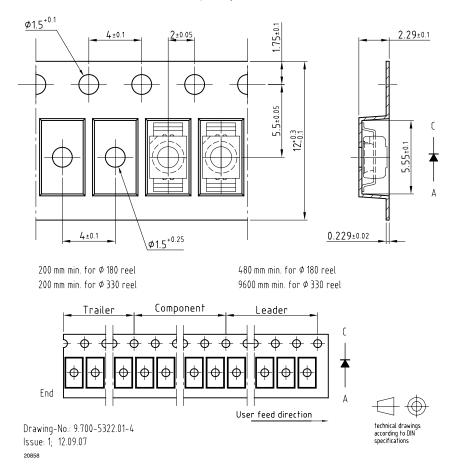


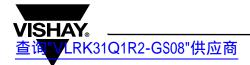
Figure 8. Forward Voltage vs. Ambient Temperature

TAPING DIMENSIONS in millimeters

Taping and orientation

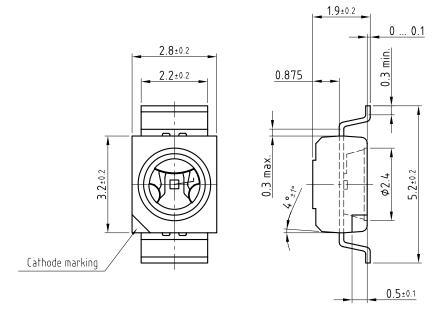
Reels come in quantity of 8000 units or 2000 units. Reel diameters are 330 mm and 180 mm respectively.



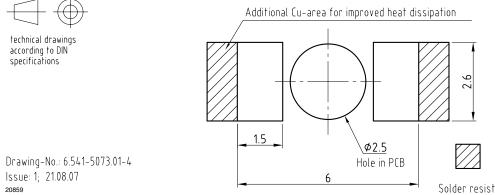


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PACKAGE DIMENSIONS in millimeters



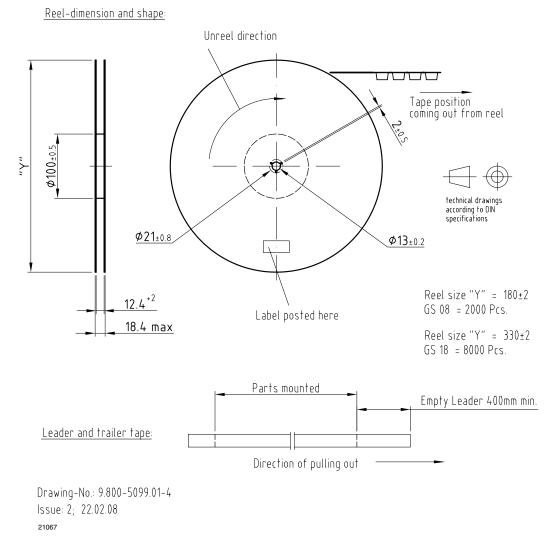
Recommended solder pad



Vianaly/Selmiconductors



REEL DIMENSIONS in millimeters



SOLDERING PROFILE

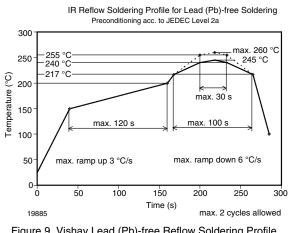


Figure 9. Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020C)

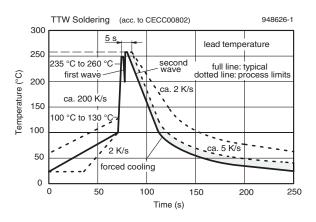
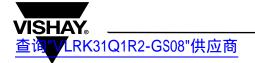
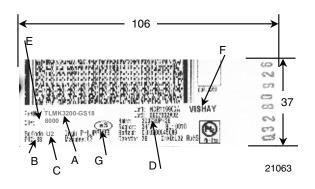


Figure 10. Double Wave Soldering of Opto Devices (all Packages)



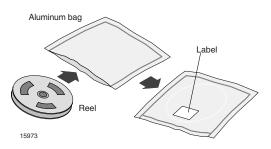
LABEL OF FAN FOLD BOX EXAMPLE:



- A) Type of component
- B) PTC = manufacturing plant
- C) SEL selection code (bin):e.g.: U2 = code for luminous intensity group
- D) Batch/date code
- E) Total quantity
- F) Company code
- G) Code for lead (Pb)-free classification (e3)

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

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RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity < 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.

CAUTH This bag co MOISTURE -SENST	ntains
1. Shelf life in sealed bag 12 months at <40	0°C and < 90% relative humidity (RH)
 After this bag is opened devices that will vapor-phase reflow, or equivalent proce 260°C) must be: Mounted within 672 hours at fa b) Stored at ≤10% RH. 	ssing (peak package body temp.
 3. Devices require baking before mounting a) Humidity Indicator Card is >10% b) 2a or 2b is not met. 	
 If baking is required, devices may be bal 192 hours at 40°C + 5°C/-0°C and - 96 hours at 60±5°Cand <5%RH 24 hours at 100±5°C 	
Bag Seal Date:(If blank, see bar co	de label)
Note: LEVEL defined by EIA JE	EDEC Standard JESD22-A113

Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

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OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

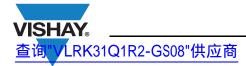
- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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Vishay

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