



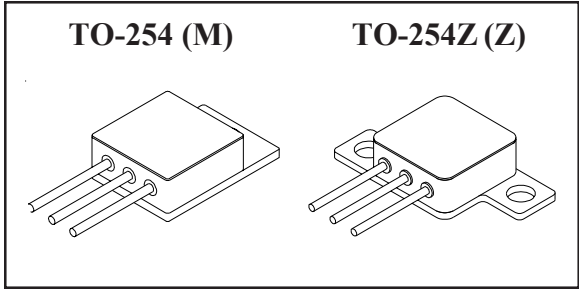
查询"SF75N10M"供应商  
**SOLID STATE DEVICES, INC.**  
 14701 Firestone Blvd \* La Mirada, Ca 90638  
 Phone: (562) 404-7855 \* Fax: (562) 404-1773

**DESIGNER'S DATA SHEET**

- FEATURES:**
- Rugged construction with poly silicon gate
  - low RDS (on) and high transconductance
  - Excellent high temperature stability
  - Very fast switching speed
  - Fast recovery and superior dv/dt performance
  - Increased reverse energy capability
  - Low input and transfer capacitance for easy paralleling
  - Ceramic seals for improved hermeticity
  - Hermetically sealed package
  - TX, TXV and Space Level screening available
  - Replaces: IXTH75N10 Types

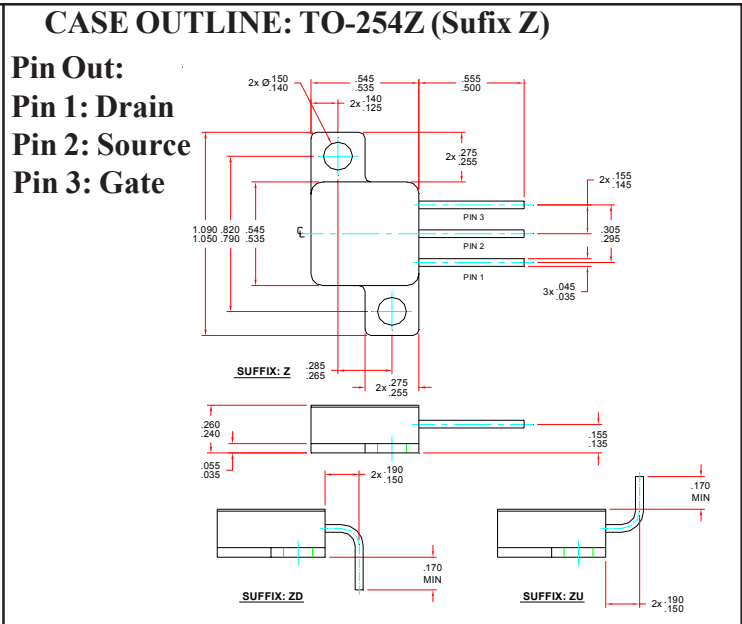
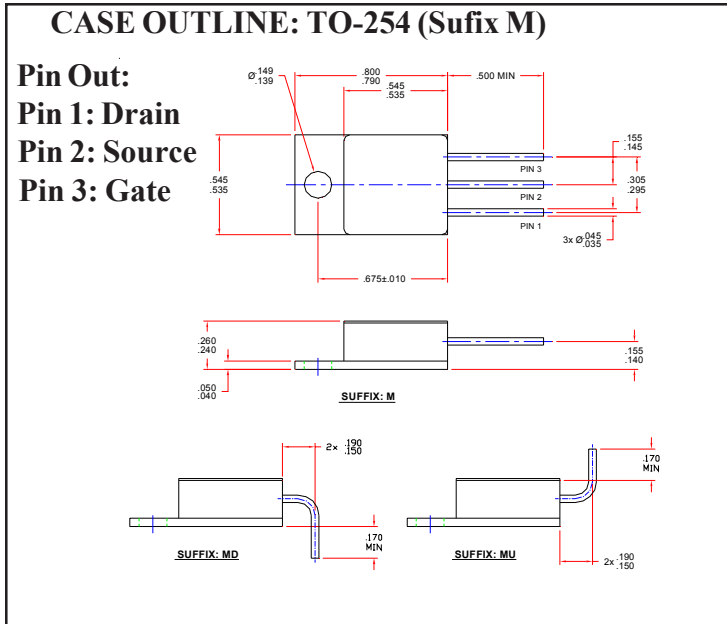
**SFF75N10M**  
**SFF75N10Z**

**75 AMP**  
**100 VOLTS**  
**0.025 Ω**  
**N-CHANNEL**  
**POWER MOSFET**



**MAXIMUM RATINGS**

CHARACTERISTIC	SYMBOL	VALUE	UNIT
Drain to Source Voltage	V <sub>DS</sub>	100	Volts
Gate to Source Voltage	V <sub>GS</sub>	± 20	Volts
Continuous Drain Current	I <sub>D</sub>	56 <sup>L</sup>	Amps
Operating and Storage Temperature	T <sub>op</sub> & T <sub>stg</sub>	-55 to +150	°C
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	0.83	°C/W
Total Device Dissipation	P <sub>D</sub>	150 114	Watts
		@ TC = 25°C @ TC = 55°C	
Repetitive Avalanche Energy	E <sub>AR</sub>	30	mJ



Available with Glass or Ceramic Seals. Contact Factory for details.

**NOTE:** All specifications are subject to change without notification. SCDs for these devices should be reviewed by SSDI prior to release.

**DATA SHEET #: F00153F**

**SFF75N10M**
[查询SFF75N10M代理商](#)
**SOLID STATE DEVICES, INC.**14701 Firestone Blvd \* La Mirada, Ca 90638  
Phone: (562) 404-7855 \* Fax: (562) 404-1773**ELECTRICAL CHARACTERISTICS @ T<sub>J</sub>=25°C (Unless Otherwise Specified)**

RATING		SYMBOL	MIN	TYP	MAX	UNIT
<b>Drain to Source Breakdown Voltage</b> (V <sub>GS</sub> =0V, I <sub>D</sub> =250μA)		<b>BV<sub>DSS</sub></b>	100	-	-	<b>V</b>
<b>Drain to Source on State Resistance</b> (V <sub>GS</sub> =10V)	I <sub>D</sub> = 37.5A I <sub>D</sub> = 75 A	<b>R<sub>DS(on)</sub></b>	- -	- -	0.025 0.030	<b>Ω</b>
<b>On State Drain Current</b> (V <sub>DS</sub> > I <sub>D(on)</sub> x R <sub>DS(on)</sub> Max, V <sub>GS</sub> = 10 V)		<b>I<sub>D(on)</sub></b>	75	-	-	<b>A</b>
<b>Gate Threshold Voltage</b> (V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 4mA)		<b>V<sub>GS(th)</sub></b>	2	-	4.0	<b>V</b>
<b>Forward Transconductance</b> (V <sub>DS</sub> > I <sub>D(on)</sub> X R <sub>DS</sub> (on) Max, I <sub>DS</sub> =50% rated I <sub>D</sub> )		<b>g<sub>fs</sub></b>	25	30	-	<b>Smho</b>
<b>Zero Gate Voltage Drain Current</b> (V <sub>DS</sub> = max rated voltage, V <sub>GS</sub> = 0 V) (V <sub>DS</sub> = 80% rated V <sub>DS</sub> , V <sub>GS</sub> = 0V, T <sub>A</sub> = 125°C)		<b>I<sub>DSS</sub></b>	- -	- -	250 1000	<b>μA</b>
<b>Gate to Source Leakage Forward</b> <b>Gate to Source Leakage Reverse</b>	At rated V <sub>GS</sub>	<b>I<sub>GSS</sub></b>	- -	- -	+200 -200	<b>nA</b>
<b>Total Gate Charge</b> <b>Gate to Source Charge</b> <b>Gate to Drain Charge</b>	V <sub>GS</sub> = 10 V 50% rated V <sub>DS</sub> 50% rated I <sub>D</sub>	<b>Q<sub>g</sub></b> <b>Q<sub>gs</sub></b> <b>Q<sub>gd</sub></b>	- - -	160 16 50	260 70 160	<b>nC</b>
<b>Turn on Delay Time</b> <b>Rise Time</b> <b>Turn off Delay Time</b> <b>Fall Time</b>	V <sub>DD</sub> =50% rated V <sub>DS</sub> 50% rated I <sub>D</sub> R <sub>G</sub> =6.2Ω V <sub>GS</sub> =10V	<b>t<sub>d</sub> (on)</b> <b>tr</b> <b>t<sub>d</sub> (off)</b> <b>tf</b>	- - - -	30 35 100 40	40 100 120 80	<b>nsec</b>
<b>Diode Forward Voltage</b> (I <sub>S</sub> = rated I <sub>D</sub> , V <sub>GS</sub> = 0V, T <sub>J</sub> = 25°C)		<b>V<sub>SD</sub></b>	-	1.3	1.75	<b>V</b>
<b>Diode Reverse Recovery Time</b> <b>Reverse Recovery Charge</b>	T <sub>J</sub> = 25°C I <sub>F</sub> = 10A di/dt = 100A/μsec	<b>t<sub>rr</sub></b> <b>Q<sub>RR</sub></b>	- -	120 -	200 -	<b>nsec</b>
<b>Input Capacitance</b> <b>Output Capacitance</b> <b>Reverse Transfer Capacitance</b>	V <sub>GS</sub> = 0 Volts V <sub>DS</sub> = 25 Volts f = 1 MHz	<b>C<sub>iss</sub></b> <b>C<sub>oss</sub></b> <b>Cr<sub>ss</sub></b>	- - -	4500 1600 800	- - -	<b>pF</b>

## NOTES:

1/ Maximum current limited by package, die rated at 75A.

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