

PROCESS CP192

Small Signal Transistor

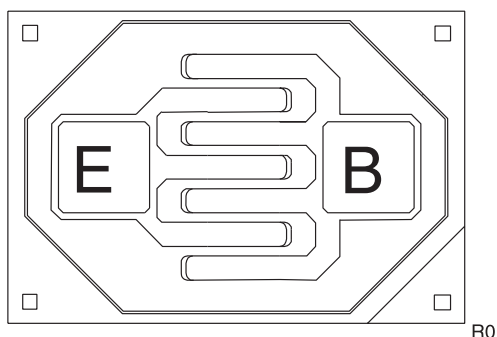
[查询"CP192"供应商](#)
NPN Amp/Switch Transistor Chip

CentralTM
Semiconductor Corp.

PROCESS DETAILS

Process	EPITAXIAL PLANAR
Die Size	13 x 17 MILS
Die Thickness	9.0 MILS
Base Bonding Pad Area	3.0 X 3.0 MILS
Emitter Bonding Pad Area	3.0 X 3.0 MILS
Top Side Metalization	Al - 30,000Å
Back Side Metalization	Au - 18,000Å

GEOMETRY



BACKSIDE COLLECTOR

GROSS DIE PER 4 INCH WAFER

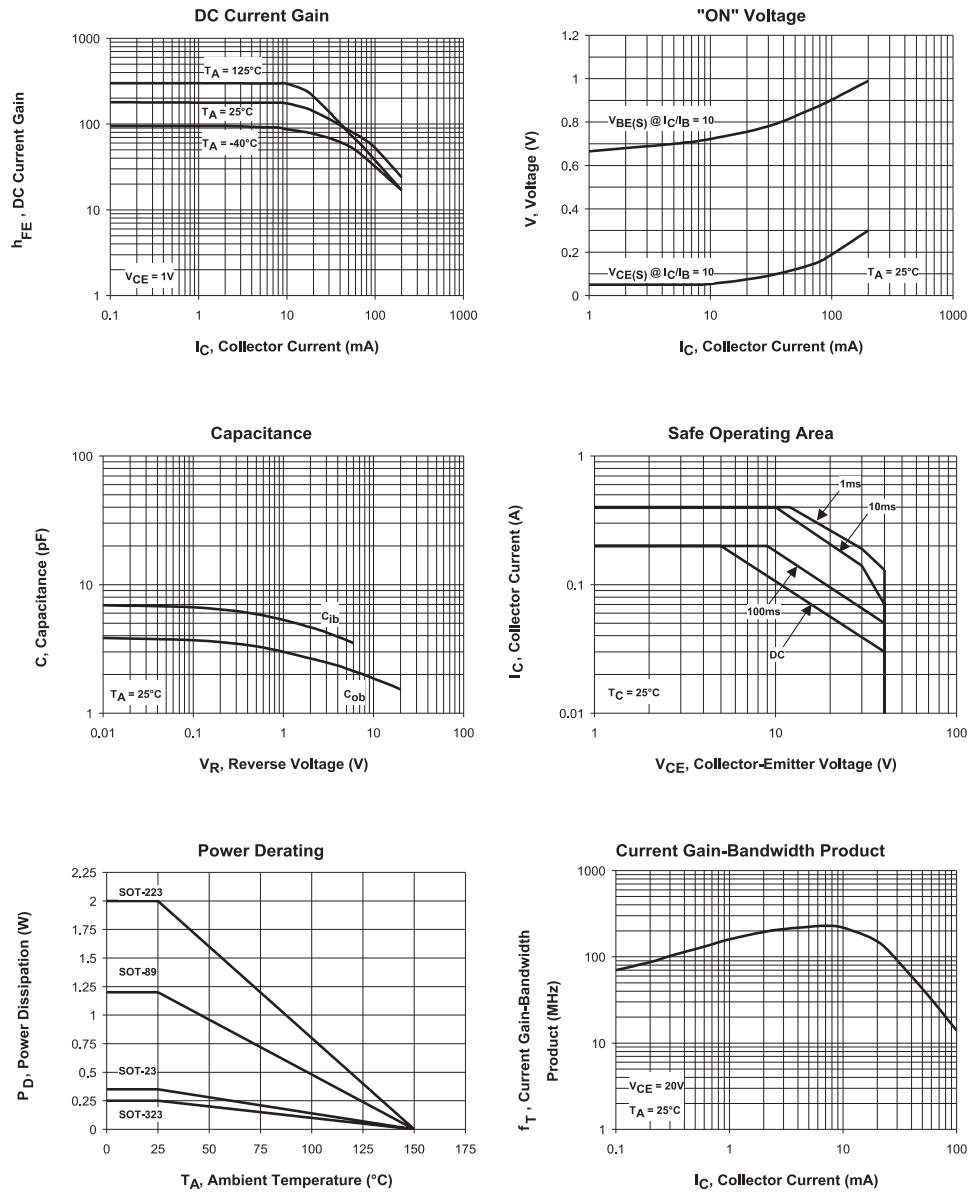
52,240

PRINCIPAL DEVICE TYPES

2N3904
CMPT3904
CMST3904
CXT3904
CZT3904

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