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1. SCOPE 1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices". 1.2 Part number. The complete part number shall be as shown in the following example: 5962-86702 Case outline Lead finish per Device type (1.2.1) Drawing number MIL-M-38510 (1.2.2)1.2.1 Device type. The device type shall identify the circuit function as follows: Circuit function Generic number 131556 Device type Quad register with two independently controlled 01 25LS2519 or 2919 three-state outputs 1.2.2 Case outlines. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows: Case outline **Outline** letter D-8 (20-lead, $1/4^{*} \times 1 1/16^{*}$), dual-in-line package F-9 (20-lead, $1/4^{*} \times 1/2^{*}$), flat package C-2 (20-terminal, .350 $^{*} \times .350^{*}$), square chip carrier package R S 2 1.3 Absolute maximum ratings. -0.5 V dc to +7.0 V dc -1.5 V dc to 7.0 V dc -65 C to +150 C Maximum power dissipation (P_D) <u>1</u>/ - - - - - - Lead temperature (soldering, 10 seconds) - - - Thermal resistance, junction-to-case (θ_{JC}): 700 mW 300°C 23°C/W - - - -21°C/W 25°C/W 150°C DC output current into outputs - - - - - - - -30 mA -30 mA to +5.0 mA DC input current - - - - - - - -- - - - - - -1/ Must withstand the added $P_{\rm D}$ due to short circuit test (e.g., $I_{\rm OS}).$ CODE IDENT. NO. DWG NO. SIZE MILITARY DRAWING 14933 5962-86702 Α DEFENSE ELECTRONICS SUPPLY CENTER 2 PAGE DAYTON, OHIO REV **DESC FORM 193A FEB 86**

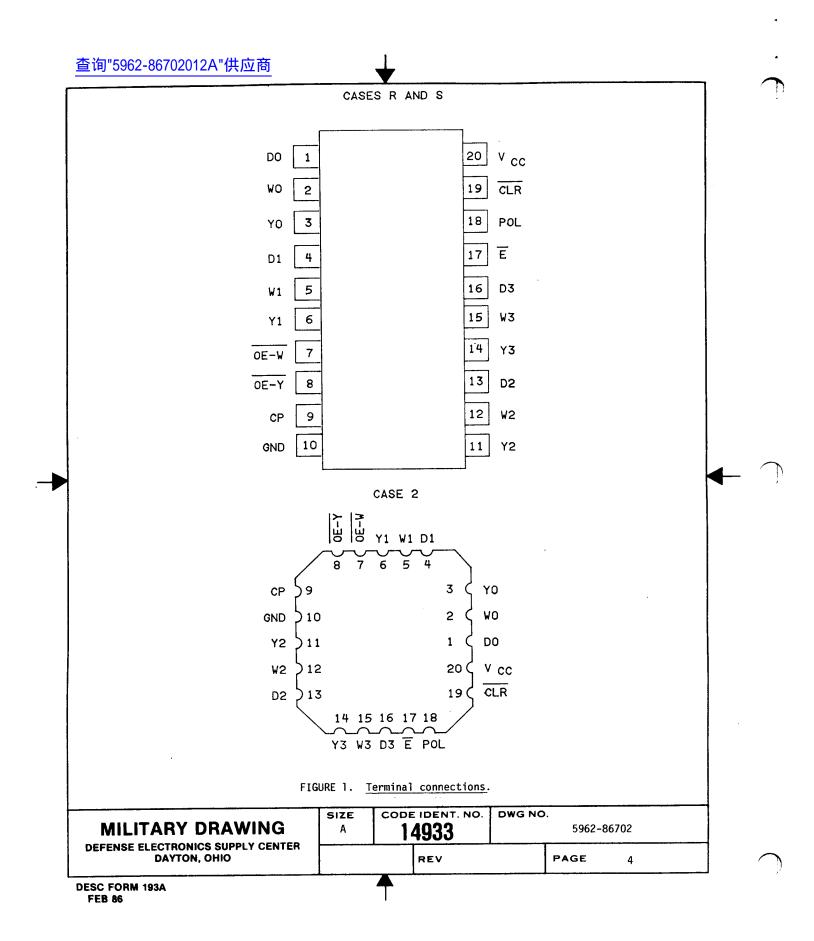
1.4 Recommended operating conditions	•		
Supply voltage (V _{CC}) Minimum high-level input voltage Maximum low-level input voltage (Ambient operating temperature ram Setup time, data enable Hold time, data enable Setup time, clear recovery (inact Clear pulse width Clock pulse width high Setup time, data Hold time, data	(V _{IH}) V _{IL}) ge (T _A) ive), to clock-	+4.5 V dc to 2.0 V dc 0.7 V dc -55 C to +125 25 ns min 0 ns min 24 ns min 20 ns min 20 ns min 20 ns min 15 ns min 10 ns min	
2. APPLICABLE DOCUMENTS			
2.1 <u>Government specification and sta</u> and standard, of the issue listed in th and Standards specified in the solicita SPECIFICATION	at iccup of the	Department of I	Jerense Index of Specifications
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	rcuits, General	Specification	UI •
STANDARD			
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	thods and Procee		
(Copies of the specification and stan acquisition functions should be obtaine contracting activity.) 2.2 <u>Order of precedence</u> . In the eve references cited herein, the text of th	d from the continut of a conflict	racting activity	ext of this drawing and the
3. REQUIREMENTS	•	-	
3.1 Item requirements. The individu MIL-STD-883, "Provisions for the use of as specified herein.	al item require MIL-STD-883 in	ments shall be conjunction wi	in accordance with 1.2.1 of th compliant non-JAN devices" an
3.2 <u>Design, construction, and physic</u> dimensions shall be as specified in MIL	-M-38510 and her	rein.	
3.2.1 <u>Terminal connections</u> . The ter	minal connection	ns shall be as	specified on figure 1.
3.2.2 <u>Truth table</u> . The truth table	shall be as spec	cified on figur	e 2.
3.2.3 Logic diagram. The logic diag	ram shall be as	specified on f	igure 3.
3.2.4 <u>Case outlines</u> . The case outli	nes shall be in	accordance wit	h 1.2.2 herein.
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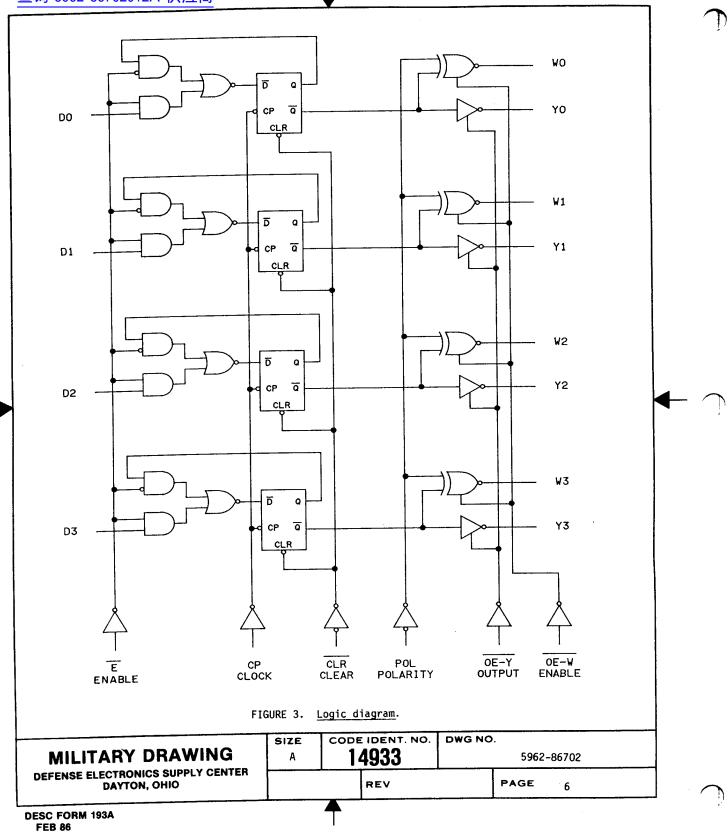
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Function	1			Inp	uts		r	Internal	Outp	
	I CP		 E 	CLR	I POL	OE-W	OE-Y	Q	ί Ψ ₁	۲ _۱
Output Three-State-Control			X X X X	X X		H L K L	L H H L 	INC NC NC NC NC	Z Enabled Z Enabled	Enabled Z Enabled Enabled
W _l Polarity				X X	L L H 	L L L	 L L	INC NC INC	 Non-Inverting Inverting	 Non-Inverting Non-Inverting
Asynchronous Clear	X X	X		L L L	L H	L L	 L L		L H	
Clock Enabled	•	x	Т IH	і н	X	x	X	NC	NC	NC
I – High NC = I	on't ca No char Low to	IL IH IH I are		H H H H 	L H L H 		L L L L		L H H L	L H H H
I = High NC = I	on't ca No chai	LL HH HH nge high	IL IL IL IL I tra	H H H H nsiti	L H L H 			L H	H H	L I H
I = High NC = I	l No char Low to	L L H H H I A re high	IL IL IL IL FIG	H H H H nsiti	1 L 1 H 1 L 1 H 1 H 2. <u>T</u>	ruth t	able.	L H H H H	H H L L	L I H

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3.3 <u>Electrical performance characteristics</u>. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full recommended ambient operating temperature range.

3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.

3.5 <u>Certificate of compliance</u>. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.6 <u>Certificate of conformance</u>. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.8 <u>Verification and review</u>. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 <u>Screening</u>. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

- a. Burn-in test (method 1015 of MIL-STD-883).
 - (1) Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_{A} = +125^{\circ}C$, minimum.
- b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, and 6 in table I, method 5005 of MIL-STD-883 shall be omitted.

c. Subgroup 7 and 8 tests shall verify the truth table.

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	 Cumbol	[Condia		Crown A	T LT	mits	Unit
Test	Symbol 	Condit -55°C <u><</u> T _A	tions <u><</u> +125°C	Group A subgroups 	Min	Max	- 0/11
High level output voltage	V _{OH}	$V_{CC} = 4.5 V, I_{OH}$ $V_{IN} = 0.7 V or 2$	= -1.0 mA, .0 V	1, 2, 3	2.4		V
Low level output voltage	IV _{OL}	$V_{CC} = 4.5 V,$ $V_{IN} = 0.7 V$	$I_{0L} = 4.0 \text{ mA}$	1, 2, 3	 	0.4	۷
		or 2.0 V	$I_{0L} = 8.0 \text{ mA}$	1, 2, 3	1	0.45	۷
			$I_{0L} = 12.0 \text{ mA}$	1, 2, 3	i 	0.5	۷
Input clamp voltage	VIC	$V_{\rm CC} = 4.5 V, I_{\rm IN}$	i = −18 mA	1, 2, 3	l 1 1	-1.5	v
High level input current	I IH1	$V_{\rm CC} = 5.5 V, V_{\rm IN}$	I = 2.7 V	1, 2, 3		20	μA
	I IH2	$V_{\rm CC} = 5.5 V, V_{\rm IN}$	= 7.0 V	1, 2, 3	 	100	μA
Low level input current	IIL	$V_{\rm CC} = 5.5 V, V_{\rm IN}$	I = 0.4 V	1, 2, 3	 	-0.36	mA
Short circuit output current	I _{OS}	$V_{\rm CC} = 5.5 V, V_{\rm OL}$	IT = 0.0 V	1, 2, 3	-15	-85	mA
Supply current	1 ^I CC	 Inputs grounded, Outputs open	V _{CC} = 5.5 V,	1, 2, 3		36	mA
Off-state output current	I _{OZH}	V _{CC} = 5.5 V, V _{OL}	17 = 2.4 V	1, 2, 3		20	μA
	IOZL	$V_{\rm CC} = 5.5 V, V_{\rm OL}$	IT = 0.4 V	1, 2, 3		-20	μA
See footnote at end of table.							
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		T			Limits	
Test	Symbol	-55	Conditions °C \leq T _A \leq +125°C	Group A subgroups 	Min Max	Unit
ropagation delay from CP to Y1	t _{PLH1}	V _{CC} = 4.5 C _L = 50 p	V to 5.5 V, F	9, 10, 11	42	ns
	t _{PHL1}			9, 10, 11	45	ns
ropagation delay from CP to W1	t _{PLH2}			9, 10, 11	43	ns
	t _{PHL2}			9, 10, 11	48	ns
ropagation delay from clear to Yi	t _{PHL3}	- 		9, 10, 11	58	ns
ropagation delay from clear to W1	tpLH4	-i 		9, 10, 11	43	ns
cieur to mi	t _{PHL4}	-i 		9, 10, 11	L 53	ns
ropagation delay from polarity to Wl	tpLH5			9, 10, 1	1 45	ns
	tPHL5			9, 10, 1	1 44	ns
Propagation delay from OE to W1 or Y1	tpzh			9, 10, 1	1 27	ns
	tpzL	-l ! !		9, 10, 1	1 35	ns
Propagation delay from OE to W1 or Y1	tPHZ	V _{CC} = +4. C _L = 5.0	.5 V to +5.5 V, pF	9, 10, 1	1 45	ns
	lt _{PLZ}			9, 10, 1	1 26	ns i
<u>1</u> / Not more than one out condition should not	put should exceed one	second.	at a time, and the		e snort circ	u1t
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4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test (method 1005 of MIL-STD-883) conditions:
 - Test condition A, B, C, or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^{\circ}C$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by appendix B of MIL-M-38510 and method 1005 of MIL-STD-883.

MIL-STD-883 test requirements	Subgroups (per method 5005, table I)
Interim electrical parameters (method 5004)	
Final electrical test parameters (method 5004)	1*, 2, 3, 7, 8, 9
Group A test requirements (method 5005)	1, 2, 3, 7, 8, 9, 10**, 11**
Groups C and D end-point lelectrical parameters (method 5005)	1, 2, 3

TABLE II. Electrical test requirements.

*PDA applies to subgroup 1. **Subgroups 10 and 11, if not tested, shall be guaranteed to the specified limits in table I.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 <u>Replaceability</u>. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

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6.3 <u>Comments</u>. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

6.4 <u>Approved source of supply</u>. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed herein has agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor CAGE number	Vendor similar part number <u>1</u> /
5962-8670201RX	34335	AM25LS2519/BRA AM2919/BRA
5962-8670201SX	34335	AM25LS2519/BSA AM2919/BSA
5962-86702012X	34335	AM25LS2519/B2C AM2919/B2C

<u>1</u>/ <u>Caution</u>. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE number

34335

Vendor name and address

Advanced Micro Devices, Incorporated 901 Thompson Place P.O.Box 3453 Sunnyvale, CA 94088

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