

## 查询"25K3229"供应商 2SK3229

# Silicon N Channel MOS FET High Speed Power Switching

REJ03G1095-0200

(Previous: ADE-208-766)

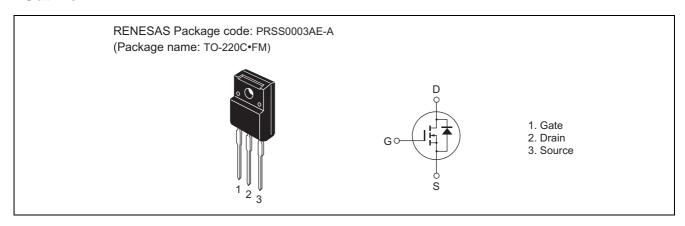
Rev.2.00

Sep 07, 2005

#### **Features**

- Low on-resistance  $R_{DS\;(on)} = 6\; m\Omega \; typ. \label{eq:DS}$
- Low drive current
- 4 V gate drive device can be driven from 5 V source

#### **Outline**



## Abso Line3 Max (如 可 Ratings

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	80	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	60	Α
Drain peak current	I <sub>D (pulse)</sub> Note 1	240	А
Body-drain diode reverse drain current	I <sub>DR</sub>	60	Α
Avalanche current	I <sub>AP</sub> Note 3	50	Α
Avalanche energy	E <sub>AR</sub> Note 3	181	mJ
Channel dissipation	Pch Note 2	35	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at Tc = 25°C

3. Value at Tch  $\leq$  25°C, Rg  $\geq$  50  $\Omega$ 

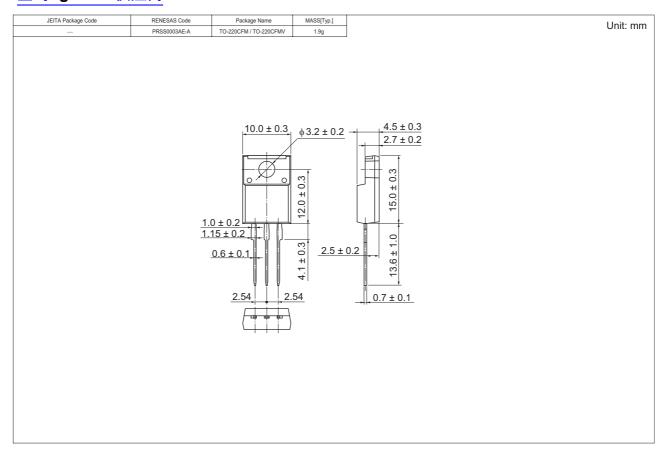
#### **Electrical Characteristics**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR) DSS</sub>	80	_		V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>		_	10	μΑ	$V_{DS} = 80 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	1.0	_	2.5	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	_	6.0	7.5	mΩ	$I_D = 30 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 4}}$
	R <sub>DS (on)</sub>	_	8.0	12	mΩ	$I_D = 30 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note 4}}$
Forward transfer admittance	y <sub>fs</sub>	50	85		S	$I_D = 30 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss	_	9700	_	pF	I <sub>D</sub> = 10 V
Output capacitance	Coss	_	1250	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	290	_	pF	f = 1 MHz
Total gate charge	Qg	_	150	_	nC	V <sub>DD</sub> = 25 V
Gate to source charge	Qgs	_	30	_	nC	$V_{GS} = 25 \text{ V}$
Gate to drain charge	Qgd	_	30	_	nC	$I_D = 60 \text{ A}$
Turn-on delay time	t <sub>d (on)</sub>	_	80	_	ns	$I_D = 30 \text{ A}$
Rise time	t <sub>r</sub>	_	280	_	ns	$V_{GS} = 10 \text{ V}$
Turn-off delay time	t <sub>d (off)</sub>	_	780	_	ns	$R_L = 1 \Omega$
Fall time	t <sub>f</sub>	_	340	_	ns	
Body-drain diode forward voltage	$V_{DF}$	_	1.0	_	V	$I_F = 60 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	80	_	ns	$I_F = 60 \text{ A}, V_{GS} = 0$
						$di_F/dt = 50 A/\mu s$

Note: 4. Pulse test

### Package Dimensions



## **Ordering Information**

Part Name	Quantity	Shipping Container
2SK3229-E	50 pcs	Plastic magazine

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