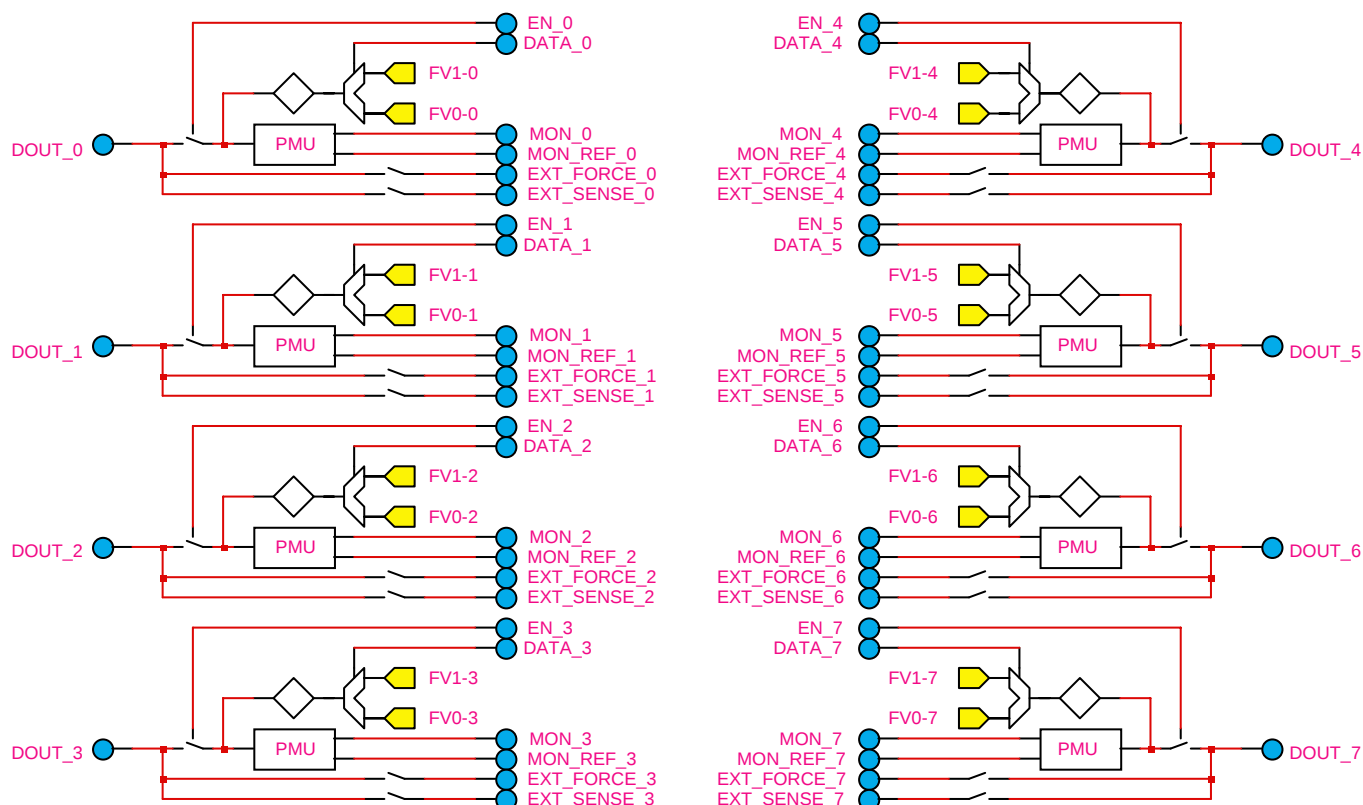


SOC Octal Wide Voltage PMU / Load

[Get Full Datasheet](#)



Per Channel Active Load

- 24 mA maximum current
- MI capability
- Independent source and sink current levels
- Extremely Low HiZ Leakage over a 32V Range
- 32V Input Compliance / 28V Output Forcing Range
- Extremely Low Input Leakage over a 32V Range

Per Channel PMU

- FV / MI / MV
- 5 Current Ranges (24 mA, 4 mA, 400 μ A, 40 μ A, 4 μ A)
- FV Current Clamps
- 32V Input Compliance / 28V Output Forcing Range
- Extremely Low Input Leakage over a 32V Range

Monitor

- Differential per Channel Monitor w/ HiZ
- Differential Central Monitor w/ HiZ

Operating Voltage Adjustable Over a Wide Voltage Range

External Force / Sense per Channel

3 Bit Serial CPU Port

On Chip DAC to Generate DC Levels

- 4 DC Levels per Channel (16 Bits / Level)
- On Chip Offset and Gain Correction per Level

Package / Power Dissipation

- 128 Lead, 14 mm X 20 mm, TQFP w/ Heat Slug
- $Pdq \leq 250$ mW / Channel; $Pdq \leq 2$ W / Chip

Product Status	Revision Date
Engineering Samples	11 / 21 / 07