Complementary ThermalTrak[™] Transistors

The ThermalTrak family of devices has been designed to eliminate thermal equilibrium lag time and bias trimming in audio amplifier applications. They can also be used in other applications as transistor die protection devices.

Features

- Thermally Matched Bias Diode
- Instant Thermal Bias Tracking
- Absolute Thermal Integrity
- High Safe Operating Area
- Pb–Free Packages are Available*

Benefits

- Eliminates Thermal Equilibrium Lag Time and Bias Trimming
- Superior Sound Quality Through Improved Dynamic Temperature Response
- Significantly Improved Bias Stability
- Simplified Assembly
 - Reduced Labor Costs
 - Reduced Component Count
- High Reliability

Applications

- High-End Consumer Audio Products
 - Home Amplifiers
 - Home Receivers
- Professional Audio Amplifiers
 - Theater and Stadium Sound Systems
 - Public Address Systems (PAs)



ON Semiconductor®

http://onsemi.com

BIPOLAR POWER TRANSISTORS 15 AMP, 350 VOLT, 230 WATT



TO-264, 5 LEAD CASE 340AA STYLE 1

MARKING DIAGRAM

SCHEMATIC





ORDERING INFORMATION

Device	Package	Shipping
NJL4281D	TO-264	25 Units / Rail
NJL4281DG	TO–264 (Pb–Free)	25 Units / Rail
NJL4302D	TO-264	25 Units / Rail
NJL4302DG	TO-264 (Pb-Free)	25 Units / Rail

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

塑椅INUM 在对INGS 供应密 unless otherwise noted)

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V _{CEO}	350	Vdc
Collector–Base Voltage	V _{CBO}	350	Vdc
Emitter-Base Voltage	V _{EBO}	5	Vdc
Collector-Emitter Voltage - 1.5 V	V _{CEX}	350	Vdc
Collector Current – Continuous – Peak (Note 1)	Ι _C	15 30	Adc
Base Current – Continuous	Ι _Β	1.5	Adc
Total Power Dissipation @ $T_C = 25^{\circ}C$ Derate Above 25°C	PD	230 1.84	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	- 65 to +150	°C
DC Blocking Voltage	V _R	200	V
Average Rectified Forward Current	I _{F(AV)}	1.0	A

THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction-to-Case	$R_{ extsf{ heta}JC}$	0.54	°C/W

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Pulse Test: Pulse Width = 5 ms, Duty Cycle < 10%.

ATTRIBUTES

	Characteristic	Value		
ESD Protection	Human Body Model Machine Model	>8000 V > 400 V		
Flammability Rating		UL 94 V-0 @ 0.125 in		

TICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Мах	Unit
OFF CHARACTERISTICS			·	
Collector Emitter Sustaining Voltage $(I_C = 50 \text{ mA}, I_B = 0)$	V _{CE(sus)}	350	-	Vdc
Collector Cut–off Current ($V_{CE} = 200 \text{ V}, I_B = 0$)	I _{CEO}	-	100	μAdc
Collector Cutoff Current ($V_{CB} = 350 \text{ Vdc}, I_E = 0$)	I _{CBO}	_	50	μAdc
Emitter Cutoff Current ($V_{EB} = 5.0 \text{ Vdc}, I_C = 0$)	I _{EBO}	_	5.0	μAdc
SECOND BREAKDOWN	I	1	1	-
Second Breakdown Collector with Base Forward Biased $(V_{CE} = 50 \text{ Vdc}, t = 1.0 \text{ s} \text{ (non-repetitive)}$ $(V_{CE} = 100 \text{ Vdc}, t = 1.0 \text{ s} \text{ (non-repetitive)}$	I _{S/b}	4.5 1.0		Adc
ON CHARACTERISTICS	ŀ			
$ \begin{array}{l} \text{DC Current Gain} \\ (I_{C} = 100 \text{ mAdc}, \text{V}_{CE} = 5.0 \text{ Vdc}) \\ (I_{C} = 1.0 \text{ Adc}, \text{V}_{CE} = 5.0 \text{ Vdc}) \\ (I_{C} = 3.0 \text{ Adc}, \text{V}_{CE} = 5.0 \text{ Vdc}) \\ (I_{C} = 5.0 \text{ Adc}, \text{V}_{CE} = 5.0 \text{ Vdc}) \\ (I_{C} = 8.0 \text{ Adc}, \text{V}_{CE} = 5.0 \text{ Vdc}) \\ (I_{C} = 15 \text{ Adc}, \text{V}_{CE} = 5.0 \text{ Vdc}) \end{array} $	h _{FE}	80 80 80 80 40 10	250 250 250 250 - -	-
Collector–Emitter Saturation Voltage $(I_C = 8.0 \text{ Adc}, I_B = 0.8 \text{ Adc})$	V _{CE(sat)}	_	1.0	Vdc
Emitter–Base Saturation Voltage $(I_C = 8.0 \text{ Adc}, I_B = 0.8 \text{ A})$	V _{BE(sat)}	_	1.4	Vdc
Base–Emitter ON Voltage (I _C = 8.0 Adc, V _{CE} = 5.0 Vdc)	V _{BE(on)}	_	1.5	Vdc
DYNAMIC CHARACTERISTICS	I	1	1	
Current–Gain – Bandwidth Product ($I_C = 1.0 \text{ Adc}, V_{CE} = 5.0 \text{ Vdc}, f_{test} = 1.0 \text{ MHz}$)	fT	35	_	MHz
Output Capacitance (V_{CB} = 10 Vdc, I _E = 0, f _{test} = 1.0 MHz)	C _{ob}	_	600	pF
Maximum Instantaneous Forward Voltage (Note 2) ($i_F = 1.0 \text{ A}, T_J = 25^{\circ}\text{C}$) ($i_F = 1.0 \text{ A}, T_J = 150^{\circ}\text{C}$)	VF	1.1 0.93		V
Maximum Instantaneous Reverse Current (Note 2) (Rated dc Voltage, $T_J = 25^{\circ}C$) (Rated dc Voltage, $T_J = 150^{\circ}C$)	i _R	1	10 00	μΑ
Maximum Reverse Recovery Time (i _F = 1.0 A, di/dt = 50 A/μs)	t _{rr}	1	00	ns

2. Diode Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.









查询"NJL4281DG"供应商

PACKAGE DIMENSIONS

TO-264, 5 LEAD CASE 340AA-01 **ISSUE O**





	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	25.857	25.984	26.111	1.018	1.023	1.028	
В	19.761	19.888	20.015	0.778	0.783	0.788	
С	4.928	5.055	5.182	0.194	0.199	0.204	
D	1.	219 BS(0	0.	0480 BS	SC	
Е	2.032	2.108	2.184	0.0800	0.0830	0.0860	
F	1.	981 BS(0	0.0780 BSC			
G	3	.81 BSC	;	0.150 BSC			
н	2.667	2.718	2.769	0.1050	0.1070	0.1090	
J	C	0.584 BSC			0.0230 BSC		
κ	20.422	20.549	20.676	0.804	0.809	0.814	
L	1	1.28 REF		0).444 RE	F	
М	0 °		7 °	0 °		7 °	
Ν		4.57 RE	F		0.180 R	EF	
Р	2.259	2.386	2.513	0.0889	0.0939	0.0989	
Q		3.480 BSC		0.1370 BSC			
R		2.54 REF		0.100 REF			
S	0 °		8 °	0 °		8 °	
U		6.17 R	EF	0.243 REF			
W	0 °		6 °	0 °		6 °	
Y		2.388 B	SC	0.0940 BSC			

DIMENSIONING AND TOLERANCING PER

2. CONTROLLING DIMENSION: MILLIMETER.

STYLE 1:

NOTES

ANSI Y14.5M, 1982.

PIN 1. BASE 2. EMITTER 3. COLLECTOR 4. ANODE

5. CATHODE

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