Low frequency transistor (–20V, –5A) 2SB1386 / 2SB1412

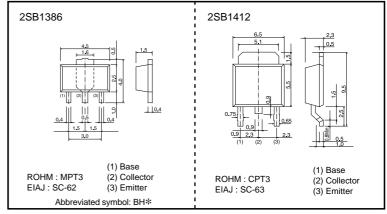
Features

- 1) Low VCE(sat). VCE(sat) = -0.35V (Typ.)(Ic/IB = -4A/-0.1A)
- 2) Excellent DC current gain characteristics.
- 3) Complements the 2SD2098 / 2SD2118.

Structure

Epitaxial planar type PNP silicon transistor

●Dimensions (Unit:mm)



^{*} Denotes her

● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Collector-base voltage		Vсво	-30	V	
Collector-emitter voltage		Vceo	-20	V	
Emitter-base voltage		VEBO	-6	V	
		lc	-5	A(DC)	
Collector current	Collector current		-10	A(Pulse) *1	
	Collector power 2SB1386		0.5	W	
Collector power		2	W *2		
dissipation	2SB1412	Pc	1	W	
			10	W(Tc=25°C)	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to 150	°C	

^{*1} Single pulse, Pw=10ms

^{*2} When mounted on a 40×40×0.7 mm ceramic board.

Transistors

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-30	-	_	V	Ic=-50μA
Collector-emitter breakdown voltage	BVceo	-20	-	_	V	Ic=-1mA
Emitter-base breakdown voltage	ВУево	-6	_	_	V	Iε= -50μA
Collector cutoff current	Ісво	_	_	-0.5	μΑ	Vcb= -20V
Emitter cutoff current	ІЕВО	_	_	-0.5	μΑ	V _{EB} = -5V
Collector-emitter saturation voltage	VCE(sat)	_	0.35	-1.0	V	Ic/I _B = -4A/ -0.1A *
DC current transfer ratio	hfe	82	_	390	_	Vce= -2V, Ic= -0.5A *
Transition frequency	f⊤	_	120	_	MHz	Vc==-6V, I==50mA, f=100MHz
Output capacitance	Cob	_	60	_	pF	Vcb= -20V, Ie=0A, f=1MHz

^{*} Measured using pulse current.

●Packaging specifications and hfe

		Package	Taping		
		Code	T100	TL	
Туре	hfe	Basic ordering unit (pieces)	1000	2500	
2SB1386	PQR		0	-	
2SB1412	PQR		_	0	

hre values are classified as follows:

Item	Р	Q	R
hfe	82 to 180	120 to 270	180 to 390

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Transistors

Electrical characteristic curves

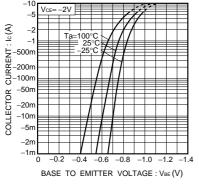


Fig.1 Grounded emitter propagation characteristics

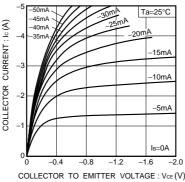


Fig.2 Grounded emitter output characteristics

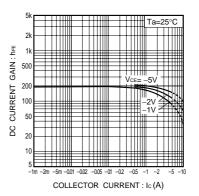


Fig.3 DC current gain vs. collector current (I)

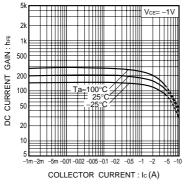


Fig.4 DC current gain vs. collector current (II)

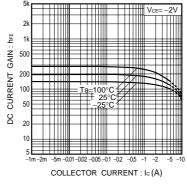


Fig.5 DC current gain vs. collector current (III)

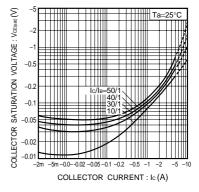


Fig.6 Collector-emitter saturation voltage vs. collector current (I)

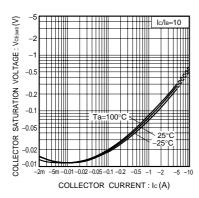


Fig.7 Collector-emitter saturation voltage vs. collector current (II)

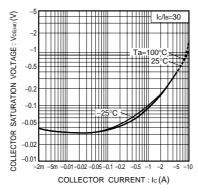


Fig.8 Collector-emitter saturation voltage vs. collector current (III)

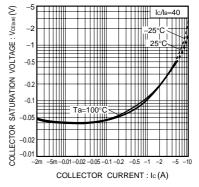
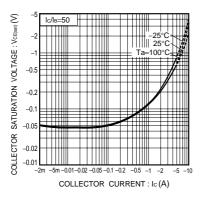
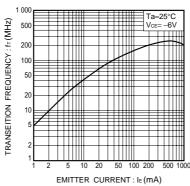


Fig.9 Collector-emitter saturation voltage vs. collector current (IV)

Transistors





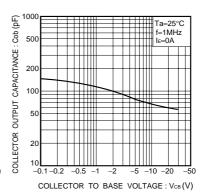


Fig.10 Collector-emitter saturation voltage vs. collector current (V)

Fig.11 Gain bandwidth product vs. emitter current

Fig.12 Collector output capacitance vs. collector-base voltage

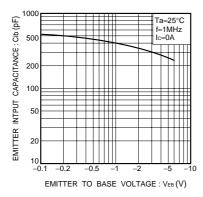


Fig.13 Emitter input capacitance vs. emitter-base voltage

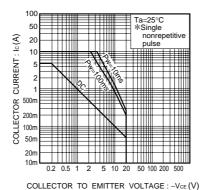


Fig.14 Safe operation area (2SB1412)

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