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TruTherm Technology

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LM95234 Quad Remote Diode and Local Temperature Sensor with SMBus Interface and

M95234 Quad Remote and Manus Interator with SMBus Interface and

LM95234 Quad Remote Diode and Local Temperature Sensor with SMBus Interface and TruTherm™ Technology

General Description

LM95234 is an 11-bit digital temperature sensor with a 2-wire System Management Bus (SMBus) interface that can very accurately monitor the temperature of four remote diodes as well as its own temperature that includes patent pending remote diode TruTherm™ BJT beta compensation beta compensation technology. The four remote diodes can be external devices such as microprocessors, graphics processors or diode-connected 2N3904s. The LM95234's TruTherm BJT beta compensation technology allows sensing of 90 nm or 65 nm process thermal diodes accurately.

The LM95234 reports temperature in two different formats for +127.875°C/–128°C range and 0°C/255°C range. The LM95234 TCRIT1, TCRIT2 and TCRIT3 outputs are triggered when any unmasked channel exceeds its corresponding programmable limit and can be used to shutdown the system, to turn on the system fans or as a microcontroller interrupt function. The current status of the TCRIT1, TCRIT2 and TCRIT3 pins can be read back from the status registers. Mask registers are available for further control of the TCRIT outputs.

Two LM95234 remote temperature channels have programmable digital filters while the other two remote channels utilize a fault-queue to minimize unwanted TCRIT events when temperature spikes are encountered.

For optimum flexibility and accuracy, each LM95234 channel includes registers for sub-micron process or 2N3904 diode model selection as well as offset correction. A three-level address pin allows connection of up to 3 LM95234s to the same SMBus master. The LM95234 includes power saving functions such as: programmable conversion rate, shutdown mode, and disabling of unused channels.

Features

Accurately senses die temperature of 4 remote ICs or diode junctions and local temperature

- TruTherm BJT beta compensation technology accurately senses sub-micron process thermal diodes
- Programmable digital filters and analog front end filter
- 0.125°C LSb temperature resolution
- 0.03125°C LSb remote temperature resolution with digital filter enabled
- $+127.875^{\circ}$ C/-128 $^{\circ}$ C and 0 $^{\circ}$ C/255 $^{\circ}$ C remote ranges
- Remote diode fault detection, model selection and offset correction
- Mask and status register support
- 3 programmable \overline{TCRIT} outputs with programmable shared hysteresis and Fault-Queue
- Programmable conversion rate and shutdown mode oneshot conversion control
- SMBus 2.0 compatible interface, supports TIMEOUT
- Three-level address pin
- 14-pin LLP package

Key Specifications

- Local Temperature Accuracy $±2.0^{\circ}C$ (max)
- Remote Diode Temperature Accuracy ±0.875°C (max)
- Supply Voltage 3.0V to 3.6V
- **Average Supply Current** (1Hz conversion rate)

Applications

- Processor/Computer System Thermal Management (e.g. Laptop, Desktop, Workstations, Server)
- **Electronic Test Equipment**
- **Office Electronics**

0.57 mA (typ)

Connection Diagram

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Simplified Block Diagram

LM95234

Typical Application LM95234 [查询"LM95234"供应商](http://www.dzsc.com/ic/sell_search.html?keyword=LM95234)

Absolute Maximum Ratings (Note 1)

Charge Device Model 1000V *Soldering process must comply with National's reflow temperature profile specifications. Refer to http:// www.national.com/packaging/. (Note 3)*

Operating Ratings

Temperature-to-Digital Converter Electrical Characteristics

Unless otherwise noted, these specifications apply for V_{DD} = +3.0Vdc to 3.6Vdc. **Boldface limits apply for T_A = T_J = T_{MIN} ≤ T_A ≤ T_{MAX}**; all other limits T_A = T_J = +25°C, unless otherwise noted.

SMBus DIGITAL SWITCHING CHARACTERISTICS

Unless otherwise noted, these specifications apply for V_{DD}=+3.0 Vdc to +3.6 Vdc, C_L (load capacitance) on output lines = 80 pF. <code>Boldface limits apply for T $_{\sf A}$ = T $_{\sf J}$ = T $_{\sf MIN}$ to T $_{\sf MAX}$; all other limits T $_{\sf A}$ = T $_{\sf J}$ = +25°C, unless otherwise noted.</code> \mathbb{Z} otherwise \mathbb{Z} dhenwise \mathbb{Z}

The switching characteristics of the LM95234 fully meet or exceed the published specifications of the SMBus version 2.0. The following parameters are the timing relationships between SMBCLK and SMBDAT signals related to the LM95234. They adhere to but are not necessarily the SMBus bus specifications.

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. DC and AC electrical specifications do not apply when operating the device beyond its rated operating conditions.

Note 2: When the input voltage (V_I) at any pin exceeds the power supplies (V_I < GND or V_I > V_{DD}), the current at that pin should be limited to 5 mA. Parasitic components and or ESD protection circuitry are shown in the table below for the LM95234's pins.

Note 3: Reflow temperature profiles are different for packages containing lead (Pb) than for those that do not.

Note 4: Human body model, 100 pF discharged through a 1.5 kΩ resistor. Machine model, 200 pF discharged directly into each pin. Charged Device Model (CDM) simulates a pin slowly acquiring charge (such as from a device sliding down the feeder in an automated assembler) then rapidly being discharged. **Note 5:** Thermal resistance junction-to-ambient when attached to a 4 layer printed circuit board per JEDEC standard JESD51-7:

 $-$ 14-lead LLP = 90°C/W (no thermal vias, no airflow)

- $-$ 14-lead LLP = 63°C/W (1 thermal via, no airflow)
- $-$ 14-lead LLP = 43°C/W (6 thermal vias, no airflow)

 $-$ 14-lead LLP = 31°C/W (6 thermal vias, 900 ln. ft. / min. airflow)

Note, all quoted values include +15% error factor from nominal value.

Note 6: Typicals are at $T_A = 25^{\circ}$ C and represent most likely parametric norm.

Note 7: Limits are guaranteed to National's AOQL (Average Outgoing Quality Level).

Note 8: Local temperature accuracy does not include the effects of self-heating. The rise in temperature due to self-heating is the product of the internal power dissipation of the LM95234 and the thermal resistance. See (Note 5) for the thermal resistance to be used in the self-heating calculation.

Note 9: The accuracy of the LM95234CISD is guaranteed when using a typical thermal diode of an Intel processor on a 65 nm process or an MMBT3904 diodeconnected transistor, as selected in the Remote Diode Model Select register. See typical performance curve for performance with Intel processor on a 90 nm process. For further information on other thermal diodes see applications *Section 3.1* "Diode Non-ideality" or send email to hardware.monitor.team@national.com. **Note 10:** Quiescent current will not increase substantially with an SMBus communication.

Note 11: This specification is provided only to indicate how often temperature data is updated. The LM95234 can be read at any time without regard to conversion state (and will yield last conversion result).

Note 12: The output rise time is measured from $(V_{IN(0)}$ max – 0.15V) to $(V_{IN(1)}$ min + 0.15V).

Note 13: The output fall time is measured from $(V_{IN(1)}$ min + 0.15V) to $(V_{IN(0)}$ max – 0.15V).

Note 14: Holding the SMBDAT and/or SMBCLK lines Low for a time interval greater than t_{TIMEOUT} will reset the LM95234's SMBus state machine, therefore setting SMBDAT and SMBCLK pins to a high impedance state.

Typical Performance Characteristics "LM95234"

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1.0 Functional Description

Report of

LMPS **CONSTRUCT IS An INSTERNATION**

LM95234 is an 11-bit digital temperature sensor with a 2-wire

System Management Bus (SMBus) interface that can monitor System Management Bus (SMBus) interface that can monitor the temperature of four remote diodes as well as its own temperature. The LM95234 can be used to very accurately monitor the temperature of up to four external devices such as microprocessors, graphics processors or diode-connected 2N3904 transistor. The LM95234 includes TruTherm BJT beta compensation technology that allows sensing of Intel processors 90 nm or 65 nm process thermal diodes accurately. The LM95234 reports temperature in two different formats for +127.875°C/–128°C range and 0°C/255°C range. The LM95234 has a Sigma-Delta ADC (Analog-to-Digital Converter) core which provides the first level of noise imunity. For improved performance in a noisy environment the LM95234 includes programmable digital filters for Remote Diode 1 and 2 temperature readings. When the digital filters are invoked the resolution for Remote Diode 1 and 2 readings increases to 0.03125°C. The LM95234 contains a diode model selection register that includes bits for each channel that select between thermal diodes of Intel processors on 65 nm process or 2N3904s. For maximum flexibility and best accuracy the LM95234 includes offset registers that allow calibration of other diode types.

> Diode fault detection circuitry in the LM95234 can detect the absence or fault state of a remote diode: whether D+ is shorted to V_{DD} , D- or ground, or whether D+ is floating.

> The LM95234 TCRIT1, TCRIT2 and TCRIT3 active low outputs are triggered when any unmasked channel exceeds its corresponding programmable limit and can be used to shutdown the system, to turn on the system fans or as a microcontroller interrupt function. The current status of the TCRIT1, TCRIT2 and TCRIT3 pins can be read back from the status registers via the SMBus interface. Two of the remote channels have two separate limits each that control the TCRIT1 and TCRIT2 pins. The remaining two channels and the local channel each have one limit to control both the TCRIT1 and TCRIT2 pins. The TCRIT3 pin shares the limits of the TCRIT2 pin but allows for different masking options. All limits have a shared programmable hysteresis register.

> Remote Diode 1 and 2 temperature channels have programmable digital filters while the other two remote temperature channels utilize a fault-queue in order to avoid false triggering the TCRIT pins.

> LM95234 has a three-level address pin to connect up to 3 devices to the same SMBus master. LM95234 also has programmable conversion rate register as well as a shutdown mode for power savings. One round of conversions can be triggered in shutdown mode by writing to the one-shot register through the SMBus interface. LM95234 can be programmed to turn off unused channels for more power savings.

> The LM95234 register set has an 8-bit data structure and includes:

- 1. Temperature Value Registers with signed format
	- Most-Significant-Byte (MSB) and Least-Significant-Byte (LSB) Local Temperature
	- MSB and LSB Remote Temperature 1
	- MSB and LSB Remote Temperature 2
	- MSB and LSB Remote Temperature 3
	- MSB and LSB Remote Temperature 4
- 2. Temperature Value Registers with unsigned format
	- MSB and LSB Remote Temperature 1
	- MSB and LSB Remote Temperature 2
- MSB and LSB Remote Temperature 3
- MSB and LSB Remote Temperature 4
- 3. Diode Configuration Registers
	- Diode Model Select
	- Remote 1 Offset
	- Remote 2 Offset
	- Remote 3 Offset
	- Remote 4 Offset
- 4. General Configuration Registers
	- Configuration (Standby, Fault Queue enable for Remote 3 and 4; Conversion Rate)
	- Channel Conversion Enable
	- Filter Setting for Remote 1 and 2
	- 1-Shot
- 5. Status Registers
	- Main Status Register (Busy bit, Not Ready, Status Register 1 to 4 Flags)
	- Status 1 (diode fault)
	- Status 2 (TCRIT1)
	- Status 3 (TCRIT2)
	- Status 4 (TCRIT3)
	- Diode Model Status
- 6. Mask Registers
	- TCRIT1 Mask
	- TCRIT2 Mask
	- TCRIT3 Mask
- 7. Limit Registers
	- Local Tcrit Limit
		- Remote 1 Tcrit-1 Limit
		- Remote 2 Tcrit-1 Limit
		- Remote 3 Tcrit Limit
		- Remote 4 Tcrit Limit
		- Remote 1 Tcrit-2 and Tcrit-3 Limit
		- Remote 2 Tcrit-2 and Tcrit-3 Limit
	- Common Tcrit Hysteresis
- 8. Manufacturer ID Register
- 9. Revision ID Register

1.1 CONVERSION SEQUENCE

The LM95234 takes approximately 190 ms to convert the Local Temperature, Remote Temperatures 1 through 4, and to update all of its registers. These conversions for each thermal diode are addressed in a round robin sequence. Only during the conversion process the busy bit (D7) in Status register (02h) is high. The conversion rate may be modified by the Conversion Rate bits found in the Configuration Register (03h). When the conversion rate is modified a delay is inserted between each round of conversions, the actual time for each round remains at 190 ms (typical all channels enabled). The time a round takes depends on the number of channels that are on. Different conversion rates will cause the LM95234 to draw different amounts of average supply current as shown in *Figure 1*. This curve assumes all the channels are on. If channels are turned off the average current will drop since the round robin time will decrease and the shutdown time will increase during each conversion interval.

FIGURE 1. Conversion Rate Effect on Power Supply Current

1.2 POWER-ON-DEFAULT STATES

LM95234 always powers up to these known default states. The LM95234 remains in these states until after the first conversion.

- 1. All Temperature readings set to 0°C until the end of the first conversion
- 2. Diode Model Select: Remote 1 set to 65 nm Intel processor, Remote 2-4 set to MMBT3904
- 3. Remote offset for all channels 0°C
- 4. Configuration: Active converting, Fault Queue enabled for Remote 3 and 4
- 5. Continuous conversion with all channels enabled, $time = 1s$
- 6. Enhanced digital filter enabled for Remote 1 and 2
- 7. Status Registers depends on state of thermal diode inputs
- 8. Local and Remote Temperature Limits for TCRIT1, TCRIT2 and TCRIT3 outputs:

- 9. Manufacturers ID set to 01h
- 10. Revision ID set to 79h

1.3 SMBus INTERFACE

The LM95234 operates as a slave on the SMBus, so the SMBCLK line is an input and the SMBDAT line is bidirectional. The LM95234 never drives the SMBCLK line and it does not support clock stretching. According to SMBus specifications, the LM95234 has a 7-bit slave address. Three SMBus device address can be selected by connecting A0 (pin 6) to either Low, Mid-Supply or High voltages. The LM95234 has the following SMBus slave address:

1.4 TEMPERATURE CONVERSION SEQUENCE

Each of the 5 temperature channels of LM95234 can be turned OFF independent from each other via the Channel Enable Register. Turning off unused channels will increase the conversion speed in the fastest conversion speed mode. If the slower conversion speed settings are used, disabling unused channels will reduce the average power consumption of LM95234.

1.4.1 DIGITAL FILTER

In order to suppress erroneous remote temperature readings due to noise as well as increase the resolution of the temperature, the LM95234 incorporates a digital filter for Remote 1 and 2 Temperature Channels. When a filter is enabled the filtered readings are used for the TCRIT comparisons. There are two possible digital filter settings that are enabled through the Filter Setting Register at register address 0Fh. The filter for each channel can be set according to the following table:

Figure 2 describes the filter output in response to a step input and an impulse input.

FIGURE 3. Digital Filter Response in a typical Intel processor on a 65 nm or 90 nm process. The filter curves were purposely offset for clarity.

Figure 3 shows the filter in use in a typical Intel processor on a 65/90 nm process system. Note that the two curves have been purposely offset for clarity. Inserting the filter does not induce an offset as shown.

1.5 FAULT QUEUE

In order to suppress erroneous TCRIT1, TCRIT2 and TCRIT3 triggering the LM95234 incorporates a Fault Queue for the unfiltered remote channels 3 and 4. The Fault Queue acts to ensure the remote temperature measurement of these channels is genuinely beyond the corresponding Tcrit limit by not triggering until three consecutive out of limit measurements have been made, see *Figure 4* for an example. The Fault Queue defaults on upon power-up. The fault queue for channels 3 and 4 can be turned ON or OFF via bits 0 and 1 of the Configuration Register. When the fault queue is enabled, the TCRIT1, TCRIT2 and TCRIT3 pins will be triggered if the temperature is above the Tcrit limit for 3 consecutive conversions and the corresponding mask bit is 0 in the TCRIT Mask registers. Similarly the temperature needs to be below the Tcrit limit minus the hysteresis value for three consecutive

conversions for the TCRIT1, TRCIT2 and TCRIT3 pins to deactivate.

FIGURE 4. Fault Queue Response Diagram (with 0°C hysteresis)

1.6 TEMPERATURE DATA FORMAT

Temperature data can only be read from the Local and Remote Temperature value registers. The data format for all temperature values is left justified 16-bit word available in two 8-bit registers. Unused bits will always report "0". All temperature data is clamped and will not roll over when a temperature exceeds full-scale value.

Remote temperature data for all channels can be represented by an 11-bit, two's complement word or unsigned binary word with an LSb (Least Significant Bit) equal to 0.125°C.

11-bit, Unsigned Binary

When the digital filter is enabled on Remote 1 and 2 channels temperature data is represented by a 13-bit unsigned binary or 12-bit plus sign (two's complement) word with an LSb equal to 0.03125°C.

13-bit, 2's Complement (12-bit plus sign)

Local Temperature data is only represented by an 11-bit, two's complement, word with an LSb equal to 0.125°C.

11-bit, 2's Complement (10-bit plus sign)

1.7 SMBDAT OPEN-DRAIN OUTPUT

The SMBDAT output is an open-drain output and does not have internal pull-ups. A "high" level will not be observed on this pin until pull-up current is provided by some external source, typically a pull-up resistor. Choice of resistor value depends on many system factors but, in general, the pull-up resistor should be as large as possible without effecting the SMBus desired data rate. This will minimize any internal temperature reading errors due to internal heating of the LM95234. The maximum resistance of the pull-up to provide a 2.1V high level, based on LM95234 specification for High Level Output Current with the supply voltage at 3.0V, is 82 kΩ (5%) or 88.7 kΩ (1%).

1.8 TCRIT1, TCRIT2, and TCRIT3 OUTPUTS

The LM95234's TCRIT pins are active-low open-drain outputs and do not include internal pull-up resistors. A "high" level will not be observed on these pins until pull-up current is provided by some external source, typically a pull-up resistor. Choice of resistor value depends on many system factors but, in general, the pull-up resistor should be as large as possible without effecting the performance of the device receiving the signal. This will minimize any internal temperature reading errors due to internal heating of the LM95234. The maximum resistance of the pull-up to provide a 2.1V high level, based on LM95234 specification for High Level Output Current with the supply voltage at 3.0V, is 82 kΩ (5%) or 88.7 kΩ (1%). The three TCRIT pins can each sink 6 mA of current and still guarantee a "Logic Low" output voltage of 0.4V. If all three pins are set at maximum current this will cause a power dissipation of 7.2 mW. This power dissipation combined with a thermal resistance of 77.8°C/W will cause the LM95234's junction temperature to rise approximately 0.6°C and thus cause the Local temperature reading to shift. This can only be cancelled out if the environment that the LM95234 is enclosed in has stable and controlled air flow over the LM95234, as airflow can cause the thermal resistance to change dramatically.

1.9 Tcrit LIMITS AND TCRIT OUTPUTS

Figure 5 describes a simplified diagram of the temperature comparison and status register logic. *Figure 6* describes a simplified logic diagram of the circuitry associated with the status registers, mask registers and the TCRIT output pins.

a) TCRIT1 Mask Register, Status Register 1 and 2, and TCRIT1 output logic diagram.

c) TCRIT3 Mask Register, Status Register 1 and 4, and TCRIT3 output logic diagram.

FIGURE 6. Logic diagrams for the TCRIT1, TCRIT2, and TCRIT3 outputs.

If enabled, local temperature is compared to the user programmable Local Tcrit Limit Register (Default Value = 85° C). The result of this comparison is stored in Status Register 2, Status Register 3 and Status Register 4 (see *Figure 5*).The $comparison$ result can trigger $\overline{TCRIT1}$ pin, $\overline{TCRIT2}$ pin or TCRIT3 pin depending on the settings in the TCRIT1 Mask, TCRIT2 Mask and TRCIT3 Mask Registers (see *Figure 6*). The comparison result can also be read back from the Status Register 2, Status Register 3 and Status Register 4.

If enabled, remote temperature 1 is compared to the user programmable Remote 1 Tcrit-1 Limit Register (Default Value 110°C) and Remote 1 Tcrit-2 Limit Register (Default Value = 85°C). The result of this comparison is stored in Status Register 2, Status Register 3 and Status Register 4 (see *Figure* 5). The comparison result can trigger TCRIT1 pin, TCRIT2 pin or TCRIT3 pin depending on the settings in the TCRIT1 Mask, TCRIT2 Mask and TRCIT3 Mask Registers (see *Figure 6*). The comparison result can also be read back from the Status Register 2, Status Register 3 and Status Register 4. The remote temperature 2 operates in a similar manner to remote temperature 1 using its associated user programmable limit registers: Remote 2 Tcrit-1 Limit Register (Default Value 110° C) and Remote 2 Tcrit-2 Limit Register (Default Value = 85° C). When enabled, the remote temperature 3 is compared to the user programmable Remote 3 Tcrit Limit Register (Default Value 85°C). The comparison result can trigger TCRIT1 pin, TCRIT2 pin or TCRIT3 pin depending on the settings in the TCRIT1 Mask, TCRIT2 Mask and TCRIT3 Mask Registers. The comparison result can also be read back from the Status Register 2, Status Register 3 and Status Register 4. The remote temperature 4 operates in a similar manner to remote temperature 3 using its associated user programmable limit register: Remote 4 Tcrit Limit Register (Default Value 85°C).

FIGURE 7. TCRIT response diagram (masking options not included)

The TCRIT response diagram of *Figure 7* shows the local temperature interaction with the Tcrit limit and hysteresis value. As can be seen in the diagram when the local temperature exceeds the Tcrit limit register value the LTn Status bit is set and the \overline{T} CRITn output(s) is/are activated. The Status bit(s) and outputs are not deactivated until the temperature goes below the value calculated by subtracting the Common Hysteresis value programmed from the limit. This diagram mainly shows an example function of the hysteresis and is not meant to show complete function of the possible settings and options of all the TCRIT outputs and limit values.

1.10 DIODE FAULT DETECTION

The LM95234 is equipped with operational circuitry designed to detect fault conditions concerning the remote diodes. In the event that the D+ pin is detected as shorted to GND, D−, V_{DD} or D+ is floating, the Remote Temperature reading is –128.000 °C if signed format is selected and 0 °C if unsigned format is selected. In addition, the appropriate status register bits RD1M or RD2M (D1 or D0) are set.

1.11 COMMUNICATING with the LM95234

The data registers in the LM95234 are selected by the Command Register. At power-up the Command Register is set to "00", the location for the Read Local Temperature Register. The Command Register latches the last location it was set to. Each data register in the LM95234 falls into one of three types of user accessibility:

- 1. Read only
- 2. Write only
- 3. Write/Read same address

A **Write** to the LM95234 will always include the address byte and the command byte. A write to any register requires one data byte.

Reading the LM95234 can take place either of two ways:

- 1. If the location latched in the Command Register is correct (most of the time it is expected that the Command Register will point to one of the Read Temperature Registers because that will be the data most frequently read from the LM95234), then the read can simply consist of an address byte, followed by retrieving the data byte.
- 2. If the Command Register needs to be set, then an address byte, command byte, repeat start, and another address byte will accomplish a read.

The data byte has the most significant bit first. At the end of a read, the LM95234 can accept either acknowledge or No Acknowledge from the Master (No Acknowledge is typically used as a signal for the slave that the Master has read its last byte). It takes the LM95234 190 ms (typical, all channels enabled) to measure the temperature of the remote diodes and internal diode. When retrieving all 11 bits from a previous remote diode temperature measurement, the master must insure that all 11 bits are from the same temperature conversion. This may be achieved by reading the MSB register first. The LSB will be locked after the MSB is read. The LSB will be unlocked after being read. If the user reads MSBs consecutively, each time the MSB is read, the LSB associated with that temperature will be locked in and override the previous LSB value locked-in.

insure a timeout of all devices on the bus the SMBCLK p**⊼ ያለ⁄iBDA⁄T** lines must be held low for at least 35ms. When SMBDAT is HIGH, have the master initiate an R
 LASS
 LASS

SMBus start. The LM95234 will respond properly to an SMBus start condition at any point during the communication. After the start the LM95234 will expect an SMBus Address address byte.

1.13 ONE-SHOT CONVERSION

The One-Shot register is used to initiate a round of conversions and comparisons when the device is in standby mode,

2.0 LM95234 Registers

after which the device returns to standby. This is not a data register and it is the write operation that causes the one-shot conversion. The data written to this address is irrelevant and is not stored. A zero will always be read from this register. All the channels that are enabled in the Channel Enable Register will be converted once and the $\overline{TCRIT1}$, $\overline{TCRIT2}$ and TCRIT3 pins will reflect the comparison results based on this round of conversion results of the channels that are not masked.

Command register selects which registers will be read from or written to. Data for this register should be transmitted during the Command Byte of the SMBus write communication.

P0-P7: Command

Register Summary

2.1 VALUE REGISTERS

For data synchronization purposes, the MSB register should be read first if the user wants to read both MSB and LSB registers. The LSB will be locked after the MSB is read. The LSB will be unlocked after being read. If the user reads MSBs consecutively, each time the MSB is read, the LSB associated with that temperature will be locked in and override the previous LSB value lockedin

2.1.1 Local Value Registers

2.1.2 Remote Temperature Value Registers with Signed Format

The Local temperature MSB value register range is +127°C to −128°C. The value programmed in this register is used to determine a local temperature error event.

2.1.3 Remote Temperature Value Registers with Unsigned Format

2.2 DIODE CONFIGURATION REGISTERS 2.2.1 Diode Model Select d
CM9
B2 2.2. Holde™odel Select
D2 2.2. Holde™odel Select

2.2.2 Remote 1-4 Offset

2.3 CONFIGURATION REGISTERS

2.3.1 Main Configuration Register

2.3.2 Conversion Rate Register

2.3.3 Channel Conversion Enable

When a conversion is disabled for a particular channel it is skipped. The continuous conversion rate is effected all other conversion rates are not effected as extra standby time is inserted in order to compensate. See Conversion Rate Register description.

2.3.4 Filter Setting

2.3.5 1-Shot

2.4 STATUS REGISTERS

2.4.1 Common Status Register

2.4.2 Status 1 Register (Diode Fault)

Status fault bits for open or shorted diode (i.e. Short Fault: D+ shorted to Ground or D-; Open Fault: D+ shorted to V_{DD}, or floating). During fault conditions the temperature reading is 0 °C if unsigned value registers are read or –128.000 °C if signed value registers are read.

LM95234

LM95234

2.4.3 Status 2 (TCRIT1)

Status bits for TCRITT. When one or more of these bits are set and if not masked the TCRITT output will activate. TCRITT will deactivate when all these bits are cleared.

Status bits for TCRIT2. When one or more of these bits are set and if not masked the TCRIT2 output will activate. TCRIT2 will deactivate when all these bits are cleared.

2.4.5 Status 4 (TCRIT3)

Status bits for TCRIT3. When one or more of these bits are set and if not masked the TCRIT3 output will activate. TCRIT3 will deactivate when all these bits are cleared.

2.4.6 Diode Model Status

2.5 MASK REGISTERS

2.5.1 TCRIT1 Mask Register

The mask bits in this register allow control over which error events propagate to the TCRIT1 pin.

2.5.2 TCRIT2 Mask Registers

2.5.3 TCRIT3 Mask Register d
2.5.3 TCBLT3 Mask Regis
2.5.3 TCBLT3 Mask Regist
2.5 The mask bits in this regist

The mask bits in this register allow control over which error events propagate to the TCRIT3 pin.

2.6 LIMIT REGISTERS

2.6.1 Local Limit Register

The Local Limit register range is 0°C to 127°C. The value programmed in this register is used to determine a local temperature error event.

2.6.2 Remote Limit Registers

The range for these registers is 0°C to 255°C.

Limit assignments for each TCRIT output pin:

2.6.3 Common Tcrit Hysteresis Register

The hysteresis register range is 0°C to 32°C. The value programmed in this register is used to modify all the limit values for decreasing temperature.

2.7 IDENTIFICATION REGISTERS

3.0 Applications Hints

The LM95234 can be applied easily in the same way as other
The LM95234 can be applied easily in the same way as other integrated-circuit temperature sensors, and its remote diode sensing capability allows it to be used in new ways as well. It can be soldered to a printed circuit board, and because the path of best thermal conductivity is between the die and the pins, its temperature will effectively be that of the printed circuit board lands and traces soldered to the LM95234's pins. This presumes that the ambient air temperature is almost the same as the surface temperature of the printed circuit board; if the air temperature is much higher or lower than the surface temperature, the actual temperature of the LM95234 die will be at an intermediate temperature between the surface and air temperatures. Again, the primary thermal conduction path is through the leads, so the circuit board temperature will contribute to the die temperature much more strongly than will the air temperature.

To measure temperature external to the LM95234's die, incorporates remote diode sensing technology. This diode can be located on the die of a target IC, allowing measurement of the IC's temperature, independent of the LM95234's temperature. A discrete diode can also be used to sense the temperature of external objects or ambient air. Remember that a discrete diode's temperature will be affected, and often dominated, by the temperature of its leads. Most silicon diodes do not lend themselves well to this application. It is recommended that an MMBT3904 transistor base emitter junction be used with the collector tied to the base.

The LM95234's TruTherm BJT beta compensation technology allows accurate sensing of integrated thermal diodes, such as those found on most processors. With TruTherm technology turned off, the LM95234 can measure a diode-connected transistor such as the MMBT3904 or the thermal diode found in an AMD processor.

The LM95234 has been optimized to measure the remote thermal diode integrated in a typical Intel processor on 65 nm or 90 nm process or an MMBT3904 transistor. Using the Remote Diode Model Select register any of the four remote inputs can be optimized for a typical Intel processor on 65 nm or 90 nm process or an MMBT3904.

3.1 DIODE NON-IDEALITY

3.1.1 Diode Non-Ideality Factor Effect on Accuracy

When a transistor is connected as a diode, the following relationship holds for variables V_{BE} , T and I_F:

$$
I_F = I_S \times \left[e^{\left(\frac{V_{BE}}{\eta \times V_t}\right)} - 1 \right]
$$
(1)

where:

$$
V_t = \frac{kT}{q}
$$

- q = 1.6×10⁻¹⁹ Coulombs (the electron charge),
- $T =$ Absolute Temperature in Kelvin
- k = 1.38×10−23 joules/K (Boltzmann's constant),
- n is the non-ideality factor of the process the diode is manufactured on,
- \bullet I_S = Saturation Current and is process dependent,
- I f = Forward Current through the base-emitter junction
- V_{BF} = Base-Emitter Voltage drop

In the active region, the -1 term is negligible and may be eliminated, yielding the following equation

$$
I_F = I_S \times \left[e^{\left(\frac{V_{BE}}{\eta \times V_t}\right)} \right]
$$
 (2)

In *Equation 2*, η and I_S are dependant upon the process that was used in the fabrication of the particular diode. By forcing two currents with a very controlled ratio(I_{F2} / I_{F1}) and measuring the resulting voltage difference, it is possible to eliminate the I_S term. Solving for the forward voltage difference yields the relationship:

$$
\Delta V_{BE} = \eta \times \left(\frac{kT}{q}\right) \times \ln\left(\frac{I_{F2}}{I_{F1}}\right)
$$
 (3)

Solving *Equation 3* for temperature yields:

$$
T = \frac{q \times \Delta V_{BE}}{\eta \times k \times \ln\left(\frac{I_{F2}}{I_{F1}}\right)}
$$
(4)

Equation 4 holds true when a diode connected transistor such as the MMBT3904 is used. When this "diode" equation is applied to an integrated diode such as a processor transistor with its collector tied to GND as shown in *Figure 9* it will yield a wide non-ideality spread. This wide non-ideality spread is not due to true process variation but due to the fact that *Equation 4* is an approximation.

TruTherm BJT beta compensation technology uses the transistor equation, *Equation 5*, which is a more accurate representation of the topology of the thermal diode found in an FPGA or processor.

$$
T = \frac{q \times \Delta V_{BE}}{\eta \times k \times \ln\left(\frac{I_{C2}}{I_{C1}}\right)}
$$
(5)

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FIGURE 9. Thermal Diode Current Paths

TruTherm should only be enabled when measuring the temperature of a transistor integrated as shown in the processor of *Figure 9*, because *Equation 5* only applies to this topology.

3.1.2 Calculating Total System Accuracy

The voltage seen by the LM95234 also includes the $\mathsf{I}_\mathsf{F}\mathsf{R}_\mathsf{S}$ voltage drop of the series resistance. The non-ideality factor, η, is the only other parameter not accounted for and depends on the diode that is used for measurement. Since ΔV_{BE} is proportional to both η and T, the variations in η cannot be distinguished from variations in temperature. Since the nonideality factor is not controlled by the temperature sensor, it will directly add to the inaccuracy of the sensor. For the for Intel processor on 65 nm process, Intel specifies a +4.06%/ −0.897% variation in η from part to part when the processor diode is measured by a circuit that assumes diode equation, *Equation 4*, as true. As an example, assume a temperature sensor has an accuracy specification of $\pm 1.0^{\circ}$ C at a temperature of 80°C (353 Kelvin) and the processor diode has a nonideality variation of +1.19%/−0.27%. The resulting system accuracy of the processor temperature being sensed will be:

$$
T_{ACC} = + 1.0\degree C + (+4.06\% \text{ of } 353 \text{ K}) = +15.3\degree C
$$

$$
T_{ACC}
$$
 = -1.0°C + (-0.89% of 353 K) = -4.1 °C

TrueTherm technology uses the transistor equation, *Equation 4*, resulting in a non-ideality spread that truly reflects the process variation which is very small. The transistor equation non-ideality spread is ±0.39% for the Intel processor on 90 nm process. The resulting accuracy when using TruTherm BJT beta compensation technology improves to:

$$
T_{ACC}
$$
 = ±0.75°C + (±0.39% of 353 K) = ± 2.16 °C

The next error term to be discussed is that due to the series resistance of the thermal diode and printed circuit board traces. The thermal diode series resistance is specified on most processor data sheets. For Intel processors in 65 nm process, this is specified at 4.52Ω typical. The LM95234 accommodates the typical series resistance of Intel Processor on 65 nm process. The error that is not accounted for is the spread of the processor's series resistance, that is 2.79Ω to 6.24 Ω or ±1.73 Ω . The equation to calculate the temperature error due to series resistance (T_{FB}) for the LM95234 is simply:

$$
T_{ER} = \left(0.62 \frac{^{\circ}C}{\Omega}\right) \times R_{PCB}
$$
 (6)

Solving *Equation 6* for R_{PCB} equal to ±1.73 Ω results in the additional error due to the spread in the series resistance of $±1.07^{\circ}$ C. The spread in error cannot be canceled out, as it would require measuring each individual thermal diode device. This is quite difficult and impractical in a large volume production environment.

Equation 6 can also be used to calculate the additional error caused by series resistance on the printed circuit board. Since the variation of the PCB series resistance is minimal, the bulk of the error term is always positive and can simply be cancelled out by subtracting it from the output readings of the LM95234.

3.1.3 Compensating for Different Non-Ideality

In order to compensate for the errors introduced by non-ideality, the temperature sensor is calibrated for a particular processor. National Semiconductor temperature sensors are always calibrated to the typical non-ideality and series resistance of a given processor type. The LM95234 is calibrated for two non-ideality factors and series resistance values thus supporting the MMBT3904 transistor and Intel processors on 65 nm process without the requirement for additional trims. For most accurate measurements TruTherm BJT beta compensation mode should be turned on when measuring the Intel processor on 65 nm process to minimize the error introduced by the false non-ideality spread (see *3.1.1 Diode Non-Ideality Factor Effect on Accuracy*). When a temperature sensor calibrated for a particular processor type is used with a different processor type, additional errors are introduced.

Temperature errors associated with non-ideality of different processor types may be reduced in a specific temperature range of concern through use of software calibration. Typical Non-ideality specification differences cause a gain variation of the transfer function, therefore the center of the temperature range of interest should be the target temperature for calibration purposes. The following equation can be used to calculate the temperature correction factor (T_{CF}) required to compensate for a target non-ideality differing from that supported by the LM95234.

$$
T_{CF} = \left(\frac{\eta_s - \eta_{PROCESOR}}{\eta_s}\right) \times (T_{CR} + 273K)
$$
 (7)

where

- \bullet η _S = LM95234 non-ideality for accuracy specification
- $n_{PROCESOR}$ = Processor thermal diode typical non-ideality
- T_{CR} = center of the temperature range of interest in $°C$

The correction factor should be directly added to the temperature reading produced by the LM95234. For example when using the LM95234, with the 3904 mode selected, to measure a AMD Athlon processor, with a typical non-ideality of 1.008, for a temperature range of 60 °C to 100 °C the correction factor would calculate to:

$$
T_{\text{CF}} = \left(\frac{1.003 - 1.008}{1.003}\right) \cdot (80 + 273) = -1.75 \text{°C}
$$

Therefore, 1.75°C should be subtracted from the temperature readings of the LM95234 to compensate for the differing typical non-ideality target.

3.2 PCB LAYOUT FOR MINIMIZING NOISE

FIGURE 10. Ideal Diode Trace Layout

In a noisy environment, such as a processor mother board, layout considerations are very critical. Noise induced on traces running between the remote temperature diode sensor and the LM95234 can cause temperature conversion errors. Keep in mind that the signal level the LM95234 is trying to measure is in microvolts. The following guidelines should be followed:

- 1. V_{DD} should be bypassed with a 0.1 µF capacitor in parallel with 100 pF. The 100 pF capacitor should be placed as close as possible to the power supply pin. A bulk capacitance of approximately 10 µF needs to be in the near vicinity of the LM95234.
- 2. A 100 pF diode bypass capacitor is recommended to filter high frequency noise but may not be necessary. Make sure the traces to the 100 pF capacitor are matched. Place the filter capacitors close to the LM95234 pins.
- Ideally, the LM95234 should be placed within 10 cm of the Processor diode pins with the traces being as straight, short and identical as possible. Trace resistance of 1Ω can cause as much as 0.62°C of error. This error can be compensated by using simple software offset compensation.
- 4. Diode traces should be surrounded by a GND guard ring to either side, above and below if possible. This GND guard should not be between the D+ and D− lines. In the event that noise does couple to the diode lines it would be ideal if it is coupled common mode. That is equally to the D+ and D− lines.
- 5. Avoid routing diode traces in close proximity to power supply switching or filtering inductors.
- 6. Avoid running diode traces close to or parallel to high speed digital and bus lines. Diode traces should be kept at least 2 cm apart from the high speed digital traces.
- 7. If it is necessary to cross high speed digital traces, the diode traces and the high speed digital traces should cross at a 90 degree angle.
- 8. The ideal place to connect the LM95234's GND pin is as close as possible to the Processors GND associated with the sense diode.
- 9. Leakage current between D+ and GND and between D+ and D− should be kept to a minimum. Thirteen nanoamperes of leakage can cause as much as 0.2°C of error in the diode temperature reading. Keeping the printed circuit board as clean as possible will minimize leakage current.

Noise coupling into the digital lines greater than 400 mVp-p (typical hysteresis) and undershoot less than 500 mV below GND, may prevent successful SMBus communication with the LM95234. SMBus no acknowledge is the most common symptom, causing unnecessary traffic on the bus. Although

(8)

the SMBus maximum frequency of communication is rather low (100 kHz max), care still needs to be taken to ensure the SMBus maximum frequency of communication is rather

lowr **to a structure** of the system with multiple parts on the
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bushing a situation within a bus and long printed circuit board traces. An RC lowpass filter with a 3 dB corner frequency of about 40 MHz is included on the LM95234's SMBCLK input. Additional resistance can be

added in series with the SMBDAT and SMBCLK lines to further help filter noise and ringing. Minimize noise coupling by keeping digital traces out of switching power supply areas as well as ensuring that digital lines containing high speed data communications cross at right angles to the SMBDAT and SMBCLK lines.

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Notes

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