

Power MOSFET

6 Amps, 30 Volts
N-Channel SO-8 FETKY™

The FETKY product family incorporates low $R_{DS(on)}$ MOSFETs packaged with an industry leading, low forward drop, low leakage Schottky Barrier rectifier to offer high efficiency components in a space saving configuration. Independent pinouts for MOSFET and Schottky die allow the flexibility to use a single component for switching and rectification functions in a wide variety of applications.

Features

- Pb-Free Packages are Available

Applications

- Buck Converter
- Buck-Boost
- Synchronous Rectification
- Low Voltage Motor Control
- Battery Packs
- Chargers
- Cell Phones

MOSFET MAXIMUM RATINGS

($T_J = 25^\circ\text{C}$ unless otherwise noted) (Note 1)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	30	Vdc
Drain-to-Gate Voltage ($R_{GS} = 1.0\text{ M}\Omega$)	V_{DGR}	30	Vdc
Gate-to-Source Voltage – Continuous	V_{GS}	± 20	Vdc
Drain Current – (Note 2)			
– Continuous @ $T_A = 25^\circ\text{C}$	I_D	6.0	Adc
– Single Pulse ($t_p \leq 10\text{ }\mu\text{s}$)	I_{DM}	30	Apk
Total Power Dissipation @ $T_A = 25^\circ\text{C}$ (Note 2)	P_D	2.0	Watts
Single Pulse Drain-to-Source Avalanche Energy – Starting $T_J = 25^\circ\text{C}$ ($V_{DD} = 30\text{ Vdc}$, $V_{GS} = 5.0\text{ Vdc}$, $V_{DS} = 20\text{ Vdc}$, $I_L = 9.0\text{ Apk}$, $L = 10\text{ mH}$, $R_G = 25\text{ }\Omega$)	E_{AS}	325	mJ

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

- Pulse Test: Pulse Width $\leq 250\text{ }\mu\text{s}$, Duty Cycle $\leq 2.0\%$.
- Mounted on 2" square FR4 board
(1 in sq, 2 oz. Cu 0.06" thick single sided), 10 sec. max.

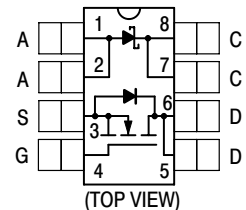


ON Semiconductor®

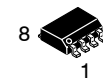
<http://onsemi.com>

MOSFET
6.0 AMPERES
30 VOLTS
24 m Ω @ $V_{GS} = 10\text{ V}$ (Typ)

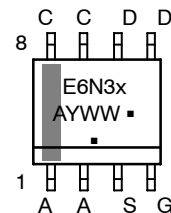
SCHOTTKY DIODE
6.0 AMPERES
30 VOLTS
420 mV @ $I_F = 3.0\text{ A}$



MARKING DIAGRAM & PIN ASSIGNMENT



SO-8
CASE 751
STYLE 18



E6N3 = Device Code
x = Blank or S
A = Assembly Location
Y = Year
WW = Work Week
▪ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
NTMSD6N303R2	SO-8	2500/Tape & Reel
NTMSD6N303R2G	SO-8 (Pb-Free)	2500/Tape & Reel
NTMSD6N303R2SG	SO-8 (Pb-Free)	2500/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

NTMSD6N303R2

SCHOTTKY RECTIFIER ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage DC Blocking Voltage	V _{RRM} V _R	30	Volts
Average Forward Current (Note 3) (Rated V _R) T _A = 104°C	I _O	2.0	Amps
Peak Repetitive Forward Current (Note 3) (Rated V _R , Square Wave, 20 kHz) T _A = 108°C	I _{frm}	4.0	Amps
Non-Repetitive Peak Surge Current (Surge applied at rated load conditions, half-wave, single phase, 60 Hz)	I _{fsm}	30	Amps

THERMAL CHARACTERISTICS – SCHOTTKY AND MOSFET

Thermal Resistance – Junction-to-Ambient (Note 4) – MOSFET	R _{θJA}	167	°C/W
Thermal Resistance – Junction-to-Ambient (Note 5) – MOSFET	R _{θJA}	97	
Thermal Resistance – Junction-to-Ambient (Note 3) – MOSFET	R _{θJA}	62.5	
Thermal Resistance – Junction-to-Ambient (Note 4) – Schottky	R _{θJA}	197	
Thermal Resistance – Junction-to-Ambient (Note 5) – Schottky	R _{θJA}	97	
Thermal Resistance – Junction-to-Ambient (Note 3) – Schottky	R _{θJA}	62.5	
Operating and Storage Temperature Range	T _J , T _{stg}	–55 to +150	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

3. Mounted on 2" square FR4 board (1 in sq, 2 oz. Cu 0.06" thick single sided), 10 sec. max.

4. Mounted with minimum recommended pad size, PC Board FR4.

5. Mounted on 2" square FR4 board (1 in sq, 2 oz. Cu 0.06" thick single sided), Steady State.

SCHOTTKY RECTIFIER ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristics	Symbol	Value		Unit
Maximum Instantaneous Forward Voltage (Note 6) I _F = 100 mAdc I _F = 3.0 Adc I _F = 6.0 Adc	V _F	T _J = 25°C	T _J = 125°C	Volts
		0.28	0.13	
		0.42	0.33	
		0.50	0.45	
Maximum Instantaneous Reverse Current (Note 6) V _R = 30 V	I _R	T _J = 25°C	T _J = 125°C	μA mA
		250 –	– 25	
Maximum Voltage Rate of Change V _R = 30 V	dV/dt	10,000		V/μs

6. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%

NTMSD6N303R2

MOSFET ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

查看 NTMSD6N303R2-D 数据手册

Characteristic	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Drain-to-Source Breakdown Voltage (V _{GS} = 0 Vdc, I _D = 250 μA)	V _{(BR)DSS}	30	–	–	Vdc
Temperature Coefficient (Positive)		–	30	–	mV/°C
Zero Gate Voltage Drain Current (V _{DS} = 24 Vdc, V _{GS} = 0 Vdc, T _J = 25°C)	I _{DSS}	–	–	1.0	μAdc
(V _{DS} = 24 Vdc, V _{GS} = 0 Vdc, T _J = 125°C)		–	–	20	
Gate-Body Leakage Current (V _{GS} = ±20 Vdc, V _{DS} = 0 Vdc)	I _{GSS}	–	–	100	nAdc

ON CHARACTERISTICS (Note 7)

Gate Threshold Voltage (V _{DS} = V _{GS} , I _D = 250 μAdc)	V _{GS(th)}	1.0	1.8	2.5	Vdc
Temperature Coefficient (Negative)		–	4.6	–	mV/°C
Static Drain-to-Source On-State Resistance (V _{GS} = 10 Vdc, I _D = 6 Adc)	R _{DS(on)}	–	0.024	0.032	Ω
(V _{GS} = 4.5 Vdc, I _D = 3.9 Adc)		–	0.030	0.040	
Forward Transconductance (V _{DS} = 15 Vdc, I _D = 5.0 Adc)	g _{FS}	–	10	–	Mhos

DYNAMIC CHARACTERISTICS

Input Capacitance	(V _{DS} = 24 Vdc, V _{GS} = 0 Vdc, f = 1.0 MHz)	C _{iss}	–	680	950	pF
Output Capacitance		C _{oss}	–	210	300	
Reverse Transfer Capacitance		C _{rss}	–	70	135	

SWITCHING CHARACTERISTICS (Notes 7 & 8)

Turn-On Delay Time	(V _{DD} = 15 Vdc, I _D = 1 A, V _{GS} = 10 V, R _G = 6 Ω)	t _{d(on)}	–	9	18	ns
Rise Time		t _r	–	22	40	
Turn-Off Delay Time		t _{d(off)}	–	45	80	
Fall Time		t _f	–	45	80	
Turn-On Delay Time	(V _{DD} = 15 Vdc, I _D = 1 A, V _{GS} = 4.5 V, R _G = 6 Ω)	t _{d(on)}	–	13	30	ns
Rise Time		t _r	–	27	50	
Turn-Off Delay Time		t _{d(off)}	–	22	40	
Fall Time		t _f	–	34	70	
Gate Charge	(V _{DS} = 15 Vdc, V _{GS} = 10 Vdc, I _D = 5 A)	Q _T	–	19	30	nC
		Q ₁	–	2.4	–	
		Q ₂	–	5.0	–	
		Q ₃	–	4.3	–	

BODY-DRAIN DIODE RATINGS (Note 7)

Diode Forward On-Voltage	(I _S = 1.7 Adc, V _{GS} = 0 V) (I _S = 1.7 Adc, V _{GS} = 0 V, T _J = 150°C)	V _{SD}	–	0.75	1.0	Vdc
			–	0.62	–	
Reverse Recovery Time	(I _S = 5 A, V _{GS} = 0 V, dI _S /dt = 100 A/μs)	t _{rr}	–	26	–	ns
		t _a	–	11	–	
		t _b	–	15	–	
Reverse Recovery Stored Charge (I _S = 5 A, dI _S /dt = 100 A/μs, V _{GS} = 0 V)		Q _{RR}	–	0.015	–	μC

7. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.

8. Switching characteristics are independent of operating junction temperature.

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TYPICAL MOSFET ELECTRICAL CHARACTERISTICS

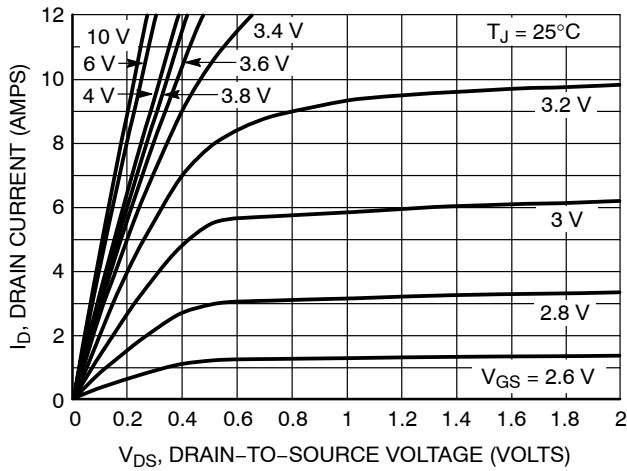


Figure 1. On-Region Characteristics

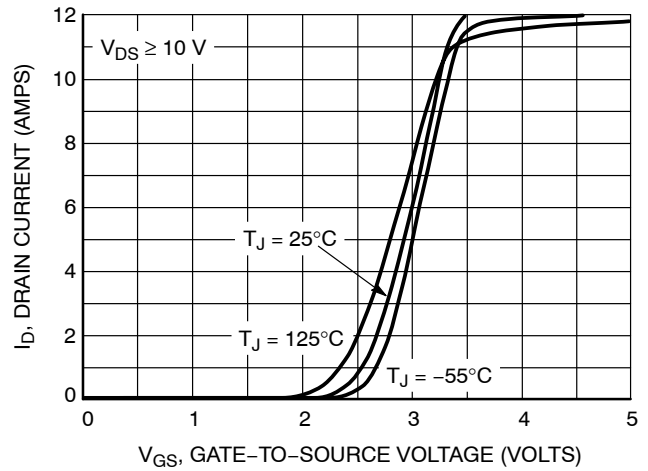


Figure 2. Transfer Characteristics

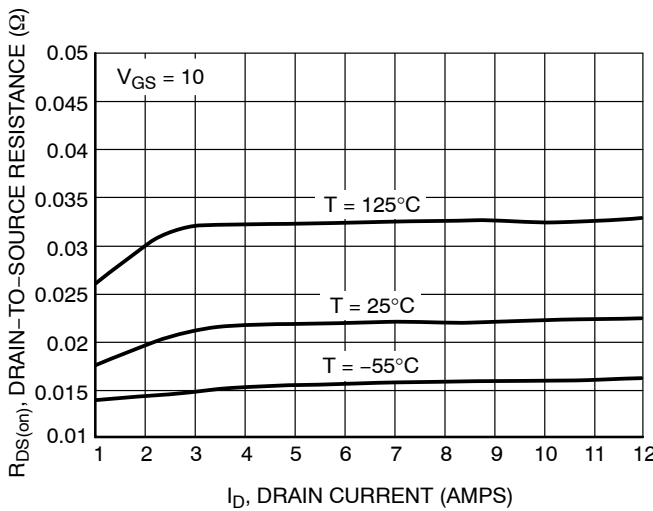


Figure 3. On-Resistance versus Drain Current and Temperature

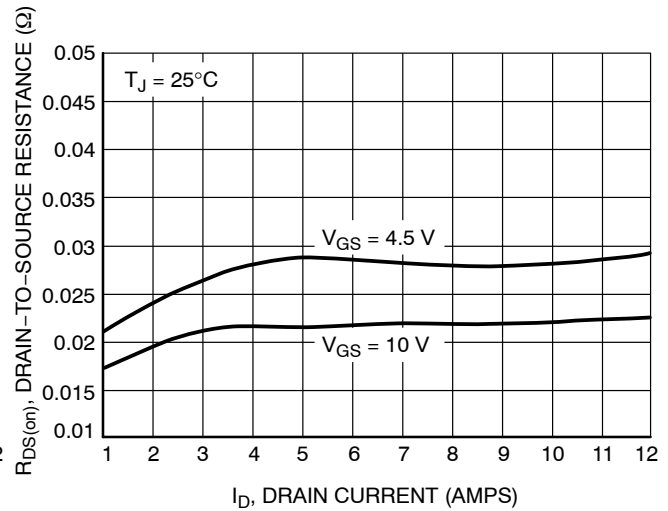


Figure 4. On-Resistance versus Drain Current and Gate Voltage

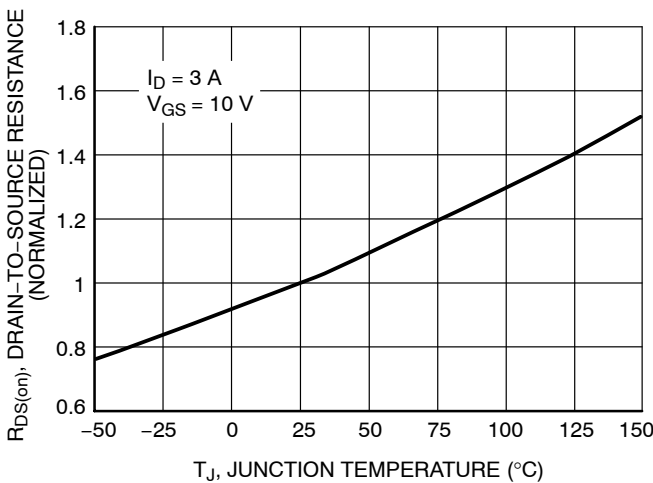


Figure 5. On-Resistance Variation with Temperature

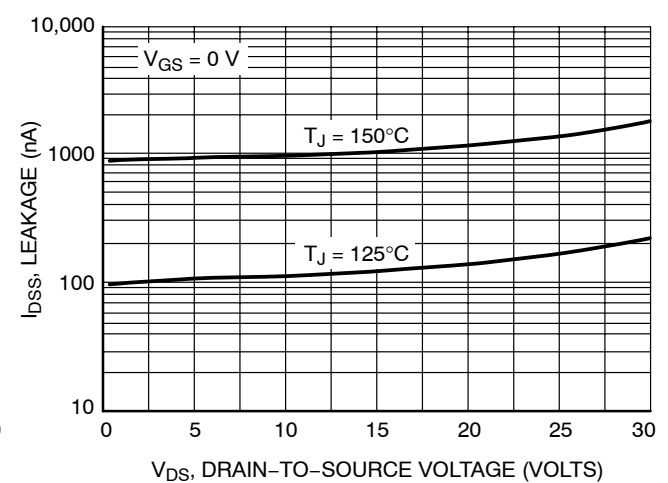
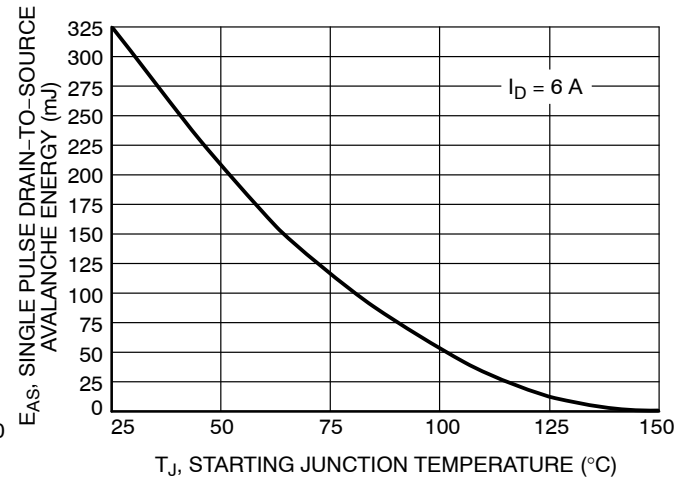
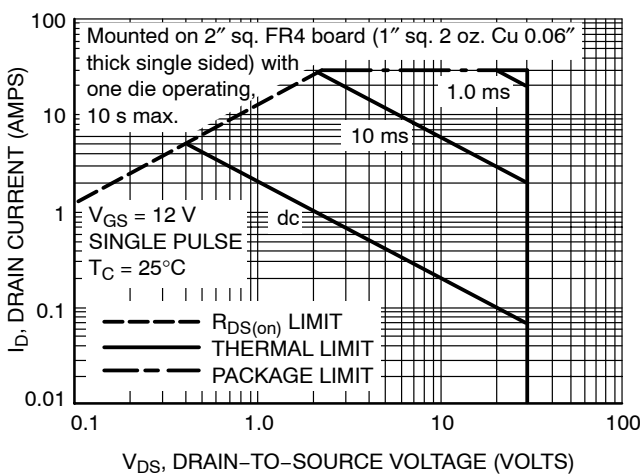
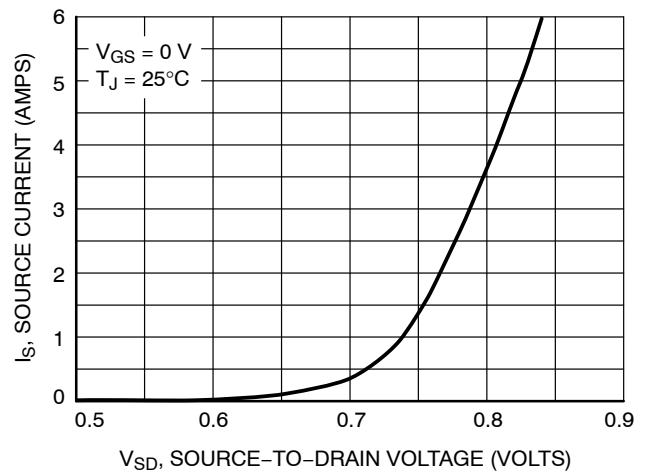
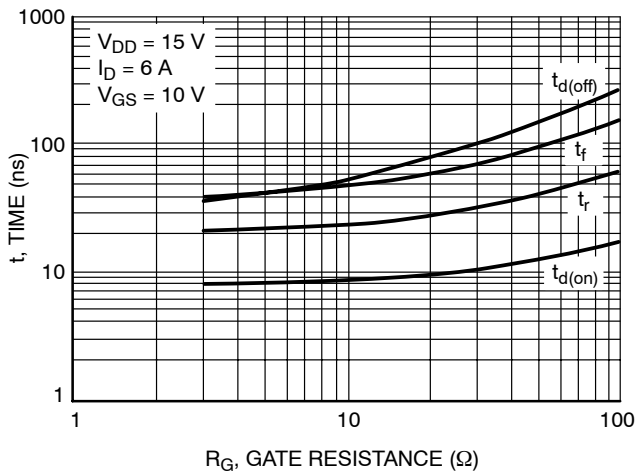
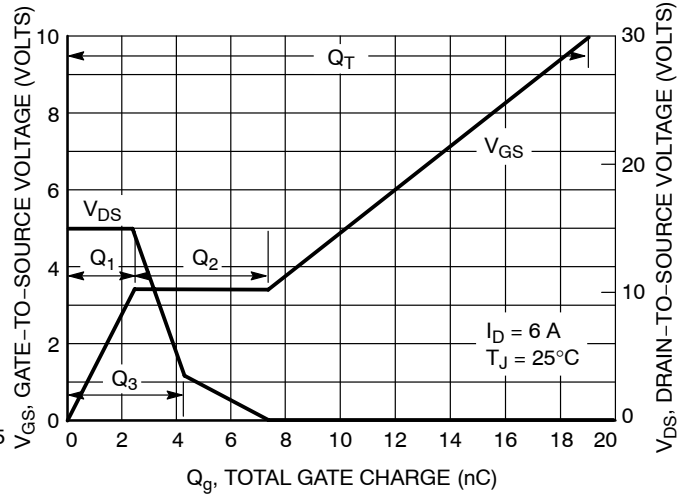
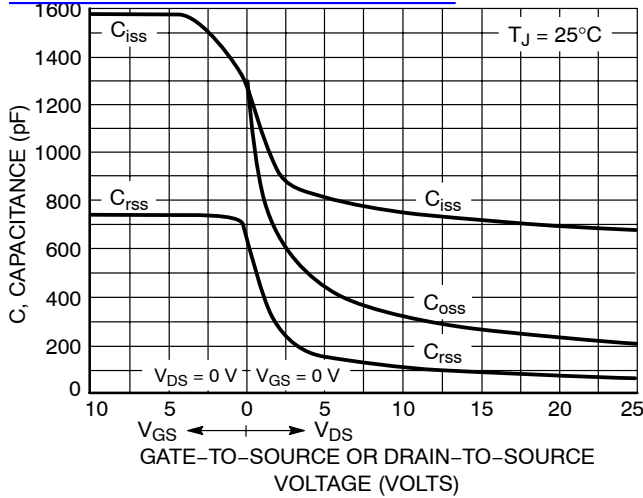


Figure 6. Drain-to-Source Leakage Current versus Voltage

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TYPICAL FET ELECTRICAL CHARACTERISTICS

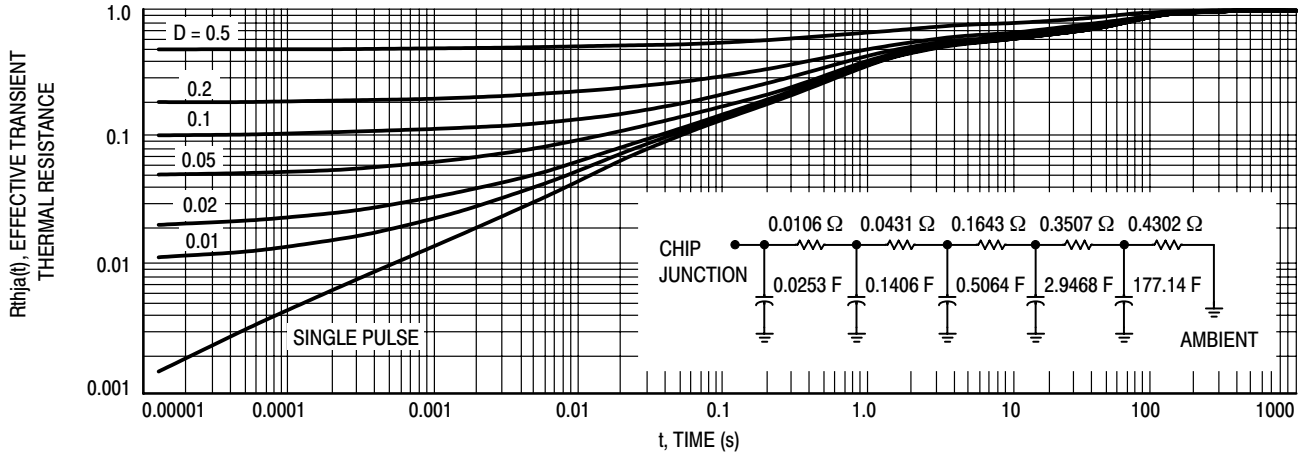


Figure 13. FET Thermal Response

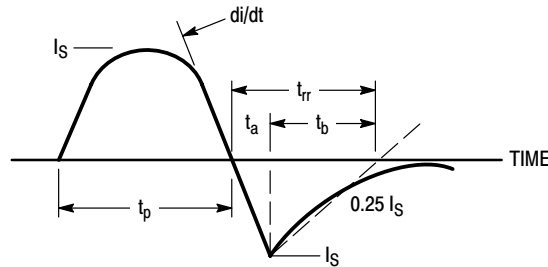


Figure 14. Diode Reverse Recovery Waveform

TYPICAL SCHOTTKY ELECTRICAL CHARACTERISTICS

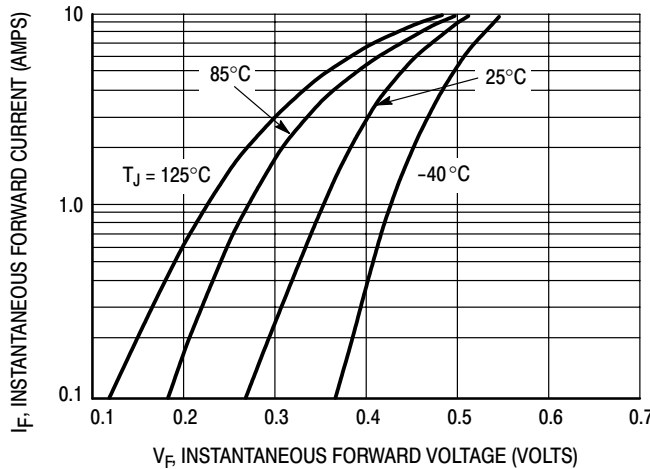


Figure 15. Typical Forward Voltage

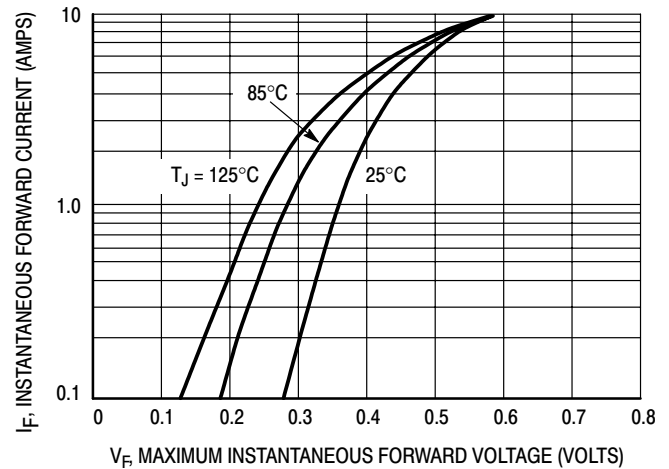


Figure 16. Maximum Forward Voltage

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TYPICAL SCHOTTKY ELECTRICAL CHARACTERISTICS

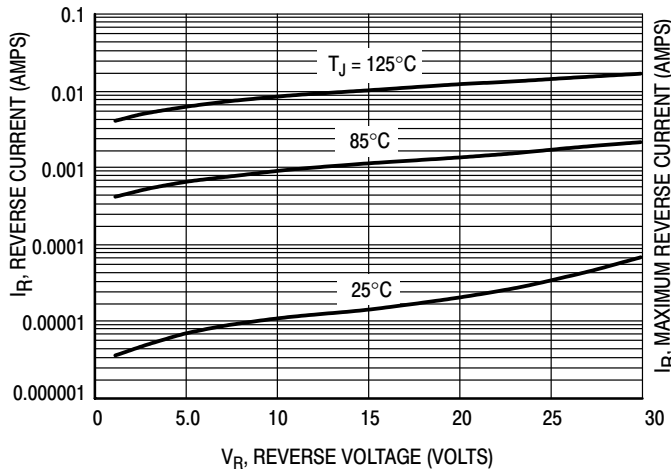


Figure 17. Typical Reverse Current

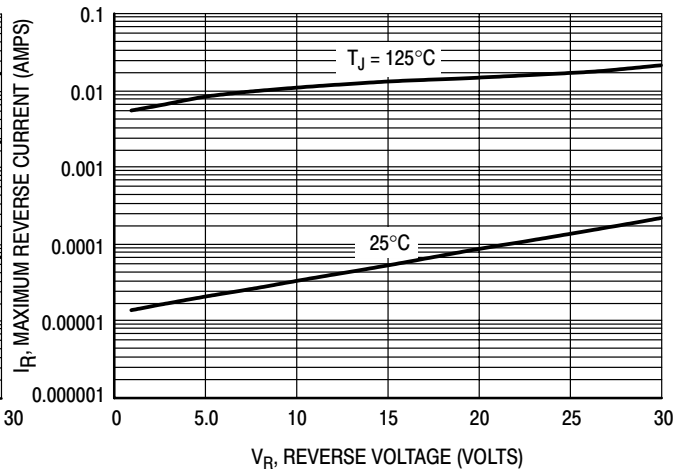


Figure 18. Maximum Reverse Current

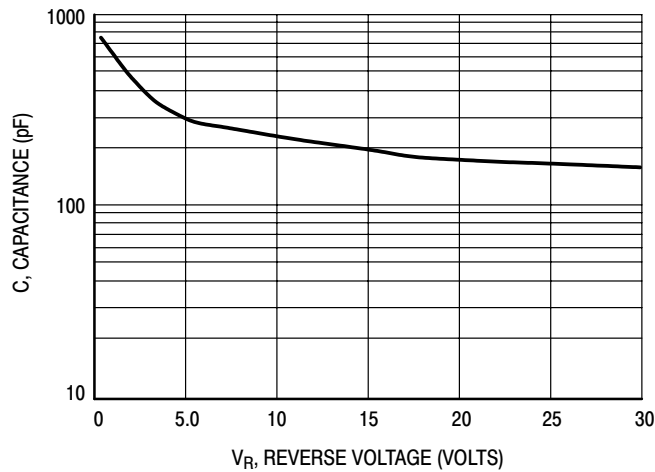


Figure 19. Typical Capacitance

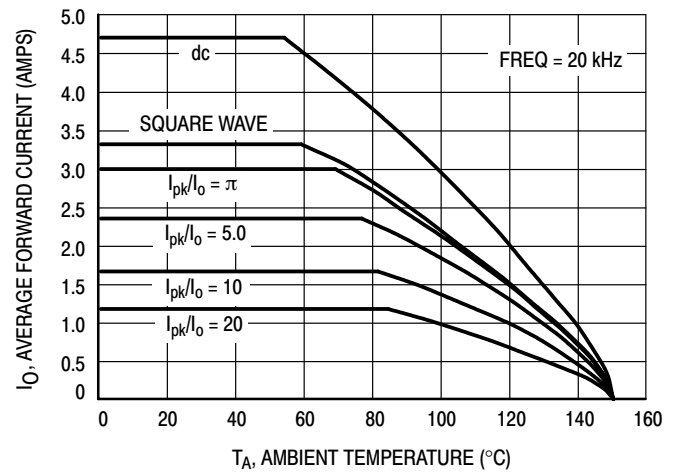


Figure 20. Current Derating

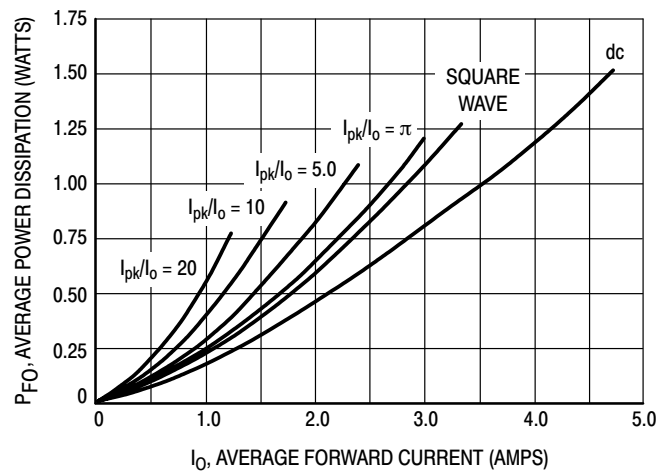


Figure 21. Forward Power Dissipation

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TYPICAL SCHOTTKY ELECTRICAL CHARACTERISTICS

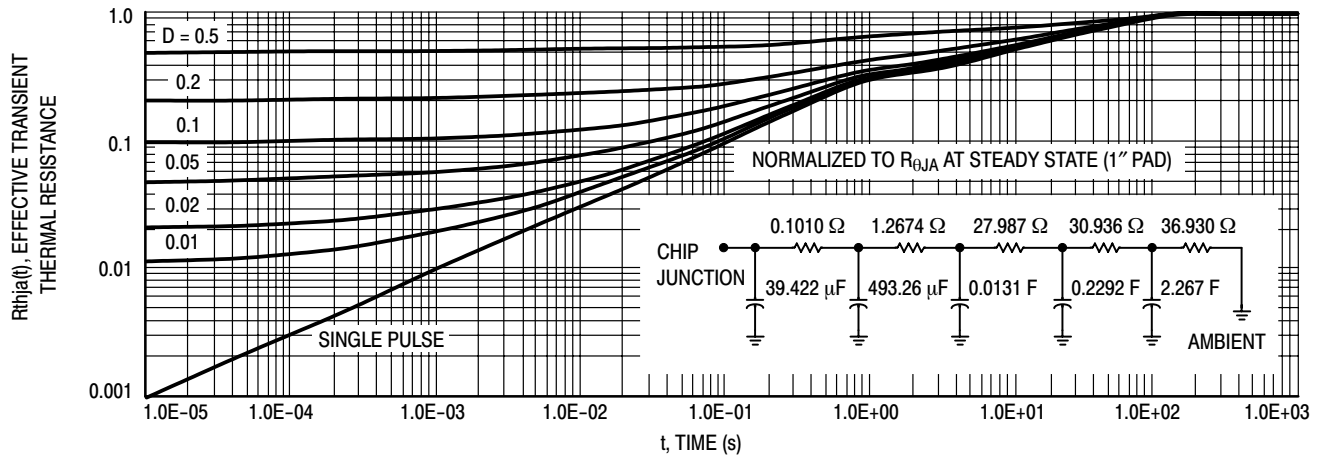
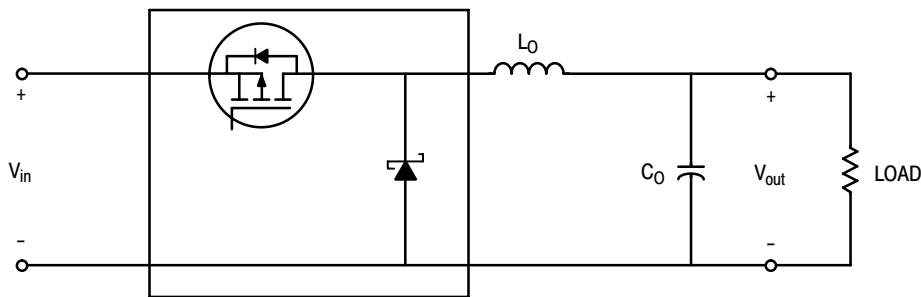


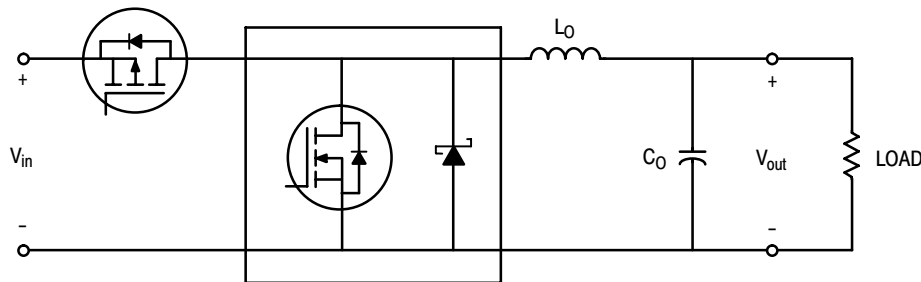
Figure 22. Schottky Thermal Response

TYPICAL APPLICATIONS

STEP DOWN SWITCHING REGULATORS

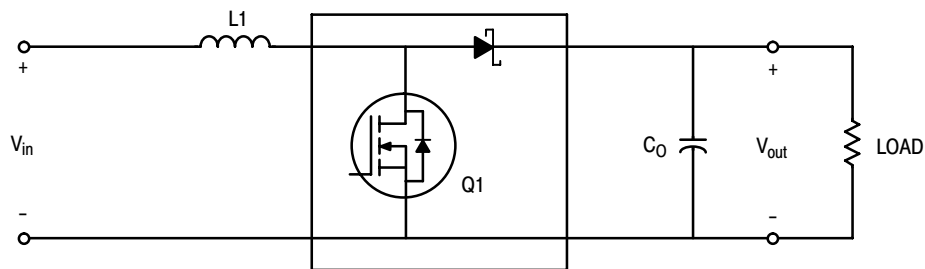


Buck Regulator

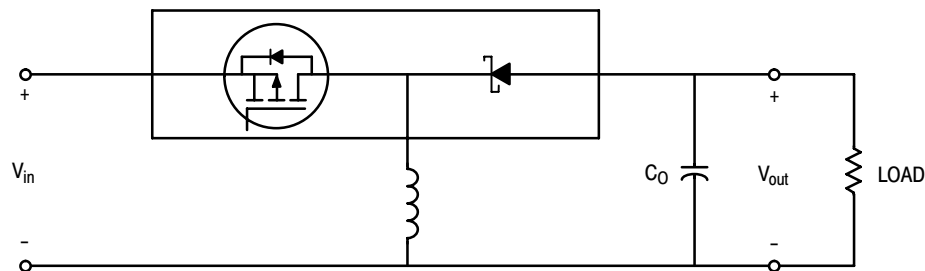


Synchronous Buck Regulator

STEP UP SWITCHING REGULATORS

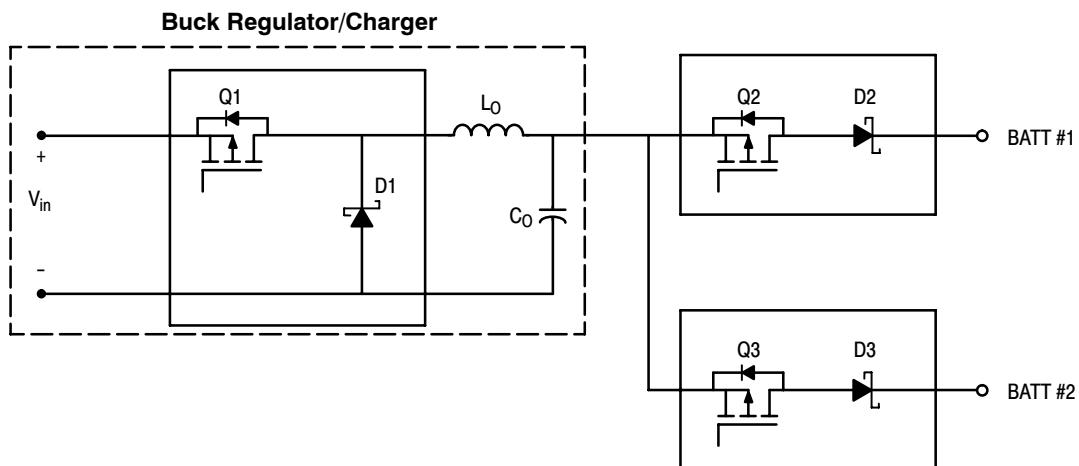


Boost Regulator



Buck-Boost Regulator

MULTIPLE BATTERY CHARGERS

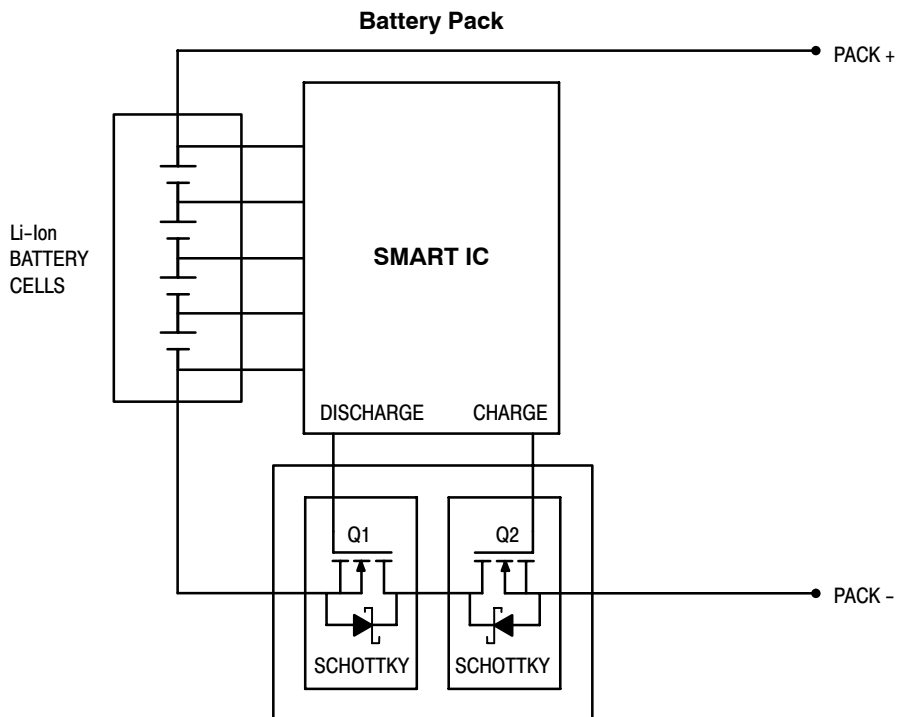


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TYPICAL APPLICATIONS

Li-Ion BATTERY PACK APPLICATIONS



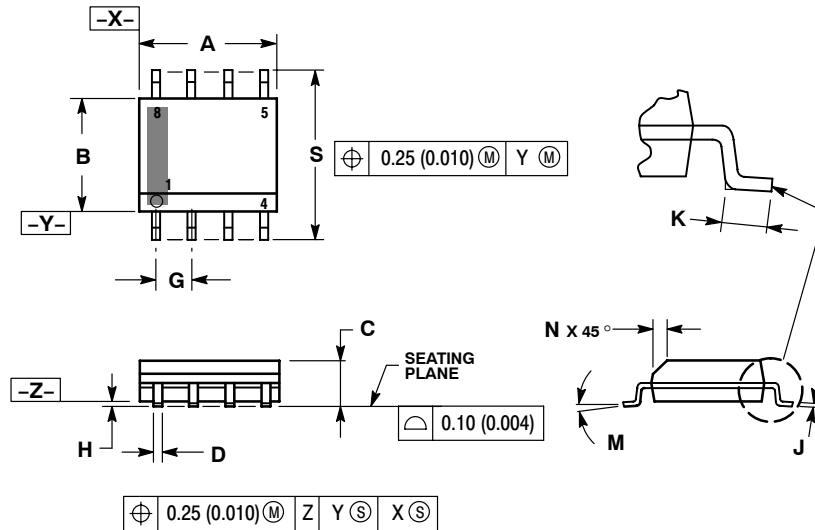
- Applicable in battery packs which require a high current level.
- During charge cycle Q2 is on and Q1 is off. Schottky can reduce power loss during fast charge.
- During discharge Q1 is on and Q2 is off. Again, Schottky can reduce power dissipation.
- Under normal operation, both transistors are on.

NTMSD6N303R2

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PACKAGE DIMENSIONS

SOIC-8 NB
CASE 751-07
ISSUE AG



NOTES:

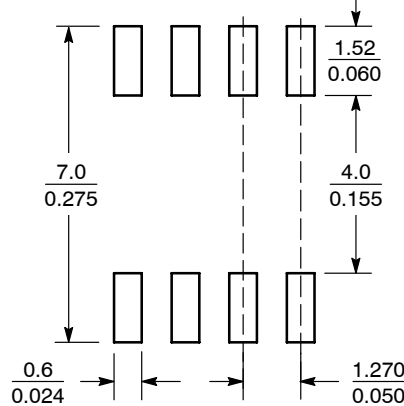
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.
6. 751-01 THRU 751-06 ARE OBSOLETE. NEW STANDARD IS 751-07.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.80	5.00	0.189	0.197
B	3.80	4.00	0.150	0.157
C	1.35	1.75	0.053	0.069
D	0.33	0.51	0.013	0.020
G	1.27 BSC		0.050 BSC	
H	0.10	0.25	0.004	0.010
J	0.19	0.25	0.007	0.010
K	0.40	1.27	0.016	0.050
M	0°	8°	0°	8°
N	0.25	0.50	0.010	0.020
S	5.80	6.20	0.228	0.244

STYLE 18:

- PIN 1. ANODE
- ANODE
- SOURCE
- GATE
- DRAIN
- DRAIN
- CATHODE
- CATHODE

SOLDERING FOOTPRINT*



SCALE 6:1 $\left(\frac{\text{mm}}{\text{inches}} \right)$

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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