

查询"5962-88550012A"供应商

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5962-E1050

DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part number. The complete part number shall be as shown in the following example:

5962-88550	01	R	X
-----	-----	-----	-----
Drawing number	Device type (1.2.1)	Case outline (1.2.2)	Lead finish per MIL-M-38510

1.2.1 Device type. The device type shall identify the circuit function as follows:

Device type	Generic number	Circuit function
01	54F273	Octal edge-triggered D-type flip-flop with asynchronous master reset

1.2.2 Case outlines. The case outlines shall be as designated in appendix C of MIL-M-38510, and as follows:

Outline letter	Case outline
R	D-8 (20-lead, 1.060" x .310" x .200"), dual-in-line package
S	F-9 (20-lead, .540" x .300" x .100"), flat package
2	C-2 (20-terminal, .358" x .358" x .100"), square chip carrier package

1.3 Absolute maximum ratings

Supply voltage range	- - - - -	-0.5 V dc to +7.0 V dc
Input voltage range	- - - - -	-0.5 V dc to +7.0 V dc
Maximum power dissipation (P _D) 1/	- - - - -	484 mW
Storage temperature range	- - - - -	-65°C to +150°C
Lead temperature (soldering, 10 seconds)	- - - - -	+300°C
Thermal resistance, junction-to-case (θ _{JC})	- - - - -	See MIL-M-38510, appendix C
Junction temperature (T _J)	- - - - -	+175°C

1/ Must withstand the added P_D due to short-circuit test; e.g., I_{OS}.

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1.4 Recommended operating conditions.

Supply voltage (V_{CC})	- - - - -	+4.5 V dc to +5.5 V dc
Case operating temperature range (T_C)	- - - - -	-55°C to +125°C
Minimum high level input voltage (V_{IH})	- - - - -	2.0 V dc
Maximum low level input voltage (V_{IL})	- - - - -	0.8 V dc
Minimum setup time (t_s):		
high or low, DATA to CP	- - - - -	4 ns
Minimum hold time (t_h):		
high or low, DATA to CP	- - - - -	1 ns
Minimum clock pulse width:		
low, CP (tp_{W1})	- - - - -	6 ns
high or low, MR(tp_{W2})	- - - - -	6 ns
Minimum recovery time(t_{REC}):		
MR to CP	- - - - -	9 ns

2. APPLICABLE DOCUMENTS

2.1 Government specification and standard. Unless otherwise specified, the following specification and standard, of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

(Copies of the specification and standard required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.2 Truth table. The truth table shall be as specified on figure 2.

3.2.3 Test circuit and switching waveforms. The test circuit and switching waveforms shall be as specified on figure 3.

3.2.4 Case outlines. The case outlines shall be in accordance with 1.2.2 herein.

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TABLE I. Electrical performance characteristics.

Test	Symbol	Conditions $-55^{\circ}\text{C} < T_C < +125^{\circ}\text{C}$ unless otherwise specified	Group A subgroups	Limits		Unit
				Min	Max	
High level output voltage	V_{OH}	$V_{CC} = 4.5 \text{ V}, I_{OH} = -1 \text{ mA}$ $V_{IH} = 2.0 \text{ V}, V_{IL} = 0.8 \text{ V}$	1,2,3	2.5		V
Low level output voltage	V_{OL}	$V_{CC} = 4.5 \text{ V}, I_{OL} = 20 \text{ mA}$ $V_{IH} = 2.0 \text{ V}, V_{IL} = 0.8 \text{ V}$	1,2,3		0.5	V
Input clamp voltage	V_{IC}	$V_{CC} = 4.5 \text{ V}, I_{IN} = -18 \text{ mA}$	1,2,3		-1.2	V
Low level input current	I_{IL}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.5 \text{ V}$ Inputs not under test $> 4.5 \text{ V}$	1,2,3		-0.6	mA
High level input current	I_{IH}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 2.7 \text{ V}$ Inputs not under test $> 4.5 \text{ V}$	1,2,3		20	μA
Output short circuit current	I_{OS}	$V_{CC} = 5.5 \text{ V}, V_{OUT} = 0.0 \text{ V}$ ^{1/}	1,2,3	-60	-150	mA
Power supply current	I_{CCH}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 4.5 \text{ V}$	1,2,3		85	mA
	I_{CCL}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.0 \text{ V}$			88	
Functional testing		See 4.3.1c	7, 8			
Propagation delay time, CP to Qn	t_{PLH1}	$V_{CC} = 5.0 \text{ V}$ $C_L = 50 \text{ pF}$ $R_L = 500\Omega$ ^{2/} See Figure 3	9,10,11	3.5	11.5	ns
	t_{PHL1}			3.0	12	
Propagation delay time, MR to Qn	t_{PHL2}			3.0	12	
Maximum clock frequency ^{3/}	f_{MAX}		9	100		MHz

^{1/} Not more than one output should be shorted at a time and the duration of the short circuit condition should not exceed 1 second.

^{2/} Propagation delay limits are based on single output switching. Unused outputs = 3.5 V or $\leq 0.3 \text{ V}$.

^{3/} f_{MAX} is guaranteed, but not tested, at $V_{CC} = 5.0 \text{ V}$, $T_C = -55^{\circ}\text{C}$ and $T_C = +125^{\circ}\text{C}$, to the limits specified.

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Cases R, S, and 2	
Terminal number	Terminal symbol
1	\overline{MR}
2	Q0
3	D0
4	D1
5	Q1
6	Q2
7	D2
8	D3
9	Q3
10	GND
11	CP
12	Q4
13	D4
14	D5
15	Q5
16	Q6
17	D6
18	D7
19	Q7
20	VCC

Connection	Description
Q0 - Q7	Data outputs
D0 - D7	Data inputs
CP	Clock pulse input (Active rising edge)
\overline{MR}	Master reset(active low)

FIGURE 1. Terminal connections.

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Operating mode	Inputs			Outputs
	$\overline{\text{MR}}$	CP	Dn	Qn
Reset (clear)	L	X	X	L
Load (1)	H	\uparrow	h	H
Load (0)	H	\uparrow	1	L

H = High voltage level steady-state
 h = High voltage level one setup time prior to the low-to-high clock transition
 L = Low voltage level steady-state
 1 = Low voltage level one setup time prior to the low-to-high clock transition
 X = irrelevant
 \uparrow = Low-to-High clock transition

FIGURE 2. Truth table.

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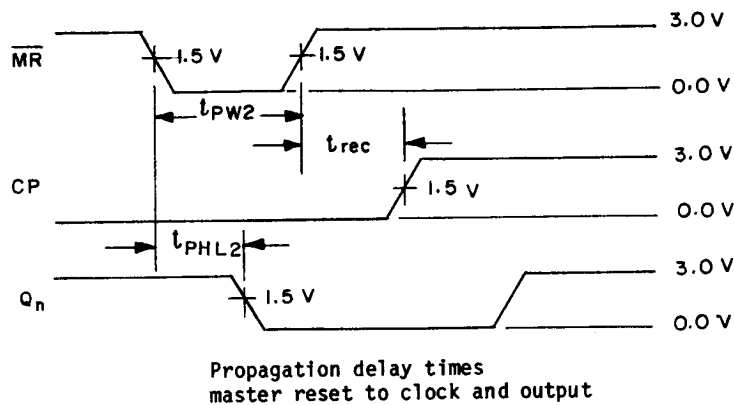
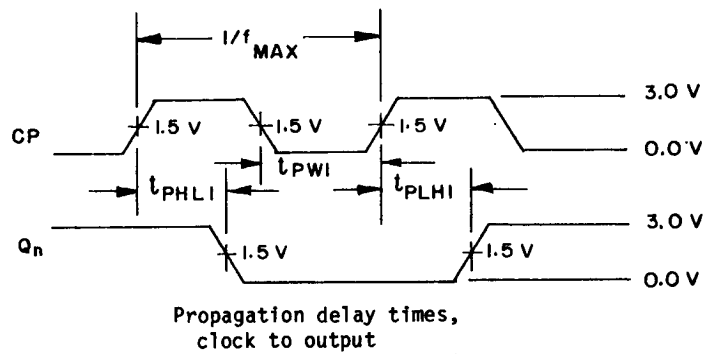
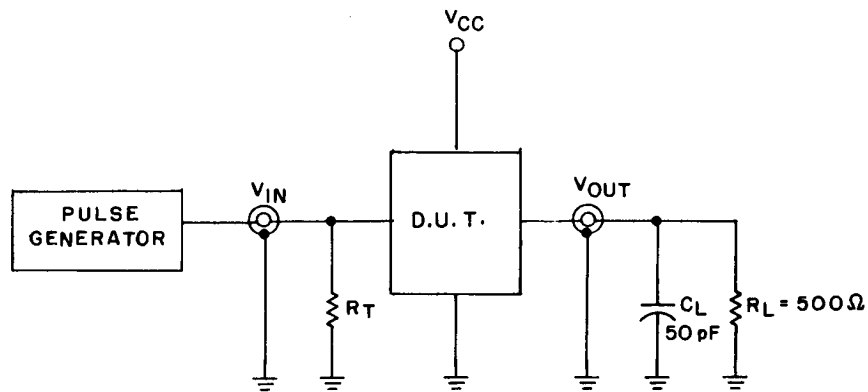
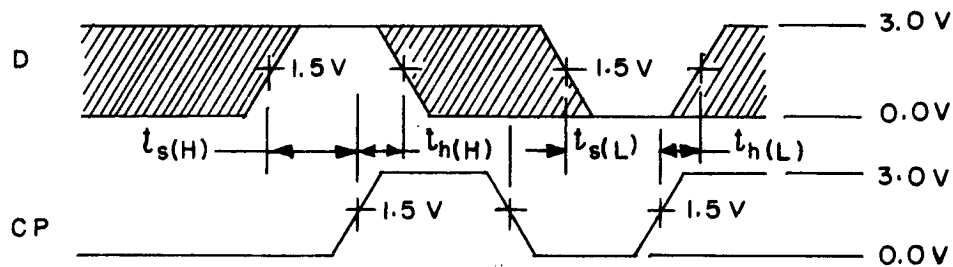


FIGURE 3. Test circuit and switching waveforms.

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Data set up and hold times.

NOTES:

1. C_L includes probe and jig capacitance.
2. Input pulse characteristics: PRR = 1 MHz, $t_{TLH} = t_{THL} = 2.5$ ns, duty cycle = 50%.
3. $R_T = Z_{OUT}$ of pulse generators.
4. The shaded areas indicate when the input is permitted to change for predictable output performance.

FIGURE 3. Test circuit and switching waveforms. - Continued.

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3.3 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I and apply over the full case operating temperature range.

3.4 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the part number listed in 1.2 herein. In addition, the manufacturer's part number may also be marked as listed in 6.4 herein.

3.5 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in 6.4. The certificate of compliance submitted to DESC-ECS prior to listing as an approved source of supply shall state that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

3.6 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

3.7 Notification of change. Notification of change to DESC-ECS shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.8 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A, B or D using the circuit submitted with the certificate of compliance (see 3.5 herein).

(2) $T_A = +125^{\circ}\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, and 6 in table I, method 5005 of MIL-STD-883 shall be omitted.

c. Subgroup 7 and 8 tests shall verify the truth table on figure 2.

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TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (per method 5005, table 1)
Interim electrical parameters (method 5004)	---
Final electrical test parameters (method 5004)	1*, 2, 3, 7, 8, 9, 10, 11
Group A test requirements (method 5005)	1, 2, 3, 7, 8, 9, 10, 11
Groups C and D end-point electrical parameters (method 5005)	1, 2, 3

* PDA applies to subgroup 1.

4.3.2 Groups C and D inspections.

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test conditions, method 1005 of MIL-STD-883:
 - (1) Test condition A, B or D using the circuit submitted with the certificate of compliance (see 3.5 herein).
 - (2) $T_A = +125^{\circ}\text{C}$, minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Comments. Comments on this drawing should be directed to DESC-ECS, Dayton, Ohio 45444, or telephone 513-296-5375.

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6.4 Approved source of supply. An approved source of supply is listed herein. Additional sources will be added as they become available. The vendor listed herein has agreed to this drawing and a certificate of compliance (see 3.5 herein) has been submitted to DESC-ECS.

Military drawing part number	Vendor CAGE number	Vendor similar part number <u>1/</u>
5962-8855001RX	18324	54F273/BRA
5962-8855001SX	18324	54F273/BSA
5962-88550012X	18324	54F273/B2A

1/ Caution. Do not use this number for item acquisition. Items acquired to this number may not satisfy the performance requirements of this drawing.

Vendor CAGE
number

18324

Vendor name
and address

Signetics Corporation
4130 S. Market Court
Sacramento, CA 95834

**STANDARDIZED
MILITARY DRAWING**

DEFENSE ELECTRONICS SUPPLY CENTER
DAYTON, OHIO 45444

SIZE
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