NSS12200WJ146

12 V, 3 A, Low V_{CE(sat)} PNP Transistor

ON Semiconductor's e²PowerEdge family of low $V_{CE(sat)}$ transistors are miniature surface mount devices featuring ultra low saturation voltage ($V_{CE(sat)}$) and high current gain capability. These are designed for use in low voltage, high speed switching applications where affordable efficient energy control is important.

Typical application are DC–DC converters and power management in portable and battery powered products such as cellular and cordless phones, PDAs, computers, printers, digital cameras and MP3 players. Other applications are low voltage motor controls in mass storage products such as disc drives and tape drives. In the automotive industry they can be used in air bag deployment and in the instrument cluster. The high current gain allows e²PowerEdge devices to be driven directly from PMU's control outputs, and the Linear Gain (Beta) makes them ideal components in analog amplifiers.

Features

- High Current Capability (3 A)
- High Power Handling (Up to 650 mW)
- Low V_{CE(s)} (170 mV Typical @ 1 A)
- Small Size
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Benefits

- High Specific Current and Power Capability Reduces Required PCB Area
- Reduced Parasitic Losses Increases Battery Life

MAXIMUM RATINGS ($T_A = 25^{\circ}C$)

Rating	Symbol	Max	Unit
Collector-Emitter Voltage	V _{CEO}	-12	Vdc
Collector-Base Voltage	V _{CBO}	-12	Vdc
Emitter-Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current – Continuous – Peak	I _C I _{CM}	-2.0 -3.0	Adc
Electrostatic Discharge	ESD	HBM Class 3 MM Class C	

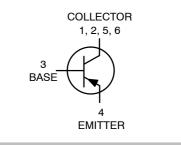
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



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$\begin{array}{c} \mbox{12 VOLTS} \\ \mbox{3.0 AMPS} \\ \mbox{PNP LOW V}_{CE(sat)} \mbox{TRANSISTOR} \\ \mbox{EQUIVALENT R}_{DS(on)} \mbox{163 m} \Omega \end{array}$





SC-88/SOT-363 CASE 419B STYLE 20

DEVICE MARKING



V2 = Specific Device Code

- M = Date Code
- = Pb–Free Package

ORDERING INFORMATION

Device	Package	Shipping [†]
NSS12200WT1G	SOT-363 (Pb-Free)	3000 / Tape & Reel

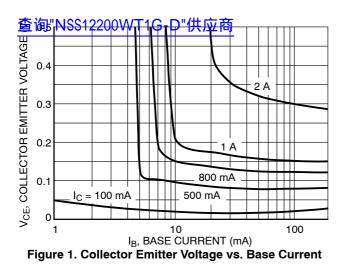
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

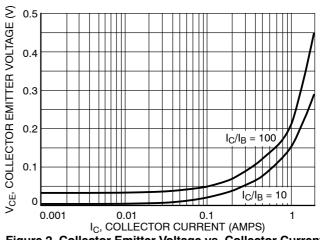
NSS12200WT1G

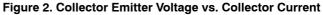
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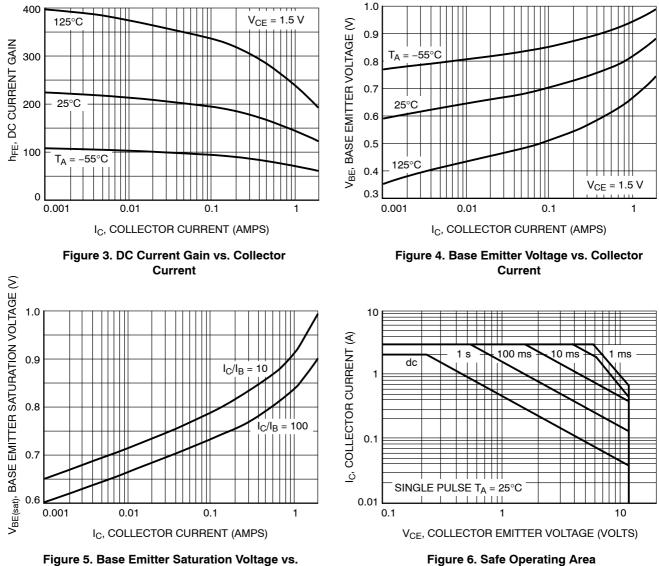
Characteristic	Symbo	bl	Мах		Unit	
Total Device Dissipation $T_A = 25^{\circ}C$	P _D (Note	e 1)	450		mW	
Derate above 25°C			3.6 275		ıW/°C	
Thermal Resistance, Junction-to-Ambient	R _{θJA} (Not	$R_{\theta JA}$ (Note 1)		c	°C/W	
Total Device Dissipation $T_A = 25^{\circ}C$	P _D (Note	e 2)	650		mW	
Derate above 25°C			5.2	m	mW/°C	
Thermal Resistance, Junction-to-Ambient	R _{θJA} (Not	e 2)	192	c	°C/W	
Thermal Resistance, Junction-to-Lead 6	R _{θJL}		105	c	°C/W	
Total Device Dissipation (Single Pulse < 10 sec.)	P _D Sing	le	1.4		W	
Junction and Storage Temperature Range	T _J , T _{st}	g	–55 to +15	0	°C	
ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise r	noted)	1				
Characteristic	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS						
Collector – Emitter Breakdown Voltage, ($I_C = -10 \text{ mAdc}$, $I_B = 0$)	V _{(BR)CEO}	-12	-15	-	Vdc	
Collector-Base Breakdown Voltage, ($I_{C} = -0.1 \text{ mAdc}, I_{E} = 0$)	V _{(BR)CBO}	-12	-25	-	Vdc	
Emitter – Base Breakdown Voltage, ($I_E = -0.1 \text{ mAdc}$, $I_C = 0$)	V _{(BR)EBO}	-5.0	-7.0	-	Vdc	
Collector Cutoff Current, ($V_{CB} = -12$ Vdc, $I_E = 0$)	I _{CBO}	-	-0.02	-0.1	μAdc	
Collector–Emitter Cutoff Current, ($V_{CES} = -12$ Vdc, $I_E = 0$)	I _{CES}	-	-0.03	-0.1	μAdc	
Emitter Cutoff Current, ($V_{CES} = -5.0 \text{ Vdc}$, $I_E = 0$)	I _{EBO}	-	-0.03	-0.1	μAdc	
ON CHARACTERISTICS				•		
DC Current Gain (Note 3) ($I_C = -0.5 \text{ A}, V_{CE} = -1.5 \text{ V}$) ($I_C = -0.8 \text{ A}, V_{CE} = -1.5 \text{ V}$) ($I_C = -1.0 \text{ A}, V_{CE} = -1.5 \text{ V}$)	h _{FE}	100 100 100	180 165 160	_ 300 _		
Collector – Emitter Saturation Voltage (Note 3) ($I_C = -0.5 \text{ A}, I_B = -10 \text{ mA}$) ($I_C = -0.8 \text{ A}, I_B = -16 \text{ mA}$) ($I_C = -1.0 \text{ A}, I_B = -20 \text{ mA}$)	V _{CE(sat)}		-0.10 -0.14 -0.17	-0.160 -0.235 -0.290	V	
Base – Emitter Saturation Voltage (Note 3) $(I_C = -1.0 \text{ A}, I_B = -20 \text{ mA})$	V _{BE(sat)}	_	-0.84	-0.95	V	
Base – Emitter Turn-on Voltage (Note 3) ($I_C = -1.0 \text{ A}, V_{CE} = -1.5 \text{ V}$)	V _{BE(on)}	_	-0.81	-0.95	V	
Cutoff Frequency (I _C = -100 mA, V _{CE} = -5.0 V, f = 100 MHz)	fT	_	100	-	MHz	
Output Capacitance (V _{CB} = -1.5 V, f = 1.0 MHz)	C _{obo}	_	50	65	pF	

FR-4, Minimum Pad, 1 oz Coverage.
FR-4, 1" Pad, 1 oz Coverage.
Pulsed Condition: Pulse Width < 300 μsec, Duty Cycle < 2%.









Base Current

NSS12200WT1G

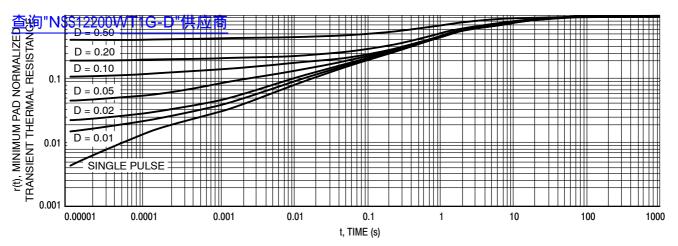


Figure 7. Normalized Thermal Response

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PACKAGE DIMENSIONS

SC-88/SC70-6/SOT-363

CASE 419B-02 **ISSUE W**

NOTES

1. DIMENSIONING AND TOLERANCING PER ANSI

Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

419B-01 OBSOLETE, NEW STANDARD 419B-02.

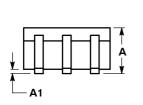
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.95	1.10	0.031	0.037	0.043
A1	0.00	0.05	0.10	0.000	0.002	0.004
A3	0.20 REF		0.008 REF			
b	0.10	0.21	0.30	0.004	0.008	0.012
С	0.10	0.14	0.25	0.004	0.005	0.010
D	1.80	2.00	2.20	0.070	0.078	0.086
E	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65 BSC		0.026 BSC			
L	0.10	0.20	0.30	0.004	0.008	0.012
HE	2.00	2.10	2.20	0.078	0.082	0.086

STYLE 20

PIN 1. COLLECTOR 2. COLLECTOR



COLLECTOR 5 6. COLLECTOR

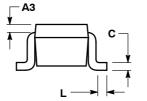


-E-

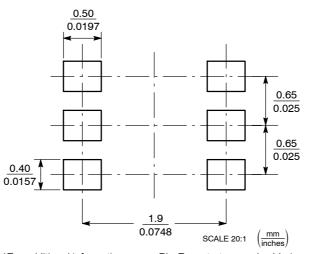
b 6 PL

0.2 (0.008) M E M

 \oplus



SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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