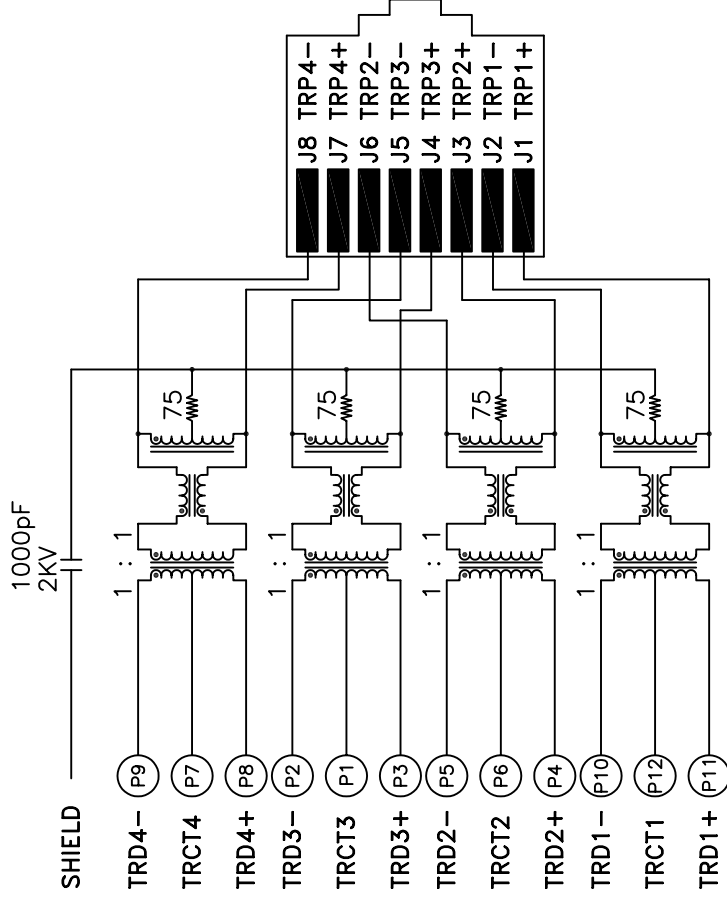
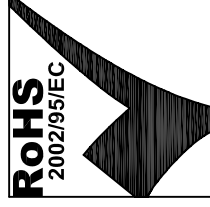




ELECTRICAL SPECIFICATIONS:

- 1.0 TURNS RATIO: $\{P3-P2\} : \{J4-J5\}$
 $\{P5-P4\} : \{J6-J3\}$
 $\{P9-P8\} : \{J8-J7\}$
 $\{P10-P11\} : \{J2-J1\}$
: 1 : 1 $\pm 2\%$
: 1 : 1 $\pm 2\%$
: 1 : 1 $\pm 2\%$
: 1 : 1 $\pm 2\%$
- 2.0 INDUCTANCE: $\{P3-P2\} : \{P5-P4\}$
 $\{P10-P11\} : \{P9-P8\}$
: 350 μH MIN. @ 0.1V, 100KHz, 8 mA DC BIAS
: 350 μH MIN. @ 0.1V, 100KHz, 8 mA DC BIAS
- 3.0 LEAKAGE INDUCTANCE: $P3-P2$ (WITH $J4$ AND $J5$ SHORT)
 $P5-P4$ (WITH $J6$ AND $J3$ SHORT)
 $P9-P8$ (WITH $J8$ AND $J7$ SHORT)
 $P10-P11$ (WITH $J1$ AND $J2$ SHORT)
: 0.3 μH MAX. @ 1MHz
: 0.3 μH MAX. @ 1MHz
: 0.3 μH MAX. @ 1MHz
: 0.3 μH MAX. @ 1MHz
- 4.0 INTERWINDING CAPACITANCE: $\{P3-P2\} : \{J4-J5\}$
 $\{P5-P4\} : \{J6-J3\}$
 $\{P9-P8\} : \{J5-J4\}$
 $\{P10-P11\} : \{J2-J1\}$
: 35pf MAX @ 1MHz
: 35pf MAX @ 1MHz
: 35pf MAX @ 1MHz
: 35pf MAX @ 1MHz
- 5.0 DC RESISTANCE: $\{J6-J3\} : \{J2-J1\} ; \{J7-J8\} : \{J4-J5\}$
: 1.2 ohms Max.

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6.0 RETURN LOSS: 1MHz TO 30MHz
30MHz TO 60MHz
60MHz TO 80MHz
80MHz TO 100MHz
: -19dB MIN.
: -13dB MIN.
: -12dB MIN.
: -10dB MIN.

7.0 DIELECTRIC WITHSTAND: (J1,J2) TO (P10,P11) ; (J5,J4) TO (P3-P2)
(J3,J6) TO (P5,P4) ; (J8,J7) TO (P9, P8)
: 1500 VAC
: 1500 VAC

8.0 INSERTION LOSS: RS=RL=100 ohms
100KHz TO 125MHz
: -1.1 dB TYP

9.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS
OUTPUT VOLTAGE = 1 V peak
PULSE WIDTH= 112nS
: 3.0 nS MAX
: 3.0 nS MAX

10.0 CROSS TALK: 1-100 MHz
: -[33-20 LOG ($\frac{F}{50 \text{ MHz}}$)] MIN.

11.0 COMMON TO COMMON MODE ATTENUATION: 1MHz TO 100MHz
: -35dB TYP

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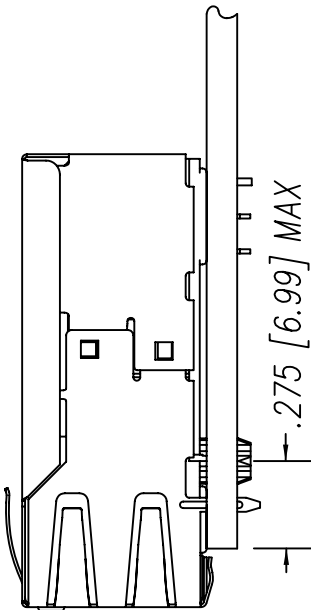
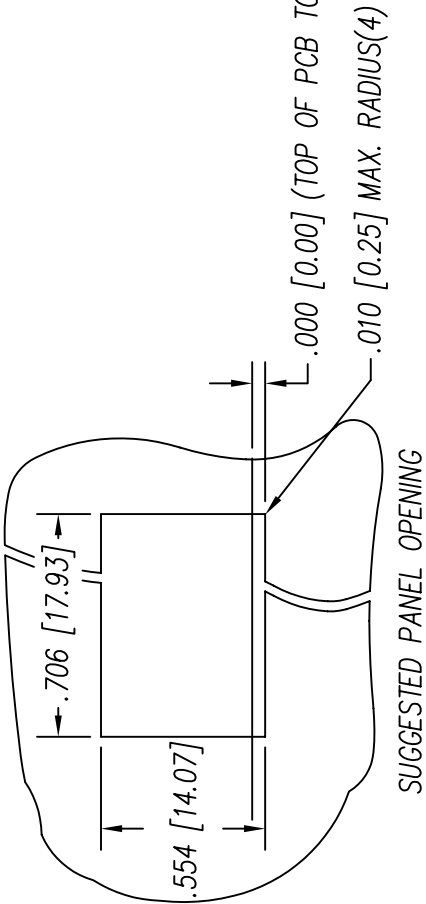
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1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE $\pm .005$ [0.13]

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