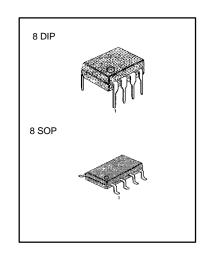
SINGLE OPERATIONAL AMPLIFIERS

The LM741 series are general purpose operational amplifiers which feature improved performance over industry standards like the LM709. It is intended for a wide range of analog applications.

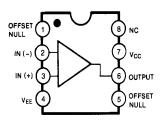
The high gain and wide range of operating voltage provide superior performance in integrator, summing amplifier, and general feedback applications.

FEATURES

- Short circuit protection
- Excellent temperature stability
- Internal frequency compensation
- High Input voltage range
- Null of offset



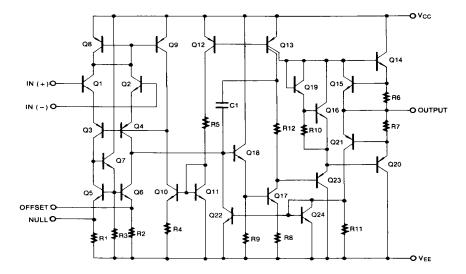
BLOCK DIAGRAM



ORDERING INFORMATION

Device	Package	Operating Temperature
LM741N	8 DIP	
LM741EN	0 0 11	0 ~ + 70°C
LM741M	8 SOP	0~+70 C
LM741EM	0 001	
LM741IN	8 DIP	
LM741EIN	O DII	40 05.00
LM741IM	8 SOP	-40 ~ +85 °C
LM741EIM	0 001	

SCHEMATIC DIAGRAM





ABSOLUTE MAXIMUM RATINGS (T_A=25 °C)

Characteristic	Symbol	LM741	LM741E	LM741I	Unit
Supply Voltage	V _{CC}	±18	±22	±18	V
Differential Input Voltage	$V_{I(DIFF)}$	30	30	30	V
Input Voltage	V_{I}	±15	±15	±15	V
Output Short Circuit Duration		Indefinite	Indefinite	Indefinite	
Power Dissipation	P_D	500	500	500	mW
Operating Temperature Range	T_{OPR}	0 ~ + 70	0 ~ + 70	-40 ~ + 85	°C
Storage Temperature Range	T_{STG}	-65 ~ + 150	-65 ~ + 150	-65 ~ + 150	°C

ELECTRICAL CHARACTERISTICS

(V_{CC} = 15V, V_{EE} = - 15V. T_A = 25 °C, unless otherwise specified)

Characteristic	Symbol	Test Conditions		LM741E			LM	Unit		
Characteristic	Symbol	Test Conditions			Тур	Max	Min	Тур	Max	Oiiit
la	V _{IO}	R _S ≤10KΩ						2.0	6.0	
Input Offset Voltage	V IO	R _S ≤50Ω			8.0	3.0				m∨
Input Offset Voltage	V _{IO(R)}	$V_{CC} = \pm 20V$		+10				+15		mV
Adjustment Range	▼10(R)	VCC = ±20V		110				110		111.0
Input Offset Current	I _{IO}				3.0	30		20	200	nA
Input Bias Current	I _{BIAS}				30	80		80	500	nA
Input Resistance	Rı	$V_{CC} = \pm 20V$		1.0	6.0		0.3	2.0		ΜΩ
Input Voltage Range	$V_{I(R)}$			±12	±13		±12	±13		V
			V _{CC} =±20V,							
		R _L ≥2KΩ	$V_{O(P.P)} = \pm 15V$	50						
Large Signal Voltage Gain	G_V		V _{CC} =±15V,				20	200		V/mV
			V _{O(P.P)} =±10V				20	200		
Output Short Circuit Current	I _{sc}			10	25	35		25		mA
		V _{CC} = ±20V	R _L ≥10KΩ	±16						- V
	1,,		R _L ≥10KΩ	±15						
Output Voltage Swing	$V_{O(P.P)}$	V _{CC} = ±15V	R _L ≥10KΩ				±12	±14		
			R₁≥10KΩ				±10	±13		
	1	$R_S \le 10K\Omega$, $V_{CM} = \pm 12V$					70	90		
Common Mode Rejection Ratio	CMRR	$R_S \le 50K\Omega$, $V_{CM} = \pm 12V$		80	95					dB
	1	$V_{CC} = \pm 15V$ to	to \/ = +15\/							
		$R_{\rm S} \leq 50\Omega$		86	96					
Power Supply Rejection Ratio	PSRR	$V_{CC} = \pm 15V$ to	V _{CC} = +15V							dB
		R _S ≤10KΩ	00 = 1 = 1				77	96		



ELECTRICAL CHARACTERISTICS (Continued)

Characteristic		Symbol	Test Conditions		LM741E			41/LM	Unit	
Cilarac	teristic	Syllibol	rest conditions	Min	Тур	Max	Min	Тур	Max	Oilit
Transient	Rise Time	t _R			0.25	0.8		0.3		μs
Response	Overshoot	OS	Unity Gain		6.0	20		10		%
Bandwidth		BW		0.43	1.5					MHz
Slew Rate		SR	Unity Gain	0.3	0.7			0.5		V/μs
Supply Current		Icc	R _L = ∞Ω					1.5	2.8	mA
		_	$V_{CC} = \pm 20V$		80	150				mW
Power Consum	ption	Pc	$V_{CC} = \pm 15V$					50	85	IIIVV

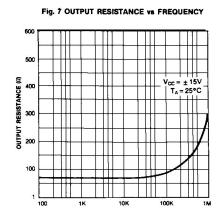
ELECTRICAL CHARACTERISTICS

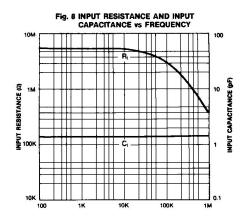
(-40 °C \leq T_A \leq 85 °C for the KA7411 °C \leq T_A \leq 70 °C for the LM741 and LM741E. V_{CC} = \pm 15V, unless otherwise specified)

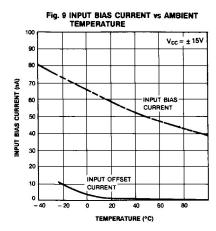
Characteristic	Symbol	nbol Test Conditions		LM741E			LM	1114		
Characteristic	Syllibol			Min	Тур	Max	Min	Тур	Max	Unit
Innut Offeet Veltage	.,	R _S ≤50Ω				4.0				mV
Input Offset Voltage	V _{IO}	R _S ≤10KΩ							7.5	IIIV
Input Offset Voltage Drift	$\Delta V_{IO}/\Delta T$				15					μV/°C
Input Offset Current	I _{IO}					70			300	nA
Input Offset Current Drift	$\Delta I_{IO}/\Delta T$					0.5				nA/°C
Input Bias Current	I _{BIAS}					0.21			0.8	μΑ
Input Resistance	R _I	$V_{CC} = \pm 20V$		0.5						ΜΩ
Input Voltage Range	$V_{I(R)}$			±12	±13		±12	±13		V
		V _{CC} =±20V V _{CC} =±15V	R _S ≥10KΩ	±16						- V
	.,		R _S ≥2KΩ	±15						
Output Voltage Swing	$V_{O(P.P)}$		R _S ≥10KΩ				±12	±14		
			R _S ≥2KΩ				±10	±13		
Output Short Circuit Current	I _{sc}			10		40	10		40	mA
	OMBB	R _S ≤10KΩ, V	$t_{CM} = \pm 12V$				70	90		
Common Mode Rejection Ratio	CMRR	R _S ≤50KΩ, V	/ _{CM} = ±12V	80	95					dB
		$V_{CC} = \pm 20V$	R _S ≤50Ω	86	96					-10
Power Supply Rejection Ratio	PSRR	to ±5V	R _S ≤10KΩ				77	96		dB
Large Signal Voltage Gain			$V_{CC} = \pm 20V$,	32						
			$V_{O(P-P)} = \pm 15V$							
	G√	R _S ≥2KΩ	$V_{CC} = \pm 15V$,				15			V/mV
Large Orginal Voltage Odlil			$V_{O(P.P)} = \pm 10V$							1
			$V_{CC} = \pm 15V$,	10						
	ĺ	$V_{O(P-P)} = \pm 2V$								

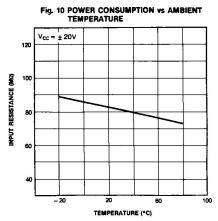


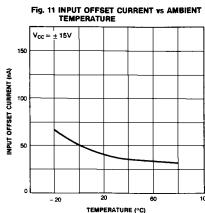
TYPICAL PERFORMANCE CHARACTERISTICS

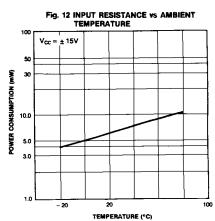




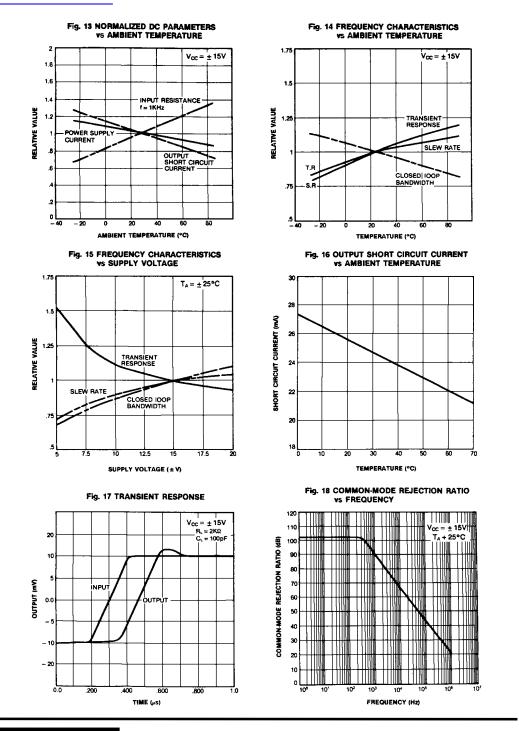








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