

SANYO Semiconductors DATA SHEET

LB11683V — Monolithic Digital IC Three-Phase Sensorless Motor Driver

Overview

The LB11683V is a three-phase full-wave current-linear-drive motor driver IC. It adopts a sensorless control system without the use of a Hall effect device. For quieter operation, the LB11683V features a current soft switching circuit and is optimal for driving the cooling fan motors used in refrigerators, etc.

Functions

- Current linear drive
- Built-in current limiter circuit
- Output stage oversaturation prevention circuit
- Coil counter-electromotive FG output
- Built-in thermal shutdown circuit
- Beat lock prevention circuit
- Lock protection circuit
- Lock detection output

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V _{CC} max		14.5	V
Output applied voltage	V _O max		14.5	V
Input applied voltage	V _I max		-0.3 to V _{CC} +0.3	V
Output current	I _O max		1.5	Α
Allowable power dissipation	Pd max	Independent IC	0.5	W
Operating temperature	Topr		-30 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

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LB11683V

Attemable Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage 1	Vcc	All operating circuits other than internal 5V Reg.	5.5 to 7.0	V
Supply voltage 2	V _{CC}	All operating circuits.	7.0 to 13.8	V

Electrical Characteristics at $Ta=25\,^{\circ}C,\,V_{CC}=12V$

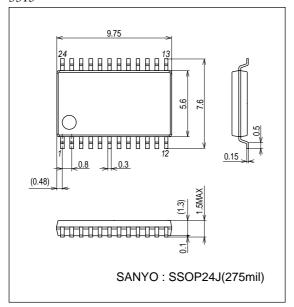
Parameter	Symbol	Conditions	Ratings			Unit	
, ,		Coriditions	min	typ	max		
Supply current	Icc	VC=V _{CC} VFC=0V		10	15	mA	
Output saturation voltage 1	V _O SAT1	I _O =0.4A, Source+Sink		1.4	2.0	V	
Output saturation voltage 2	V _O SAT2	I _O =0.8A, Source+Sink, R _F =0Ω		1.8	2.6	V	
MCOM pin common-phase input voltage range	VIC		0		V _{CC} -2	V	
PCOUT output current 1	IPCOU	Source side		-90		μΑ	
PCOUT output current 2	IPCOD	Sink side		90		μΑ	
VCOIN input current	IVCOIN	VCOIN=5V		0.1	0.2	μΑ	
VCO minimum frequency	fVCOMIN	VCOIN=open CX=0.022μF	330	400	500	Hz	
VCO maximum frequency	fVCOMAX	VCOIN=5V CX=0.022μF	14.8	18.5	22.3	kHz	
C1, C2 source current ratio	RSOURCE	1-(IC1SOURCE/IC2SOURCE)	-12		+12	%	
C1, C2 sink current ratio	RSINK	1-(IC1SINK/IC2SINK)	-12		+12	%	
C1 source, sink current ratio	RC1	IC1SOURCE/IC1SINK		50		%	
C2 source, sink current ratio	RC2	IC2SOURCE/IC2SINK		50		%	
Counter-electromotive FG output ON voltage	VOL	IFGO=1mA			0.4	V	
CT pin charge current	ICT1	Source current	1.2	1.6		μΑ	
CT pin discharge current	ICT2	Sink current	50	77		nA	
Lock protection detection voltage	VRD1		2.3	2.45	2.6	V	
Lock protection reset voltage	VRD2		1.13	1.26	1.39	V	
RD pin leak current	IRDLEAK				10	μΑ	
RD pin output LO voltage	VRDL	I _O =1mA		100	400	mV	
Current limiter setting voltage			0.45	0.5	0.55	V	
Thermal shutdown operating temperature	TTSD	Design target *	150	180	210	°C	
Thermal shutdown hysteresis	ΔTTSD	Design target *		15		°C	

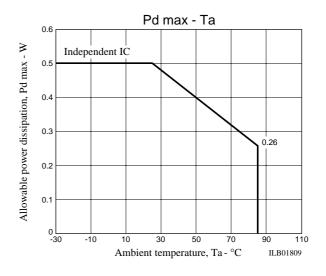
^{*:} Design target value and no measurement is made.

Package Demensions

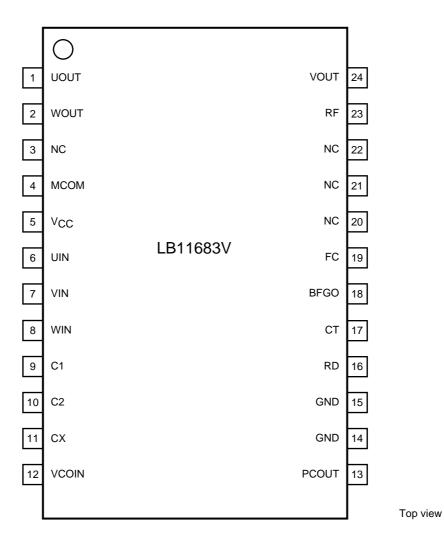
unit: mm (typ)

3315

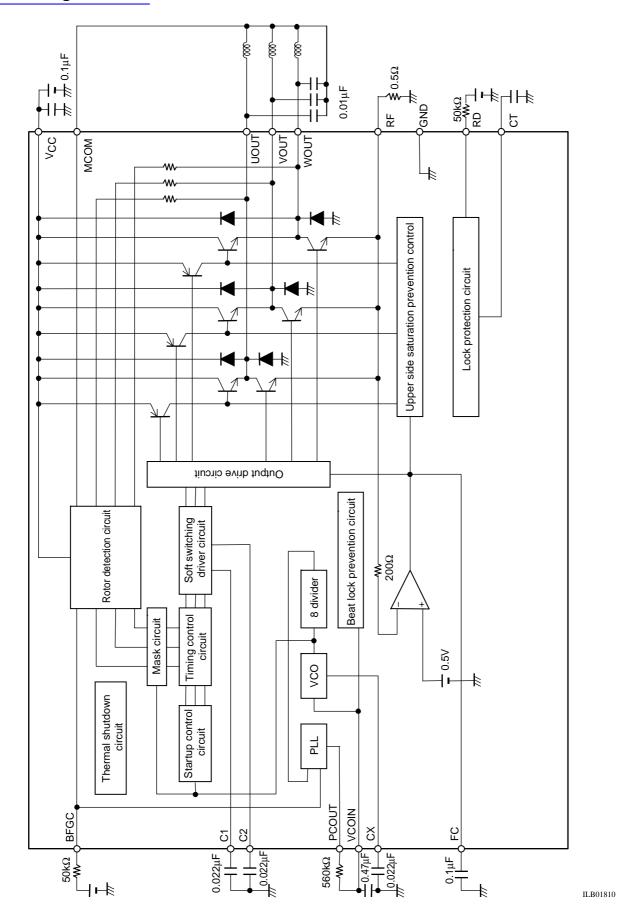




Pin Assignment



BiogkLDiagram 供应商



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Pin No.	Symbol	"供应商 Pin Voltage	Equivalent Circuit Diagram	Description
24	UOUT	<u></u>	Vcc	3-phase motor driver output
1	VOUT		**CC	,
2	WOUT		3.9Ω ↓ ↓	
23	RF		30kΩ 10kΩ 20μF 30kΩ 23 /// 2 8 /// 8	Minimum potential of 3-phase motor driver output transistor. Constant current control is made through detection of this voltage. The current limiter also functions by detecting this potential.
5	VCC	8 to 13.8V		Power supply
4	МСОМ		Vcc ②10kΩ (6) \$10kΩ (7) \$10kΩ (8) \$200Ω (9) \$10kΩ	Motor coil neutral point input pin. The coil voltage waveform is detected with reference to this voltage.
6	UIN		8 ξ 200Ω	Coil waveform detection comparator input pin. This pin is connected to each phase output
8	WIN			through the internal $10k\Omega$ resistor.
	04			Triongular ways are a still a second
9	C1		15μF 15μF 9 5μA 1kΩ	Triangular wave generating capacitor connection pin. This triangular wave performs soft-switching of the coil output waveforms.
10	C2		2S 1/2VREG-VF	
11	СХ		VREG VREG 100μA	In the VCO circuit, the operation frequency range and minimum operation frequency are determined by means of the capacitor value connected this pin and ground.
			300Ω	
12	VCOIN		VREG 10kΩ 1.75V 1.75V 12 50kΩ 50μΑ 50μΑ	VCO circuit voltage input pin. The PCOUT pin voltage is input via CR filter.

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LB11683V

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		<u>'Pird∀oltad</u> e	Equivalent Circuit Diagram	Description
13	PCOUT		VREG VCC	VCO circuit PLL output
14 15	GND			Ground for all other than the output transistor
18	BFGO		VREG	Motor counter-electromotive voltage detection FG output (single-phase only). Open collector output
19	FC		VREG VCC 1kΩ 10kΩ \$5kΩ	Frequency characteristics compensation pin. Insertion of a capacitor between this pin and ground stops oscillation of the closed loop of current control system.
16	RD		VREG ▼15kΩ 16	Lock detection output. When motor is running: low-level When motor is locked: high-level Open collector output
17	СТ		VCC VREG 0.1μA 1μA 17 15μA 25kΩ 17 17 17 17 17 17 17 17 17 1	Lock protection ON/OFF time setting capacitor connection pin. The capacitor connected between this pin and ground determines the driver ON/OFF time when the motor is locked.

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