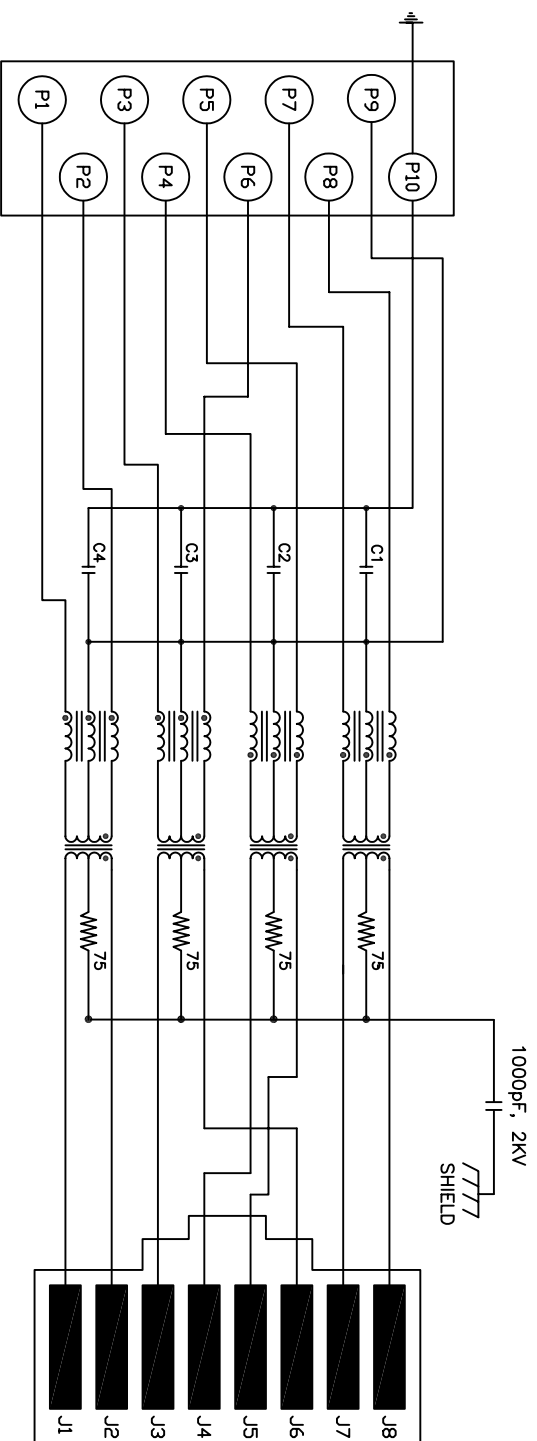




NOTE:

1.0 C1, C2, C3, C4 = 0.1 uF

ELECTRICAL SPECIFICATIONS:

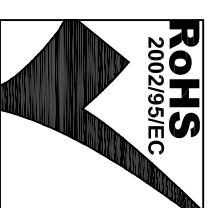
- 1.0 TURNS RATIO:

(P2-P1) :	(J2-J1)	:	1CT	:	1CT	±3%
(P6-P3) :	(J6-J3)	:	1CT	:	1CT	±3%
(P5-P4) :	(J5-J4)	:	1CT	:	1CT	±3%
(P8-P7) :	(J8-J7)	:	1CT	:	1CT	±3%
- 2.0 INDUCTANCE:

(P1-P2) :	(P5-P4)	:	350 uH MIN.	@	0.1V, 100KHZ, 8 mA DC BIAS
(P3-P6) :	(P7-P8)	:	350 uH MIN.	@	0.1V, 100KHZ, 8 mA DC BIAS
- 3.0 LEAKAGE INDUCTANCE:

P2-P1 (WITH J2 AND J1 SHORT)	:	0.3uH MAX.	@	1MHZ
P6-P3 (WITH J6 AND J3 SHORT)	:	0.3uH MAX.	@	1MHZ
P5-P4 (WITH J5 AND J4 SHORT)	:	0.3uH MAX.	@	1MHZ
P8-P7 (WITH J8 AND J7 SHORT)	:	0.3uH MAX.	@	1MHZ
- 4.0 DC RESISTANCE: (J6-J3) ; (J2-J1) ; (J5-J4) ; (J8-J7) : 1.2 ohms Max.

[查询"SI-61005-F"供应商](#)



Bel Stewart Connector
11118 Susquehanna Trail, South
Glen Rock, Pa 17327-9799
717.234.7512

Magjack

<http://www.stewartconnector.com>

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SHEET
1 OF 4

DRAWING NO.

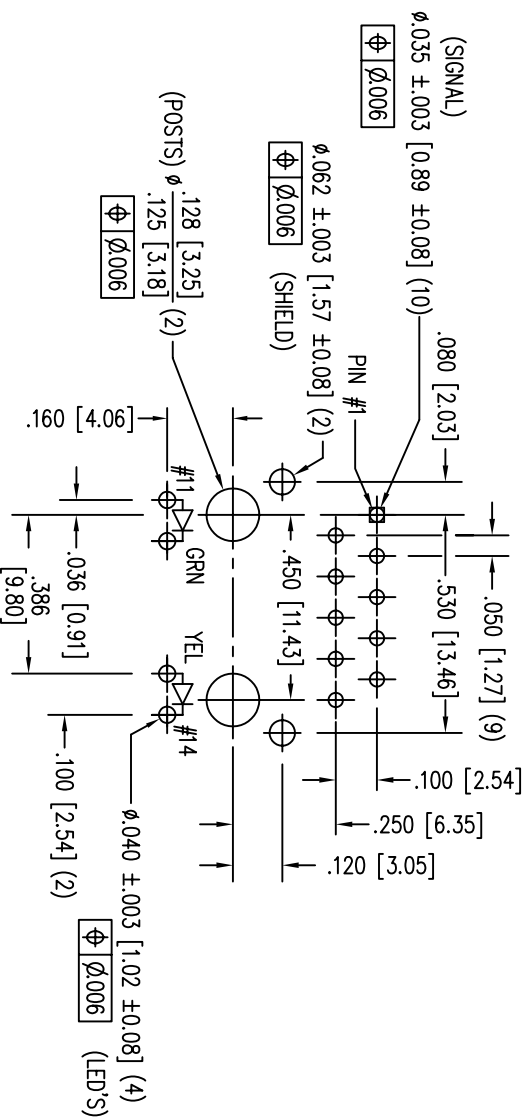
SI-61005-F

REV.
06

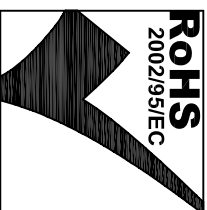
5.0 RETURN LOSS:1MHZ TO 40MHZ 60MHZ TO 100MHZ	: 18dB MIN. : 12-20 LOG ($\frac{F}{80\text{MHz}}$)
6.0 DIELECTRIC WITHSTAND: {J1,J2} TO {P1,P2} ; {J5,J4} TO {P5,P4} {J3,J6} TO {P3,P6} ; {J8,J7} TO {P8, P7}	: 1500 VAC : 1500 VAC
7.0 INSERTION LOSS: RS=RL=100 ohms 1MHZ TO 100MHZ	: 1.1 dB MAX
8.0 RISE TIME: RS=100 OHMS AND RL = 100 OHMS OUTPUT VOLTAGE = 1 V peak PULSE WIDTH= 112ns	: 3.0 nS MAX : 3.0 nS MAX
9.0 CROSS TALK: 1-100 MHz	: 40 dB TYP
10.0 COMMON TO COMMON MODE ATTENUATION: 1MHZ TO 100MHZ	: 40dB TYP

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ALL CENTERLINE DIMENSIONS ARE BASIC.



- NOTES:
1. CONNECTOR MATERIALS:
HOUSING: THERMOPLASTIC UL94 V-0
CONTACT/SHIELD: COPPER ALLOY
SHIELD PLATING: NICKEL OR TIN
CONTACT PLATING: SELECTIVE GOLD,
50 MICRO-INCHES MIN. IN CONTACT AREA.
 2. PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED.
SEE ELECTRICAL DRAWING FOR OMITTED PINS.
 3. TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS.
 4. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE $\pm .005$ [0.13]
 5. REFLOW AND WAVE SOLDER COMPATIBLE -260°C FOR
10 SECONDS MAX.

The diagram shows a parallel circuit with three branches. The top branch contains a resistor, represented by a rectangle. The bottom-left branch contains a resistor, represented by a rectangle. The bottom-right branch contains a diode, represented by a triangle pointing downwards with a vertical line through its center.

*WITH A FORWARD CURRENT OF 20 mA (TYP)

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SINGLE COLOR LED

Bel Stewart Connector

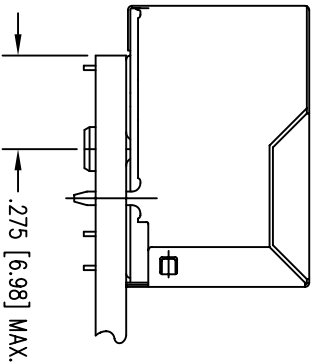
11118 Susquehanna Trail, South

Glen Rock, Pa 17327-9199

717.234.7512

<http://www.stewartconnector.com>





1. THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY.
2. ALL TOLERANCES NOT OTHERWISE SPECIFIED TO BE $\pm .005$ [0.13]

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SHEET 4 OF 4	DRAWING NO. SI-61005-F	REV. 03
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