

# **Thyristor**

Low Power Use

REJ03G0351-0200 Rev.2.00 Mar.01.05

## **Features**

•  $I_{T(AV)}: 0.3 A$ 

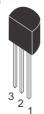
•  $V_{DRM}$ : 400 V

•  $I_{GT}$ : 100  $\mu A$ 

Planar Passivation Type

## **Outline**

PRSS0003EA-A (Package name: TO-92)





- 1. Cathode
- 2. Anode
- 3. Gate

## **Applications**

Solid state relay, leakage protector, fire alarm, timer, ring counter, electric blanket, protective circuit for acoustic equipment, strobe flasher, and other general purpose control applications

## **Maximum Ratings**

Parameter	Symbol	Voltage class	Unit	
Farameter	Symbol	8	W.Br.onc	
Repetitive peak reverse voltage	V <sub>RRM</sub>	400	V	
Non-repetitive peak reverse voltage	V <sub>RSM</sub>	500	V	
DC reverse voltage	V <sub>R (DC)</sub>	320	V	
Repetitive peak off-state voltage <sup>Note1</sup>	$V_{DRM}$	400	V	
DC off-state voltage Note1	V <sub>D (DC)</sub>	320	V	

查询"CROZAMO*S-TB"供应	Symbol	Ratings	Unit	Conditions
RMS on-state current	I <sub>T (RMS)</sub>	0.47	Α	
Average on-state current	I <sub>T (AV)</sub>	0.3	А	Commercial frequency, sine half wave 180° conduction, Ta = 30°C
Surge on-state current	I <sub>TSM</sub>	10	А	60Hz sine half wave 1 full cycle, peak value, non-repetitive
I <sup>2</sup> t for fusing	l <sup>2</sup> t	0.4	A <sup>2</sup> s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	$P_{GM}$	0.1	W	
Average gate power dissipation	P <sub>G (AV)</sub>	0.01	W	
Peak gate forward voltage	$V_{FGM}$	6	V	
Peak gate reverse voltage	$V_{RGM}$	6	V	
Peak gate forward current	I <sub>FGM</sub>	0.1	Α	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	0.23	g	Typical value

Notes: 1. With gate to cathode resistance  $R_{GK} = 1 \text{ k}\Omega$ .

## **Electrical Characteristics**

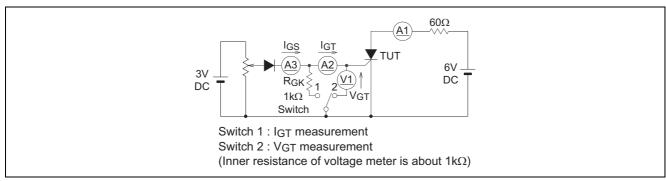
Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak reverse current	I <sub>RRM</sub>	_	_	0.1	mA	Tj = 125°C, V <sub>RRM</sub> applied
Repetitive peak off-state current	I <sub>DRM</sub>	_	_	0.1	mA	$Tj = 125$ °C, $V_{DRM}$ applied, $R_{GK} = 1 \text{ k}\Omega$
On-state voltage	$V_{TM}$	_	_	1.6	V	Ta = 25°C, I <sub>TM</sub> = 0.6 A, instantaneous value
Gate trigger voltage	V <sub>GT</sub>	_	_	0.8	V	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1$ A <sup>Note3</sup>
Gate non-trigger voltage	$V_{GD}$	0.2	_	_	V	$Tj = 125 ^{\circ}C, V_D = 1/2 V_{DRM},$ $R_{GK} = 1 k\Omega$
Gate trigger current	I <sub>GT</sub>	1	_	100 <sup>Note2</sup>	μΑ	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1$ A <sup>Note3</sup>
Holding current	I <sub>H</sub>	_	_	3	mA	$Tj = 25$ °C, $V_D = 12$ V, $R_{GK} = 1$ k $\Omega$
Thermal resistance	R <sub>th (j-a)</sub>	_	_	180	°C/W	Junction to ambient

Notes: 2. If special values of I<sub>GT</sub> are required, choose item D or E from those listed in the table below if possible.

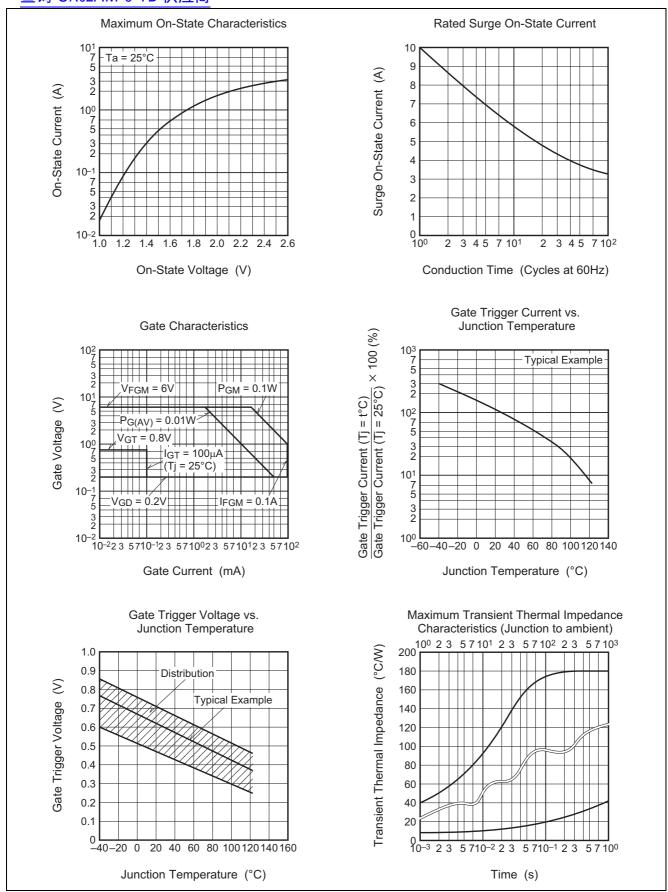
	Item	Α	В	С	D	E
Γ	I <sub>GT</sub> (μA)	1 to 30	20 to 50	40 to 100	1 to 50	20 to 100

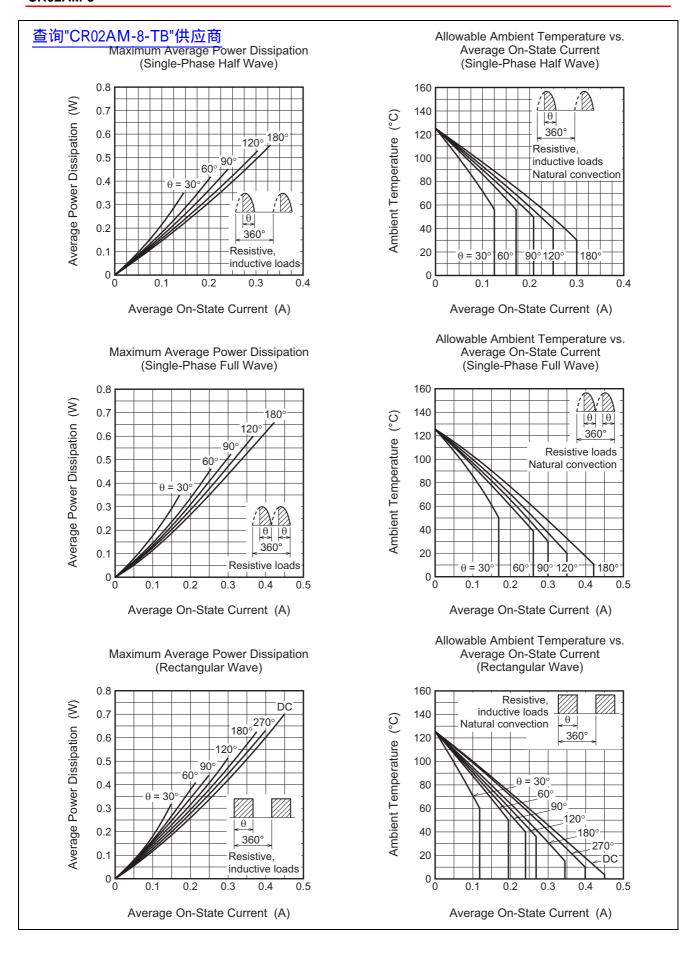
The above values do not include the current flowing through the 1  $k\Omega$  resistance between the gate and cathode.

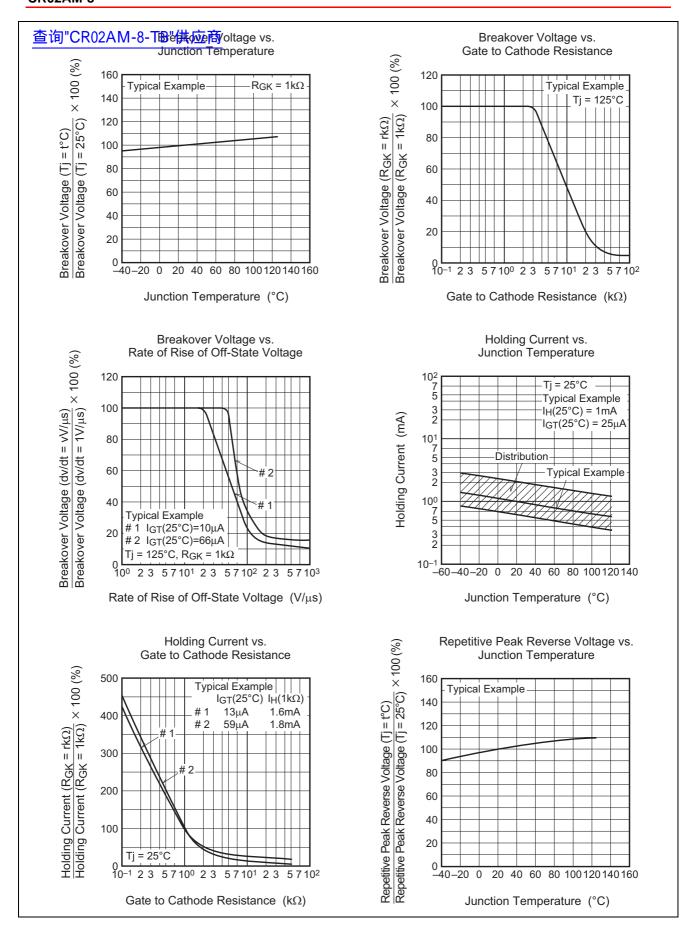
3.  $I_{GT}$ ,  $V_{GT}$  measurement circuit.

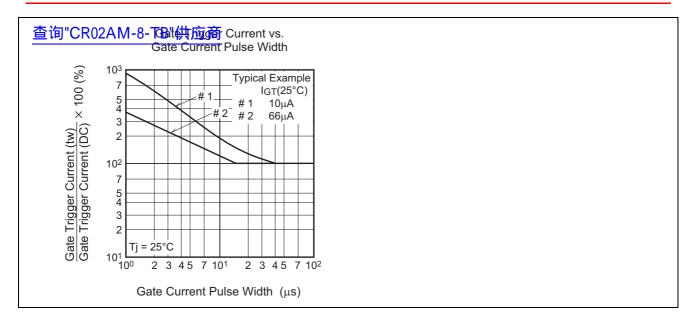


# Performance Curve 供应商

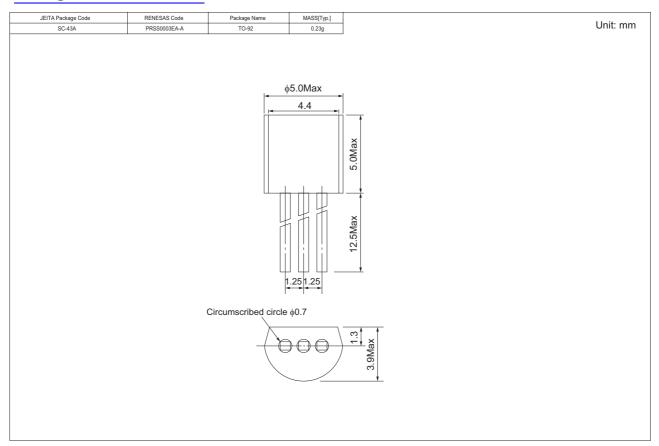








# P查试验Qe0DAnnension供应商



# **Order Code**

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Straight type	Vinyl sack	500	Type name	CR02AM-8
Lead form	Vinyl sack	500	Type name – Lead forming code	CR02AM-8-A6
Form A8	Taping	2000	Type name – TB	CR02AM-8-TB

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