

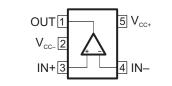
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FEATURES

- Qualified for Automotive Applications
- Wide Power-Supply Range
 - Single Supply: 3 V to 30 V
 - Dual Supply: ±1.5 V to ±15 V
- Large Output Voltage Swing: 0 V to 3.5 V (Min) (V_{CC} = 5 V)
- Low Supply Current: 500 μA (Typ)
- Low Input Bias Current: 20 nA (Typ)
- Stable With High Capacitive Loads

DESCRIPTION/ORDERING INFORMATION



DBV (SOT-23-5) PACKAGE

(TOP VIEW)

The TS321 is a bipolar operational amplifier for cost-sensitive applications in which space savings are important.

ORDERING INFORMATION⁽¹⁾

T _A	PACKAGE ⁽²⁾		ORDERABLE PART NUMBER	TOP-SIDE MARKING	
-40°C to 125°C	SOT-23-5 – DBV	Reel of 3000	TS321QDBVRQ1	9CNS	

(1) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at www.ti.com.

(2) Package drawings, thermal data, and symbolization are available at www.ti.com/packaging.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

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ABSOLUTE MAXIMUM RATINGS⁽¹⁾

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT
	Supply voltage ⁽²⁾	Single		32	V
V _{CC}	Supply voltage ?	Dual		±16	v
V_{ID}	Differential input voltage ⁽³⁾		32	V	
VI	Input voltage range ^{(2) (4)}	-0.3	32	V	
I _I	Input current ⁽⁴⁾		50	mA	
t _{short}	Duration of output short circuit to ground		U	nlimited	
θ_{JA}	Package thermal impedance, junction to free $air^{(5)}$ ⁽⁶⁾	DBV package		206	°C/W
TJ	Operating virtual junction temperature			150	°C
T _{stg}	Storage temperature range		-65	150	°C

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under Recommended Operating Conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

These voltage values are with respect to the midpoint between V_{CC+} and V_{CC-} . (2)

Differential voltages are at IN+ with respect to IN-(3)

(4)

Neither input must ever be more positive than V_{CC+} or more negative than V_{CC-} . Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient (5) temperature is $P_D = (T_J(max) - T_A)/\theta_{JA}$. Selecting the maximum of 150°C can affect reliability.

The package thermal impedance is calculated in accordance with JESD 51-7. (6)

RECOMMENDED OPERATING CONDITIONS

			MIN	MAX	UNIT
V	Supply voltage	Single supply	3	30	V
V _{CC}	Supply voltage	Dual supply			v
T _A	Operating free-air temperature		-40	125	°C

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ELECTRICAL CHARACTERISTICS

 V_{CC+} = 5 V, V_{CC-} = GND, V_{O} = 1.4 V (unless otherwise noted)

PARAMETER		TEST CONDITIONS		T _A	MIN	TYP	MAX	UNIT
V _{IO} Input offset voltage		$R_{S} = 0, 5 V < V_{CC+} < 30 V,$		25°C		0.5	4	mV
		$0 < V_{IC} < (V_{CC+} - 1.5 V)$	$0 < V_{IC} < (V_{CC+} - 1.5 V)$				5	
land affect annual				25°C		2	30	0
I _{IO}	Input offset current			Full range			50	nA
	lagest bigg groups (1)			25°C		20	150	0
I _{IB}	Input bias current ⁽¹⁾						200	nA
٨	Large-signal differential voltage	$V_{CC} = 15 \text{ V}, \text{ R}_{L} = 2 \text{ k}\Omega$		25°C	50	100		V/mV
A _{VD}	amplification	$V_0 = 1.4$ V to 11.4 V		Full range	25			
V	Common mode input voltors (2)	V 20.V		25°C	0		V _{CC+} – 1.5	V
V _{ICR}	Common-mode input voltage ⁽²⁾	V _{CC} = 30 V		Full range	0		$V_{CC+} - 2$	v
				25°C	26	27		
		N 00.14	$R_L = 2 k\Omega$	Full range	25.5			
	I Pala la cal a charactera la cal	$V_{CC} = 30 V$	D 4010	25°C	27	28		
V _{OH}	High-level output voltage		$R_L = 10 k\Omega$	Full range	26.5			V
		V _{CC} = 5 V	$R_L = 2 k\Omega$	25°C	3.5			-
				Full range	3			
	I and the set of the set	R _L = 10 kΩ		25°C		5	15	
V _{OL}	Low-level output voltage			Full range			20	mV
GBP	Gain bandwidth product	$V_{CC} = 30 \text{ V}, \text{ V}_{I} = 10 \text{ mV}, \text{ R}_{L} = 2 \text{ k}\Omega,$ f = 100 kHz, C _L = 100 pF		25°C		0.8		MHz
SR	Slew rate	$V_{CC} = 15 \text{ V}, \text{ V}_{I} = 0.5 \text{ V} \text{ to } 3 \text{ V},$ $R_{L} = 2 \text{ k}\Omega, \text{ C}_{L} = 100 \text{ pF}, \text{ unity gain}$		25°C		0.4		V/µs
φ _m	Phase margin					60		0
CMRR	Common-mode rejection ratio	R _S ≤ 10 kΩ	R _S ≤ 10 kΩ		65	85		dB
ISOURCE	Output source current	$V_{CC} = 15 \text{ V}, \text{ V}_{O} = 2 \text{ V},$	V _{ID} = 1 V	25°C	20	40		mA
	Output sink current	V _{CC} = 15 V, V _{ID} = 1 V	$V_0 = 2 V$	25°C	10	20		mA
I _{SINK}		$v_{\rm CC} = 15$ v, $v_{\rm ID} = 1$ v	$V_{0} = 0.2 V$	25°C	12	50		μA
I _O	Short-circuit to GND	V _{CC} = 15 V		25°C		40	60	mA
SVR	Supply-voltage rejection ratio	$V_{CC} = 5 V \text{ to } 30 V$		25°C	65	110		dB
I _{CC}	Total supply current	Nalaad	$V_{CC} = 5 V$	25°C 500 600		500	800	
			$V_{CC} = 30 V$		900			
		No load	$V_{CC} = 5 V$			600	900	μA
		$V_{\rm CC} = 30$		Full range			1000	
THD	Total harmonic distortion	$V_{CC} = 30 \text{ V}, V_{O} = 2 \text{ V}_{pp}, A_{V} = 20 \text{ dB}, R_{L} = 2 \text{ k}\Omega, \text{ f} = 1 \text{ kHz}, C_{L} = 100 \text{ pF}$		25°C		0.015		%
e _N	Equivalent input noise voltage	V _{CC} = 30 V, f = 1 kHz,	R _S = 100 Ω	25°C		50		nV/√Hz

The direction of the input current is out of the device. This current essentially is constant, independent of the state of the output, so no (1) loading change exists on the input lines.

The input common-mode voltage of either input signal should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is $V_{CC+} - 1.5$ V, but either or both inputs can go to 32 V without damage. (2)

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NSTRUMENTS

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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing		ickage Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
TS321QDBVRQ1	ACTIVE	SOT-23	DBV	5 3	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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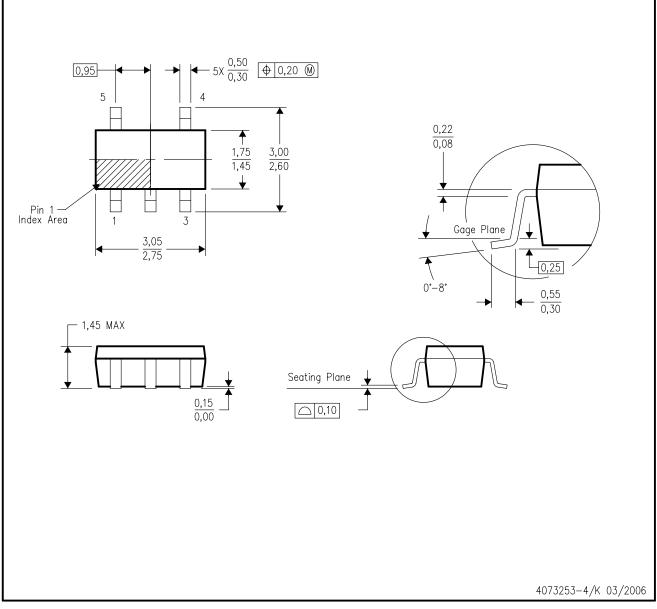
Catalog: TS321

NOTE: Qualified Version Definitions:

• Catalog - TI's standard catalog product

DBV (R-PDSO-G5)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
- D. Falls within JEDEC MO-178 Variation AA.



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