

## F100171

### Triple 4-Input Multiplexer with Enable

#### General Description

The F100171 contains three 4-input multiplexers which share a common decoder (inputs  $S_0$  and  $S_1$ ). Output buffer gates provide true and complement outputs. A HIGH on the Enable input ( $\bar{E}$ ) forces all true outputs LOW (see Truth Table). All inputs have 50 k $\Omega$  pull-down resistors.

Refer to the F100371 datasheet for:

PCC packaging

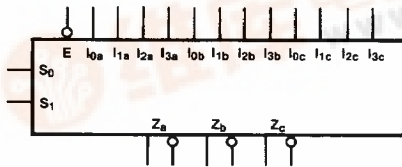
Lower power

Military versions

Extended voltage specs ( $-4.2V$  to  $-5.7V$ )

**Ordering Code:** See Section 8

#### Logic Symbol

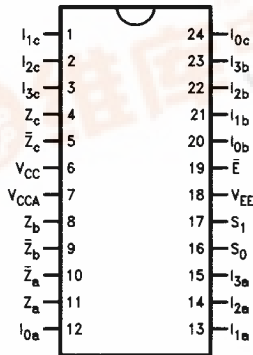


TL/F/9869-3

Pin Names	Description
$I_{0x}-I_{3x}$	Data Inputs
$S_0, S_1$	Select Inputs
$\bar{E}$	Enable Input (Active LOW)
$Z_a-Z_c$	Data Outputs
$\bar{Z}_a-\bar{Z}_c$	Complementary Data Outputs

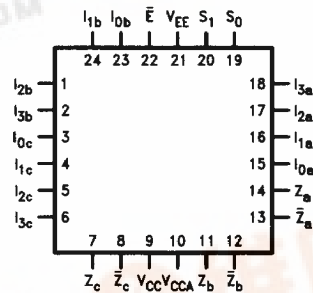
#### Connection Diagrams

24-Pin DIP



TL/F/9869-1

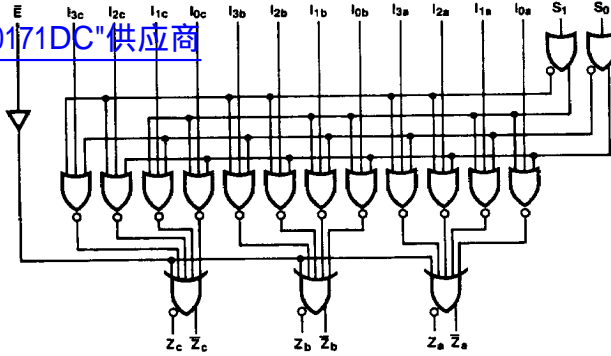
24-Pin Quad Cerpak



TL/F/9869-2

**Logic Diagram**

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**Truth Table**

Inputs			Outputs
$\bar{E}$	$S_0$	$S_1$	$Z_n$
L	L	L	$I_{0x}$
L	H	L	$I_{1x}$
L	L	H	$I_{2x}$
L	H	H	$I_{3x}$
H	X	X	L

H = HIGH Voltage Level  
 L = LOW Voltage Level  
 X = Don't Care

## Absolute Maximum Ratings

Above which the useful life may be impaired. (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Storage Temperature                    -65°C to +150°C  
Maximum Junction Temperature (T<sub>J</sub>)    +150°C

Case Temperature under Bias (T<sub>C</sub>)        0°C to +85°C  
V<sub>EE</sub> Pin Potential to Ground Pin        -7.0V to +0.5V  
Input Voltage (DC)                        V<sub>EE</sub> to +0.5V  
Output Current (DC Output HIGH)        -50 mA  
Operating Range (Note 2)                -5.7V to -4.2V

## DC Electrical Characteristics

V<sub>EE</sub> = -4.5V, V<sub>CC</sub> = V<sub>CCA</sub> = GND, T<sub>C</sub> = 0°C to +85°C (Note 3)

Symbol	Parameter	Min	Typ	Max	Units	Conditions (Note 4)	
V <sub>OH</sub>	Output HIGH Voltage	-1025	-955	-880	mV	V <sub>IN</sub> = V <sub>IH</sub> (Max) or V <sub>IL</sub> (Min)	Loading with 50Ω to -2.0V
V <sub>OL</sub>	Output LOW Voltage	-1810	-1705	-1620			
V <sub>OHC</sub>	Output HIGH Voltage	-1035			mV	V <sub>IN</sub> = V <sub>IH</sub> (Min) or V <sub>IL</sub> (Max)	Loading with 50Ω to -2.0V
V <sub>OLC</sub>	Output LOW Voltage			-1610			
V <sub>IH</sub>	Input HIGH Voltage	-1165		-880	mV	Guaranteed HIGH Signal for All Inputs	
V <sub>IL</sub>	Input LOW Voltage	-1810		-1475	mV	Guaranteed LOW Signal for All Inputs	
I <sub>IL</sub>	Input LOW Current	0.50			μA	V <sub>IN</sub> = V <sub>IL</sub> (Min)	

## DC Electrical Characteristics

V<sub>EE</sub> = -4.2V, V<sub>CC</sub> = V<sub>CCA</sub> = GND, T<sub>C</sub> = 0°C to +85°C (Note 3)

Symbol	Parameter	Min	Typ	Max	Units	Conditions (Note 4)	
V <sub>OH</sub>	Output HIGH Voltage	-1020		-870	mV	V <sub>IN</sub> = V <sub>IH</sub> (Max) or V <sub>IL</sub> (Min)	Loading with 50Ω to -2.0V
V <sub>OL</sub>	Output LOW Voltage	-1810		-1605			
V <sub>OHC</sub>	Output HIGH Voltage	-1030			mV	V <sub>IN</sub> = V <sub>IH</sub> (Min) or V <sub>IL</sub> (Max)	Loading with 50Ω to -2.0V
V <sub>OLC</sub>	Output LOW Voltage			-1595			
V <sub>IH</sub>	Input HIGH Voltage	-1150		-870	mV	Guaranteed HIGH Signal for All Inputs	
V <sub>IL</sub>	Input LOW Voltage	-1810		-1475	mV	Guaranteed LOW Signal for All Inputs	
I <sub>IL</sub>	Input LOW Current	0.50			μA	V <sub>IN</sub> = V <sub>IL</sub> (Min)	

## DC Electrical Characteristics

V<sub>EE</sub> = -4.8V, V<sub>CC</sub> = V<sub>CCA</sub> = GND, T<sub>C</sub> = 0°C to +85°C (Note 3)

Symbol	Parameter	Min	Typ	Max	Units	Conditions (Note 4)	
V <sub>OH</sub>	Output HIGH Voltage	-1035		-880	mV	V <sub>IN</sub> = V <sub>IH</sub> (Max) or V <sub>IL</sub> (Min)	Loading with 50Ω to -2.0V
V <sub>OL</sub>	Output LOW Voltage	-1830		-1620			
V <sub>OHC</sub>	Output HIGH Voltage	-1045			mV	V <sub>IN</sub> = V <sub>IH</sub> (Min) or V <sub>IL</sub> (Max)	Loading with 50Ω to -2.0V
V <sub>OLC</sub>	Output LOW Voltage			-1810			
V <sub>IH</sub>	Input HIGH Voltage	-1165		-880	mV	Guaranteed HIGH Signal for All Inputs	
V <sub>IL</sub>	Input LOW Voltage	-1830		-1490	mV	Guaranteed LOW Signal for All Inputs	
I <sub>IL</sub>	Input LOW Current	0.50			μA	V <sub>IN</sub> = V <sub>IL</sub> (Min)	

**Note 1:** Absolute maximum ratings are those values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

**Note 2:** Parametric values specified at -4.2V to -4.8V.

**Note 3:** The specified limits represent the "worst case" value for the parameter. Since these "worst case" values normally occur at the temperature extremes, additional noise immunity and guard banding can be achieved by decreasing the allowable system operating ranges.

**Note 4:** Conditions for testing shown in the tables are chosen to guarantee operation under "worst case" conditions.

**DC Electrical Characteristics** $V_{EE} = -4.2V$  to  $-4.8V$  unless otherwise specified,  $V_{CC} = V_{CCA} = GND$ ,  $T_C = 0^\circ C$  to  $+85^\circ C$ 

Symbol	Parameter	Min	Typ	Max	Units	Conditions
$I_{IH}$	Input HIGH Current $I_{0x-I3x}$ $S_0, S_1, \bar{E}$			340 300	$\mu A$	$V_{IN} = V_{IH(max)}$
$I_{EE}$	Power Supply Current	-114	-80	-56	mA	Inputs Open

**Ceramic Dual-In-Line Package AC Electrical Characteristics** $V_{EE} = -4.2V$  to  $-4.8V$ ,  $V_{CC} = V_{CCA} = GND$ 

Symbol	Parameter	$T_C = 0^\circ C$		$T_C = +25^\circ C$		$T_C = +85^\circ C$		Units	Conditions
		Min	Max	Min	Max	Min	Max		
$t_{PLH}$ $t_{PHL}$	Propagation Delay $I_{0x-I3x}$ to Output	0.45	1.70	0.45	1.60	0.50	1.70	ns	Figures 1 and 2
$t_{PLH}$ $t_{PHL}$	Propagation Delay $S_0, S_1$ to Output	0.90	2.40	0.90	2.60	1.00	3.00	ns	
$t_{PLH}$ $t_{PHL}$	Propagation Delay $\bar{E}$ to Output	0.65	2.40	0.65	2.30	0.75	2.40	ns	
$t_{TLH}$ $t_{THL}$	Transition Time 20% to 80%, 80% to 20%	0.45	1.80	0.45	1.60	0.45	1.60	ns	

**Cerpak AC Electrical Characteristics** $V_{EE} = -4.2V$  to  $-4.8V$ ,  $V_{CC} = V_{CCA} = GND$ 

Symbol	Parameter	$T_C = 0^\circ C$		$T_C = +25^\circ C$		$T_C = +85^\circ C$		Units	Conditions
		Min	Max	Min	Max	Min	Max		
$t_{PLH}$ $t_{PHL}$	Propagation Delay $I_{0x-I3x}$ to Output	0.45	1.50	0.45	1.40	0.50	1.50	ns	Figures 1 and 2
$t_{PLH}$ $t_{PHL}$	Propagation Delay $S_0, S_1$ to Output	0.90	2.20	0.90	2.40	1.00	2.80	ns	
$t_{PLH}$ $t_{PHL}$	Propagation Delay $\bar{E}$ to Output	0.65	2.20	0.65	2.10	0.75	2.20	ns	
$t_{TLH}$ $t_{THL}$	Transition Time 20% to 80%, 80% to 20%	0.45	1.70	0.45	1.50	0.45	1.50	ns	

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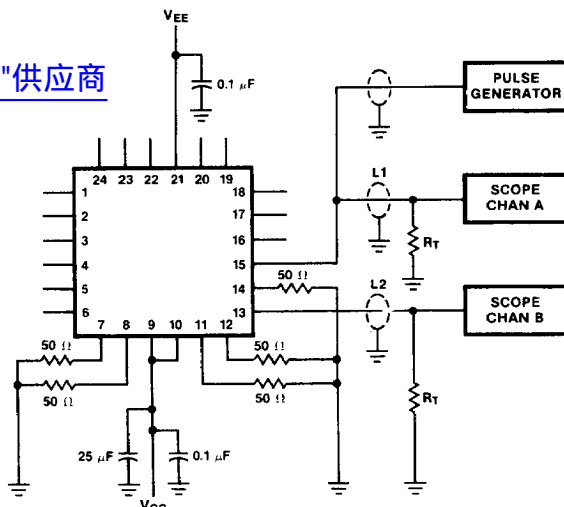


FIGURE 1. AC Test Circuit

TL/F/9869-6

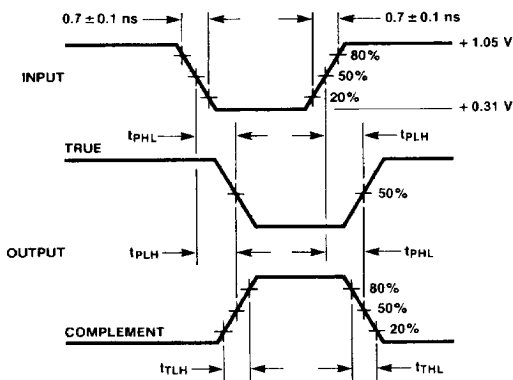


FIGURE 2. Propagation Delay and Transition Times

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**Notes:**

$V_{CC}, V_{CCA} = +2V, V_{EE} = -2.5V$

L1 and L2 = equal length 50 $\Omega$  impedance lines

$R_T = 50\Omega$  terminator internal to scope

Decoupling 0.1  $\mu F$  from GND to  $V_{CC}$  and  $V_{EE}$

All unused outputs are loaded with 50 $\Omega$  to GND

$C_L$  = Fixture and stray capacitance  $\leq 3$  pF

Pin numbers shown are for flatpak; for DIP see logic symbol