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ULTRA SMALL, LOW-INPUT VOLTAGE, LOW ron LOAD SWITCH

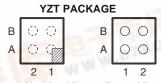
Check for Samples: TPS22907

FEATURES

- Low Input Voltage: 1.1 V to 3.6 V
- Ultra-Low ON-State Resistance
 - $r_{ON} = 44 \text{ m}\Omega \text{ at } V_{IN} = 3.6 \text{ V}$
 - r_{ON} = 50 mΩ at V_{IN} = 2.5 V
 - r_{ON} = 58 mΩ at V_{IN} = 1.8 V
 - $r_{ON} = 83 \text{ m}\Omega \text{ at } V_{IN} = 1.2 \text{ V}$
- 1-A Maximum Continuous Switch Current
- Quiescent Current < 1 μA
- Shutdown Current < 1 μA
- Low Control Input Thresholds Enable Use of Low-Voltage Logic
- Controlled Slew Rate to Avoid Inrush Currents
- ESD Performance Tested Per JESD 22
 - 3000-V Human-Body Model (A114-B, Class II)
 - 1000-V Charged-Device Model (C101)
- Ultra Small Four-Terminal Wafer Chip Scale Package (WCSP)
 - 0.9 mm × 0.9 mm,
 0.5-mm Pitch, 0.5-mm Height

APPLICATIONS

- Battery Powered Equipment
- Portable Industrial Equipment
- Portable Medical Equipment
- Portable Media Players
- Point Of Sales Terminal
- GPS Devices
- Digital Cameras
- Portable Instrumentation
- Smartphones



Laser Marking View

Bump View

Table 1. TERMINAL ASSIGNMENTS

•		2	1
	Α	V _{IN}	V _{OUT}
	В	ON	GND

DESCRIPTION

The TPS22907 is an ultra small, low R_{ON} load switch with controlled turn on. The device contains a P-channel MOSFET that operates over an input voltage range of 1.1 V to 3.6 V. The switch is controlled by an on/off input (ON), which is capable of interfacing directly with low-voltage control signals.

The TPS22907 is available in a space-saving 4-terminal WCSP with 0.5-mm pitch (YZT). The device is characterized for operation over the free-air temperature range of -40°C to 85°C.

DEVICE	r _{ON} (TYP at 1.8 V)	SLEW RATE (TYP at 1.8 V)	MAXIMUM OUTPUT CURRENT	ENABLE
TPS22907	58 mΩ	36 µs	1000 mA	Active high

ORDERING INFORMATION

T _A	PACKAGE ⁽¹⁾ (2)		ORDERABLE PART NUMBER	TOP-SIDE MARKING
-40°C to 85°C	WCSP – YZT (0.5-mm pitch)	Tape and reel	TPS22907YZTR	5K_ ⁽³⁾

(1) Package drawings, thermal data, and symbolization are available at www.ti.com/packaging.

- (2) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI website at www.ti.com.
- (3) The actual top-side marking has three preceding characters to denote year, month, and sequence code, and one following character to designate the wafer fab/assembly site. Pin 1 identifier indicates solder-bump composition (1 = SnPb, = Pb-free).

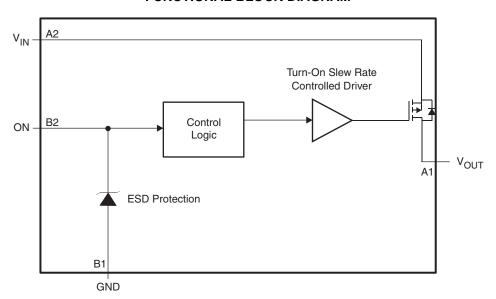


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FUNCTIONAL BLOCK DIAGRAM



FUNCTION TABLE

ON (Control Input)	V _{IN} to V _{OUT}
L	OFF
Н	ON

TERMINAL FUNCTIONS

TERN	ЛINAL	DESCRIPTION
BALL NO.	NAME	DESCRIPTION
A1	V _{OUT}	Switch output
A2	V _{IN}	Switch input. Bypass this input with a ceramic capacitor to ground.
B1	GND	Ground
B2	ON	Switch control input. Active high, do not leave floating



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ABSOLUTE MAXIMUM RATINGS(1)

			MIN	MAX	UNIT
V _{IN}	Input voltage range		-0.3	4	V
V _{OUT}	Output voltage range			V _{IN} + 0.3	V
V _{ON}	Input voltage range		-0.3	4	V
I _{MAX}	Maximum continuous switch current, T	A = -40°C to 85°C		1	Α
I _{PLS}	Maximum pulsed current (100-µs pulse	e, 2% duty cycle), T _A = -40°C to 85°C		2.7	Α
T _A	Operating free-air temperature range	Maximum pulsed current (100-μs pulse, 2% duty cycle), T _A = -40°C to 85°C Operating free-air temperature range		85	°C
T _{stg}	Storage temperature range		-65	150	°C
		Human-Body Model (HBM)		3000	
ESD	Electrostatic discharge protection	Charged-Device Model (CDM)		1000	V

⁽¹⁾ Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

DISSIPATION RATINGS

BOARD	PACKAGE	R _{θJC}	R _{θJA}	DERATING FACTOR ABOVE T _A = 25°C	T _A < 25°C	T _A = 70°C	T _A = 85°C
High-K ⁽¹⁾	YZT	28.6°C/W	116.32°C/W	-8.5969 mW/°C	859.69 mW	472.83 mW	343.87 mW

⁽¹⁾ The JEDEC high-K (2s2p) board used to derive this data was a 3- x 3-inch, multilayer board with 1-ounce internal power and ground planes and 2-ounce copper traces on top and bottom of the board.

RECOMMENDED OPERATING CONDITIONS

		MIN	MAX	UNIT
V _{IN}	Input voltage range	1.1	3.6	V
V _{OUT}	Output voltage range		V_{IN}	V
V _{IH}	High-level input voltage, ON	0.85	3.6	V
V _{IL}	Low-level input voltage, ON		0.4	V
C _{IN}	Input capacitor	1 ⁽¹⁾		μF

Product Folder Link(s): TPS22907

(1) See Application Information.





ELECTRICAL CHARACTERISTICS

 $V_{IN} = 1.1 \text{ V to } 3.6 \text{ V (unless otherwise noted)}$

v _{IN} — 1.1 v t	o o.o v (arricos ourierwise riot	cu)				
	PARAMETER	TEST CONDITIONS	T _A ⁽¹⁾	MIN TYP	(2) MAX	UNIT
lin	Quiescent current	$I_{OUT} = 0$, $V_{IN} = V_{ON}$		0.0)7 1	μA
I _{IN(OFF)}	Off supply current	V _{ON} = GND, OUT = Open		0.0)5 1	μA
I _{IN(LEAKAGE)}	Leakage current	V _{ON} = GND, V _{OUT} = 0		0.0)5 1	μΑ
		V 26V L 200 mA	25°C	4	14 60	
I _{IN(OFF)} Off supply current V _{ON} I _{IN(LEAKAGE)} Leakage current V _{IN}		$V_{IN} = 3.6 \text{ V}, I_{OUT} = -200 \text{ mA}$	Full range		67	
		V 2.5.V I 200 mA	25°C		50 63	
		$V_{IN} = 2.5 \text{ V}, I_{OUT} = -200 \text{ mA}$	Full range		70	
		V 4.0.V I 200 mA	25°C		58 72	mΩ
	ON-State resistance	$V_{IN} = 1.8 \text{ V}, I_{OUT} = -200 \text{ mA}$	Full range		80	111122
	V 4.0.V I 200 m A	25°C	8	33 106		
		$V_{IN} = 1.2 \text{ V}, I_{OUT} = -200 \text{ mA}$	Full range		117	
		V 44 V I 200 mA	25°C	9	7 125	
		$V_{IN} = 1.1 \text{ V}, I_{OUT} = -200 \text{ mA}$	Full range		140	
I _{ON}	ON input leakage current	V _{ON} = 1.1 V to 3.6 V or GND	Full range	0.00)5 1	μΑ

SWITCHING CHARACTERISTICS

 $V_{IN} = 3.6 \text{ V}, T_A = 25^{\circ}\text{C}$ (unless otherwise noted)

PARAMETER ton Turn-ON time		TEST CC	MIN	TYP	MAX	UNIT	
t _{ON}	Turn-ON time	$R_L = 500 \Omega$	C _L = 0.1 μF		28		μs
t _{OFF}	Turn-OFF time	$R_L = 500 \Omega$	$C_L = 0.1 \ \mu F$		40		μs
t _r	V _{OUT} rise time	$R_L = 500 \Omega$	$C_L = 0.1 \ \mu F$		25		μs
t _f	V _{OUT} fall time	$R_L = 500 \Omega$	$C_L = 0.1 \ \mu F$		116		μs

SWITCHING CHARACTERISTICS

 $V_{IN} = 1.8 \text{ V}, T_A = 25^{\circ}\text{C}$ (unless otherwise noted)

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	PARAMETER	TEST	CONDITIONS	MIN	TYP	MAX	UNIT
t _{ON}	Turn-ON time	$R_L = 500 \Omega$	$C_L = 0.1 \ \mu F$		48		μs
t _{OFF}	Turn-OFF time	R _L = 500 Ω	C _L = 0.1 μF		40		μs
t _r	V _{OUT} rise time	$R_L = 500 \Omega$	$C_L = 0.1 \ \mu F$		36		μs
t _f	V _{OUT} fall time	$R_L = 500 \Omega$	$C_L = 0.1 \ \mu F$		113		μs

SWITCHING CHARACTERISTICS

 $V_{IN} = 1.1 \text{ V}, T_A = 25^{\circ}\text{C}$ (unless otherwise noted)

IIN	, ,	· · · · · · · · · · · · · · · · · · ·					
PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT
t _{ON}	Turn-ON time	R _L = 500 Ω	C _L = 0.1 μF		81		μs
t _{OFF}	Turn-OFF time	$R_L = 500 \Omega$	$C_L = 0.1 \ \mu F$		42		μs
t _r	V _{OUT} rise time	$R_L = 500 \Omega$	$C_L = 0.1 \ \mu F$		57		μs
t _f	V _{OUT} fall time	$R_L = 500 \Omega$	$C_L = 0.1 \ \mu F$		113		μs

Product Folder Link(s): TPS22907

⁽¹⁾ Full range $T_A = -40^{\circ}\text{C}$ to 85°C (2) Typical values are at the specified V_{IN} and $T_A = 25^{\circ}\text{C}$.



TYPICAL CHARACTERISTICS

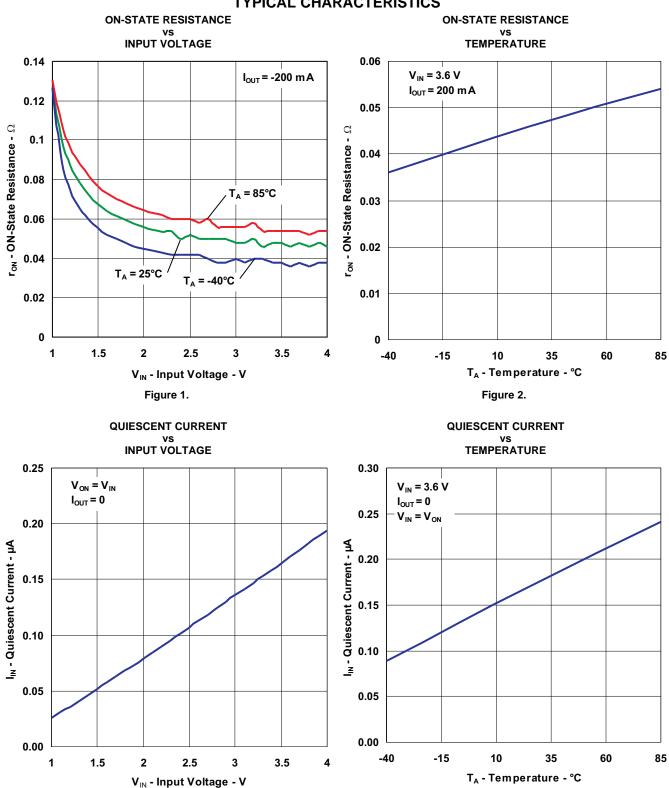


Figure 3.

Figure 4.



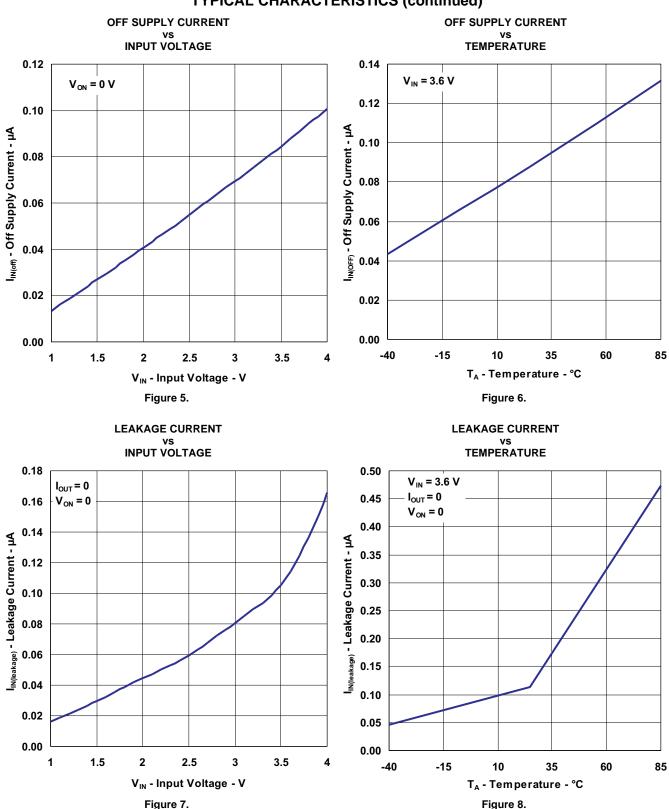
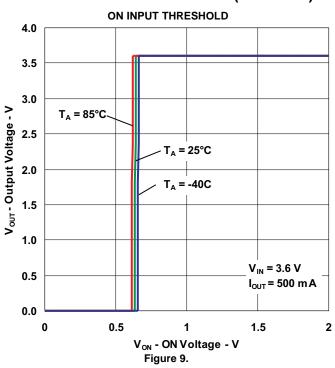
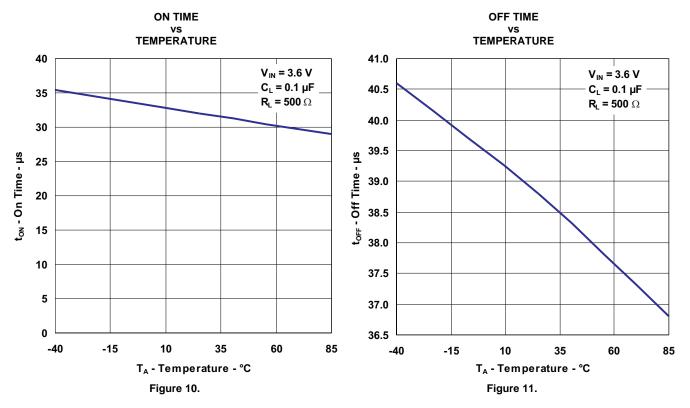


Figure 7.









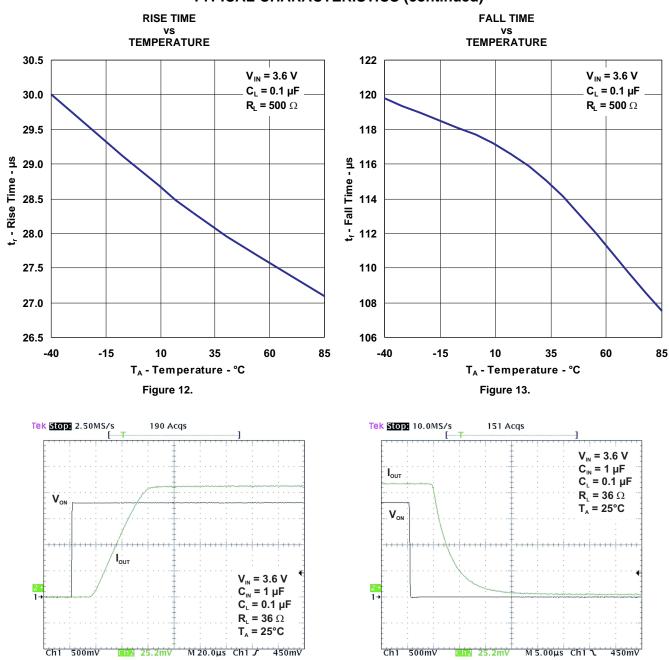


Figure 14. t_{ON} RESPONSE

Figure 15. t_{OFF} RESPONSE





Figure 16. t_{ON} RESPONSE



Figure 17. t_{OFF} RESPONSE

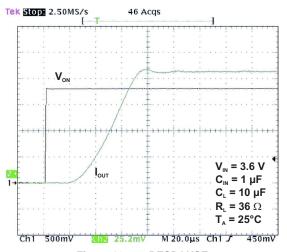


Figure 18. t_{ON} RESPONSE

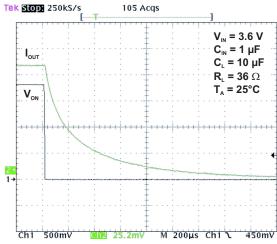


Figure 19. t_{OFF} RESPONSE

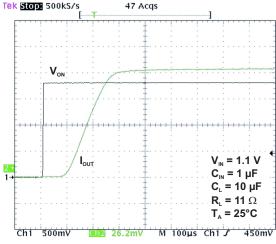


Figure 20. t_{ON} RESPONSE

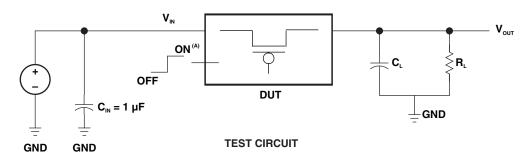


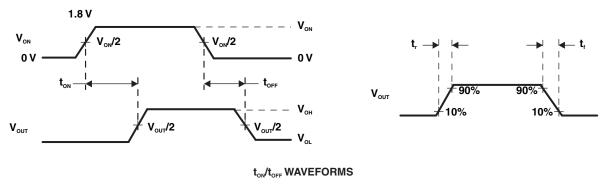
Figure 21. t_{OFF} RESPONSE

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PARAMETER MEASUREMENT INFORMATION





A. t_{rise} and t_{fall} of the control signal is 100 ns.

Figure 22. Test Circuit and $t_{\text{ON}}/t_{\text{OFF}}$ Waveforms

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APPLICATION INFORMATION

On/Off Control

The ON pin controls the state of the switch. Activating ON continuously holds the switch in the on state so long as there is no fault. ON is active high and has a low threshold, making it capable of interfacing with low-voltage signals. The ON pin is compatible with standard GPIO logic threshold. It can be used with any microcontroller with 1.2-V, 1.8-V, 2.5-V, or 3.3-V GPIOs.

Input Capacitor

To limit the voltage drop on the input supply caused by transient in-rush currents when the switch turns on into a discharged load capacitor or short-circuit, a capacitor needs to be placed between V_{IN} and GND. A 1- μ F ceramic capacitor, C_{IN} , place close to the pins is usually sufficient. Higher values of C_{IN} can be use to further reduce the voltage drop during high-current application. When switching heavy loads, it is recommended to have an input capacitor approximately ten times higher than the output capacitor to avoid excessive voltage drop.

Output Capacitor

Due to the integral body diode in the PMOS switch, a C_{IN} greater than C_L is highly recommended. A C_L greater than C_{IN} can cause V_{OUT} to exceed V_{IN} when the system supply is removed. This could result in current flow through the body diode from V_{OUT} to V_{IN} .

Board Layout

For best performance, all traces should be as short as possible. To be most effective, the input and output capacitors should be placed close to the device to minimize the effects that parasitic trace inductances may have on normal and short-circuit operation. Using wide traces for V_{IN} , V_{OUT} , and GND helps minimize the parasitic electrical effects along with minimizing the case to ambient thermal impedance.

Product Folder Link(s): TPS22907



PACKAGE OPTION ADDENDUM

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23-Dec-2009

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins F	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
TPS22907YZTR	ACTIVE	DSBGA	YZT	4	3000	Green (RoHS & no Sb/Br)	SNAGCU	Level-1-260C-UNLIM
TPS22907YZTT	ACTIVE	DSBGA	YZT	4	250	Green (RoHS & no Sb/Br)	SNAGCU	Level-1-260C-UNLIM

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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TAPE AND REEL INFORMATION





	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

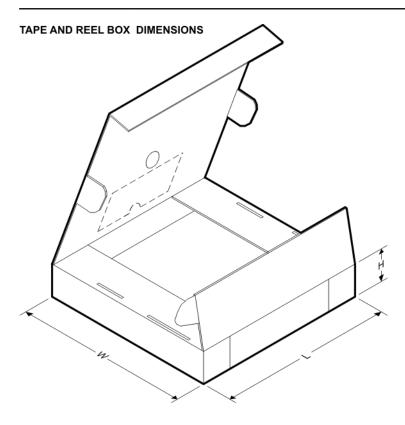


*All dimensions are nominal

Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TPS22907YZTR	DSBGA	YZT	4	3000	178.0	9.2	1.0	1.0	0.73	4.0	8.0	Q1
TPS22907YZTT	DSBGA	YZT	4	250	178.0	9.2	1.0	1.0	0.73	4.0	8.0	Q1

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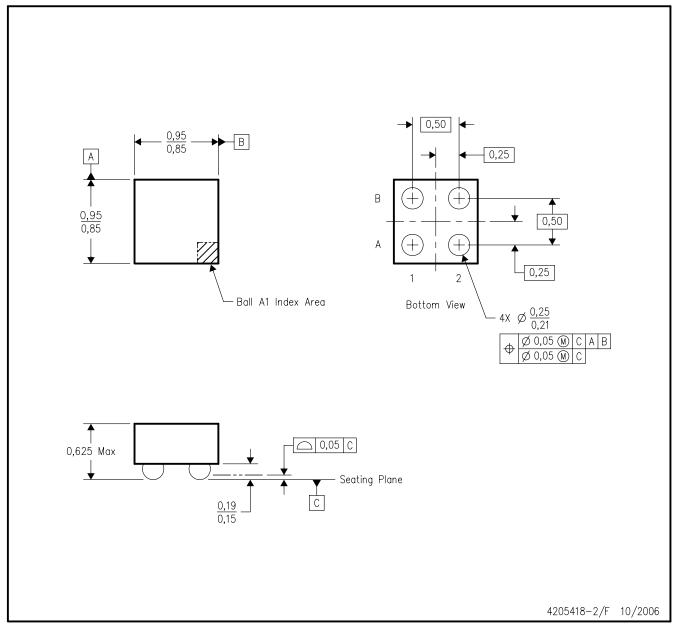


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TPS22907YZTR	DSBGA	YZT	4	3000	220.0	220.0	35.0
TPS22907YZTT	DSBGA	YZT	4	250	220.0	220.0	35.0

YZT (S-XBGA-N4)

DIE-SIZE BALL GRID ARRAY



NOTES: A.

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. NanoFree™ package configuration.
- D. This package is Lead-free. Refer to the 4 YET package (drawing 4205421) for tin-lead (SnPb).

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