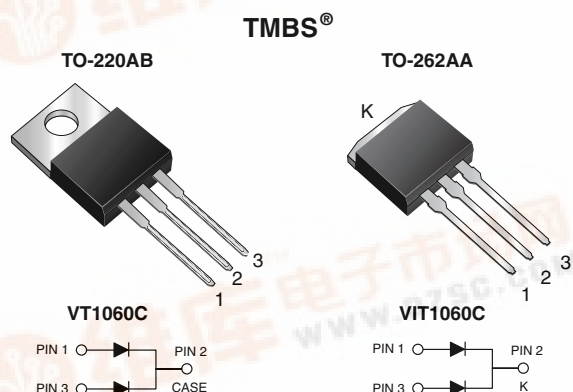


Dual High-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.39\text{ V}$ at $I_F = 2.5\text{ A}$ 

FEATURES

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Solder bath temperature 275 °C max. 10 s, per JESD 22-B106
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

RoHS
COMPLIANT
HALOGEN
FREE

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

PRIMARY CHARACTERISTICS

$I_{F(AV)}$	2 x 5.0 A
V_{RRM}	60 V
I_{FSM}	100 A
V_F at $I_F = 5.0\text{ A}$	0.50 V
T_J max.	150 °C

MECHANICAL DATA

Case: TO-220AB and TO-262AA

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS compliant, and commercial grade

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS ($T_A = 25\text{ °C}$ unless otherwise noted)

PARAMETER	SYMBOL	VT1060C	VIT1060C	UNIT
Maximum repetitive peak reverse voltage	V_{RRM}	60		V
Maximum average forward rectified current (fig. 1)	$I_{F(AV)}$	10		A
		5		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I_{FSM}	100		A
Voltage rate of change (rated V_R)	dV/dt	10 000		V/ μ s
Operating junction and storage temperature range	T_J, T_{STG}	- 55 to + 150		°C

VT1060C, VIT1060C

Vishay General Semiconductor

**ELECTRICAL CHARACTERISTICS** ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)

PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 2.5 A	T _A = 25 °C	V _F ⁽¹⁾	0.49	-	V
	I _F = 5.0 A			0.58	0.70	
	I _F = 2.5 A	T _A = 125 °C		0.39	-	
	I _F = 5.0 A			0.50	0.60	
Reverse current per diode	V _R = 60 V	T _A = 25 °C	I _R ⁽²⁾	-	700	μA
		T _A = 125 °C		6.6	25	mA

Notes(1) Pulse test: 300 μs pulse width, 1 % duty cycle(2) Pulse test: Pulse width $\leq 40\text{ ms}$ **THERMAL CHARACTERISTICS** ($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	VT1060C	VIT1060C	UNIT
Typical thermal resistance	per diode	R _{θJC}	3.5		°C/W
	per device		2.5		

ORDERING INFORMATION (Example)

PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
TO-220AB	VT1060C-M3/4W	1.87	4W	50/tube	Tube
TO-262AA	VIT1060C-M3/4W	1.45	4W	50/tube	Tube

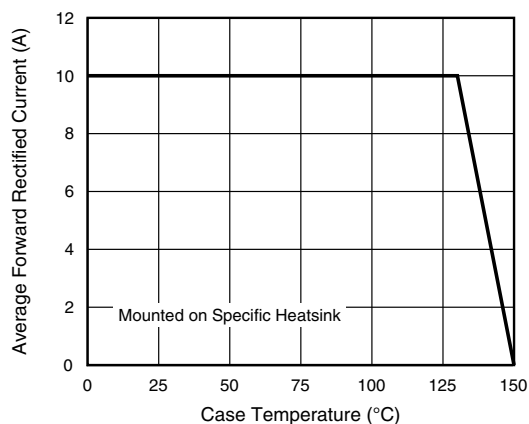
RATINGS AND CHARACTERISTICS CURVES($T_A = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)

Fig. 1 - Maximum Forward Current Derating Curve

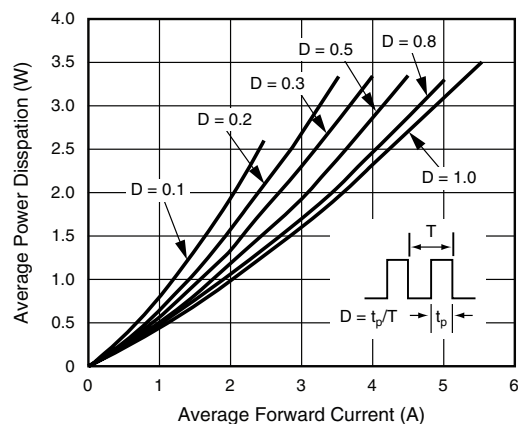


Fig. 2 - Forward Power Dissipation Characteristics

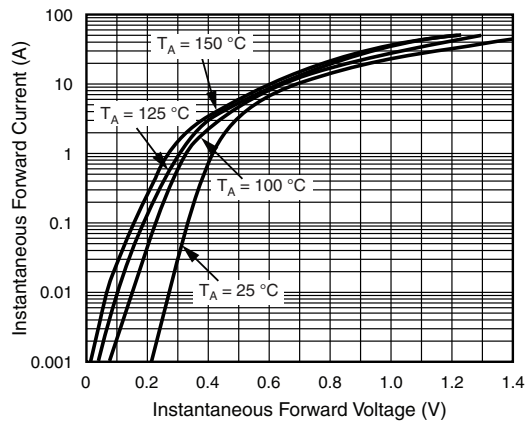


Fig. 3 - Typical Instantaneous Forward Characteristics

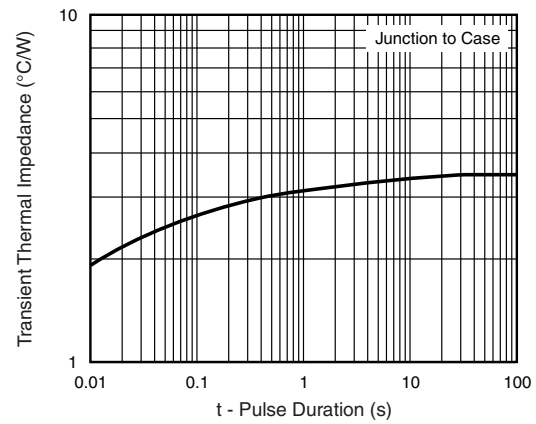


Fig. 5 - Typical Transient Thermal Impedance

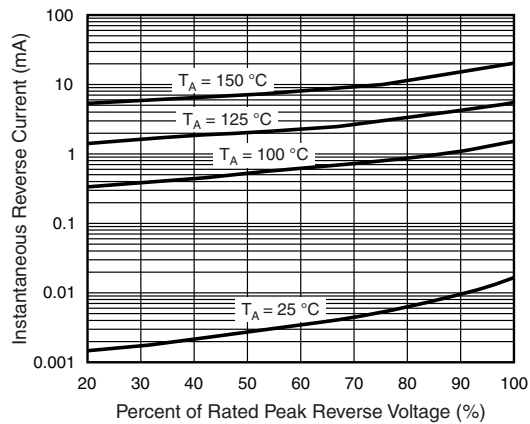


Fig. 4 - Typical Reverse Characteristics

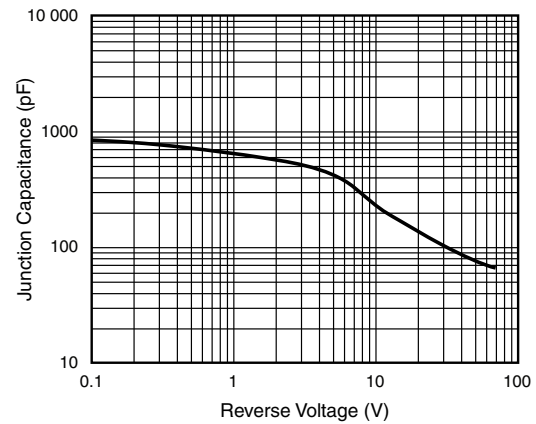


Fig. 6 - Typical Junction Capacitance

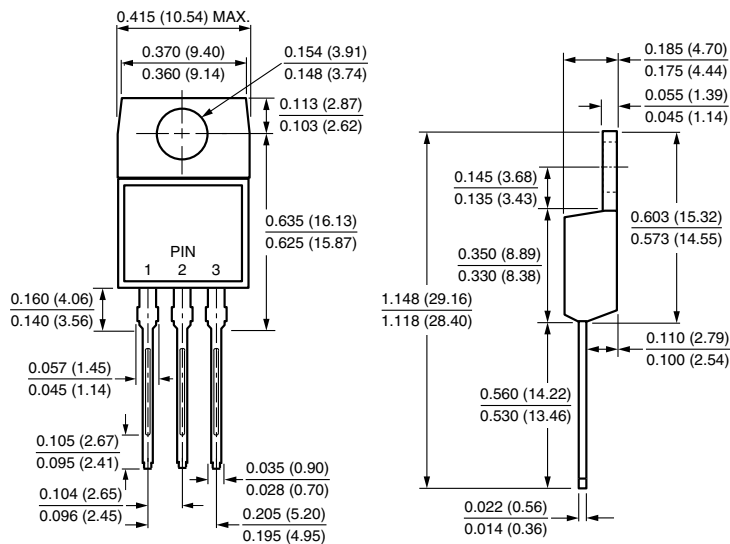
VT1060C, VIT1060C

Vishay General Semiconductor

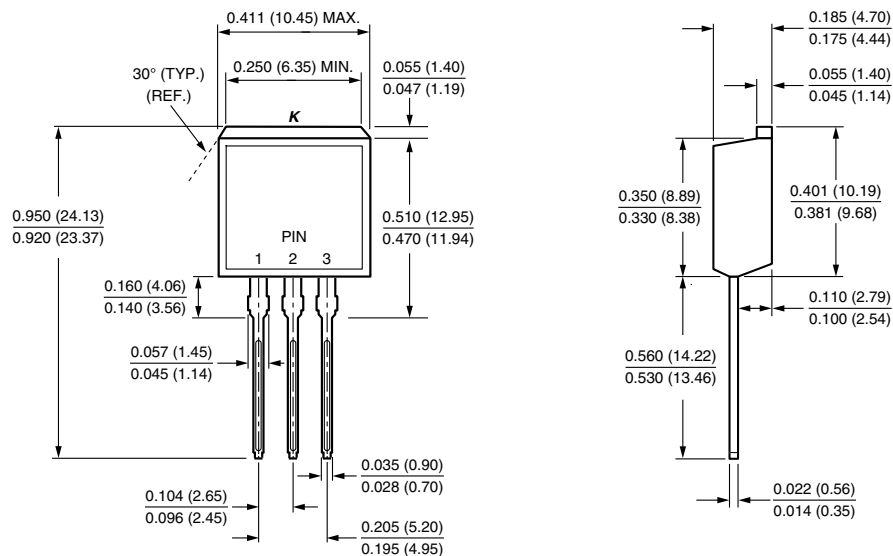


PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

TO-220AB



TO-262AA



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All product specifications and data are subject to change without notice.

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