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查询"K1381" 他 Sield Effect Transistor Silicon N Channel MOS Type (L²-π-MOSIII)

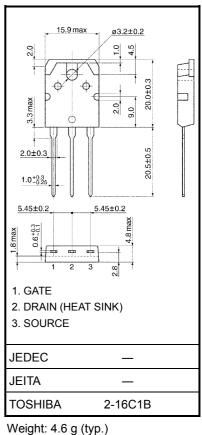
2SK1381

Relay Drive, Motor Drive and DC-DC Converter Applications

- 4 V gate drive
- Low drain-source ON resistance $: R_{DS} (ON) = 25 m\Omega (typ.)$
- High forward transfer admittance $: |Y_{fs}| = 33 \text{ S (typ.)}$
- Low leakage current $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 100 \ V)$
- Enhancement-mode $V_{th} = 0.8 \sim 2.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{ID} = 1 \text{ mA})$

Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	100	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	100	V	
Gate-source voltage		V _{GSS}	±20	V	
Drain current	DC (Note 1)	۱ _D	50	А	
	Pulse (Note 1)	I _{DP}	200	~	
Drain power dissipation (Tc = 25°C)		PD	150	W	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch−c)}	0.833	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	50	°C / W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

This transistor is an electrostatic sensitive device. Please handle with caution.

Unit: mm

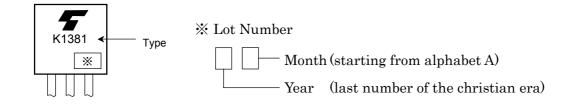
Etrical Characteristics (Ta = 25°C)

Charae	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	V _{GS} = ±20 V, V _{DS} = 0 V		_	±50	nA
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V		_	100	μA
Drain-source bi	reakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	100	_	_	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	0.8	_	2.0	V
Drain-source ON resistance		R _{DS (ON)}	V _{GS} = 4 V, I _D = 25 A		31	46	
			V _{GS} = 10 V, I _D = 25 A		25	32	mΩ
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 25 A	20	33	_	S
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		3700	_	pF
Reverse transfer capacitance		C _{rss}			580	_	
Output capacitance		Coss	1		1500	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{}_{0V} \int_{V} \int_{R_L=2\Omega} V_{OUT}$ $R_L=2\Omega$ $V_{DD}=50V$ $Duty \leq 1\%, t_w = 10\mu s$	_	16	_	- ns
	Turn-on time	t _{on}		_	46	_	
	Fall time	t _f		_	60	_	
	Turn-off time	t _{off}		_	185	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	88	_	nC
Gate-source charge		Q _{gs}	V _{DD} ≈ 80 V, V _{GS} = 10 V, I _D = 50 A	_	62	—	
Gate-drain ("miller") charge		Q _{gd}]		26	_]

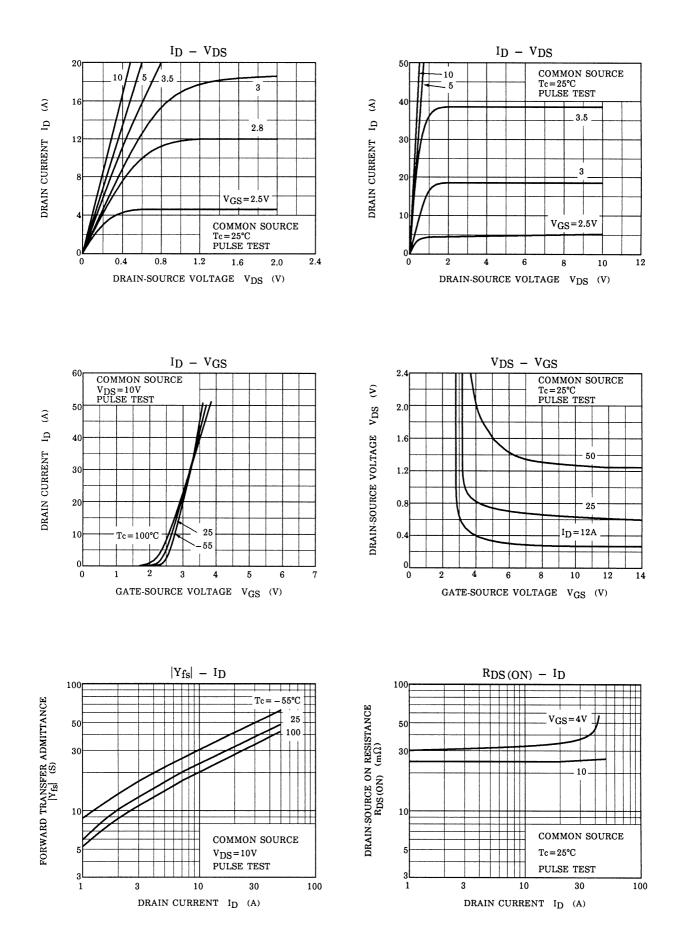
Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	50	А
Pulse drain reverse current (Note 1)	I _{DRP}	—	_	_	200	А
Forward voltage (diode)	V _{DSF}	I _{DR} = 50 A, V _{GS} = 0 V	_	_	-1.6	V
Reverse recovery time	t _{rr}	I _{DR} = 50 A, V _{GS} = 0 V		280		ns
Reverse recovered charge	Qrr	dI _{DR} / dt = 50 A / μs		0.56		μC

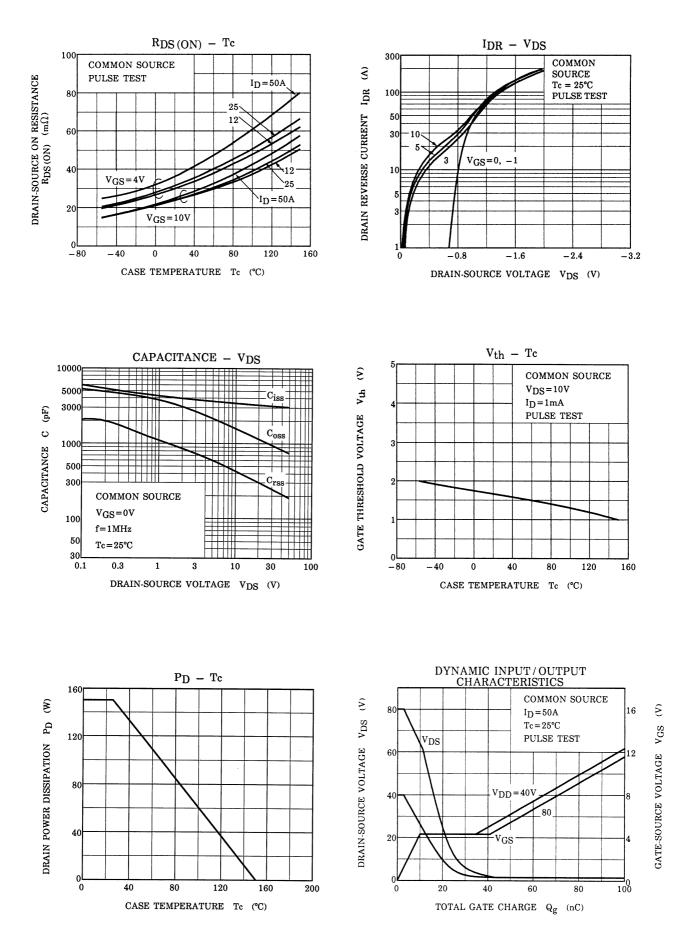
Marking



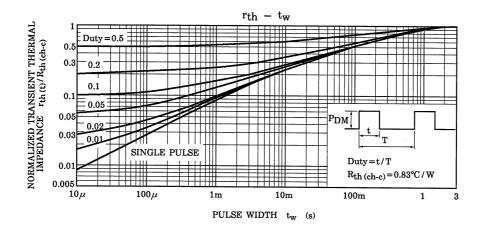
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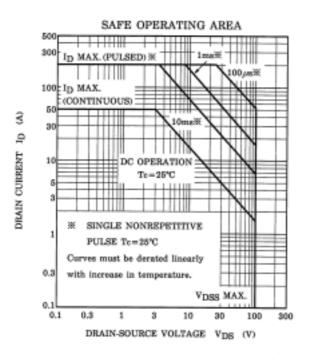


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