

# 54F/74F86 2-Input Exclusive-OR Gate

## **General Description**

This device contains four independent gates, each of which performs the logic exclusive-OR function.

## Ordering Code: See Section 11

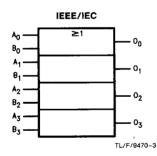
Commercial	Military	Package Number	Package Description					
74F86PC		N14A	14-Lead (0.300" Wide) Molded Dual-in-Line					
	54F86DM (Note 2)	J14A	14-Lead Ceramic Dual-in-Line					
74F86SC (Note 1)		M14A	14-Lead (0.150" Wide) Molded Small Outline, JEDEC					
74F86SJ (Note 1)		M14D	14-Lead (0.300" Wide) Molded Small Outline, EIAJ					
	54F86FM (Note 2)	W14B	14-Lead Cerpack					
	54F86LM (Note 2)	E20A	20-Lead Ceramic Leadless Chip Carrier, Type C					

Note 1: Devices also available in 13" reel. Use suffix = SCX and SJX.

Note 2: Military grade device with environmental and burn-in processing. Use suffix = DMQB, FMQB and LMQB.

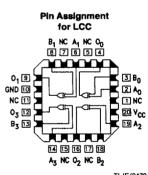
# Logic Symbol

### **Connection Diagrams**



**DIP, SOIC and Flatpak** ۰v<sub>cc</sub> 13 · A, Bn 12 B<sub>2</sub> 00 11 02 A. 10 8. ·A3 01 B<sub>3</sub> 2 GND ο. TL/F/9470-2

**Pin Assignment for** 



TL/F/9470-1

#### Unit Loading/Fan Out: See Section 2 for U.L. definitions

		54F/74F			
Pin Names	Description	U.L. HIGH/LOW	Input I <sub>IH</sub> /I <sub>IL</sub> Output I <sub>OH</sub> /I <sub>OL</sub>		
A <sub>n</sub> , B <sub>n</sub>	Inputs	1.0/1.0	20 µA/−0.6 mA		
On	Outputs	50/33.3	-1 mA/20 mA		

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#### Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified, devices are required, please icontact the National Semiconductor Sales Office/Distributors for availability and specifications.

Storage Temperature	-65°C to +150°C
Ambient Temperature under Bias	-55°C to +125°C
Junction Temperature under Bias Plastic	−55°C to +175°C −55°C to +150°C
V <sub>CC</sub> Pin Potential to Ground Pin	-0.5V to +7.0V
Input Voltage (Note 2)	-0.5V to +7.0V
Input Current (Note 2)	30 mA to + 5.0 mA
Voltage Applied to Output in HIGH State (with $V_{CC} = 0V$ )	
Standard Output	-0.5V to V <sub>CC</sub>
TRI-STATE® Output	-0.5V to +5.5V
Current Applied to Output in LOW State (Max)	twice the rated IOI (mA)

Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

Note 2: Either voltage limit or current limit is sufficient to protect inputs.

# **DC Electrical Characteristics**

# Recommended Operating Conditions

Free Air Ambient Temperature	
Military	-55°C to +125°C
Commercial	0°C to + 70°C
Supply Voltage	
Military	+4.5V to +5.5V
Commercial	+4.5V to +5.5V

Symbol	Parameter Input HIGH Voltage		54F/74F			Units	Nee	Conditions	
			Min	Тур	Max		Vcc	Conditions	
VIH			2.0			v		Recognized as a HIGH Sign	
VIL	Input LOW Voltage				0.8	v		Recognized as a LOW Signa	
V <sub>CD</sub>	Input Clamp Diode Voltage				-1.2	v	Min	$I_{\rm IN} = -18  \rm mA$	
V <sub>OH</sub>	Output HIGH Voltage	54F 10% V <sub>CC</sub> 74F 10% V <sub>CC</sub> 74F 5% V <sub>CC</sub>	2.5 2.5 2.7			v	Min	$I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$	
V <sub>OL</sub>	Output LOW Voltage	54F 10% V <sub>CC</sub> 74F 10% V <sub>CC</sub>			0.5 0.5	v	Min	$i_{OL} = 20 \text{ mA}$ $i_{OL} = 20 \text{ mA}$	
ы	Input HIGH Current	54F 74F			20.0 5.0	μΑ	Max	V <sub>IN</sub> = 2.7V	
I <sub>BVI</sub>	Input HIGH Current Breakdown Test	54F 74F			100 7.0	μA	Max	V <sub>IN</sub> = 7.0V	
ICEX	Output HIGH Leakage Current	54F 74F			250 50	μA	Max	V <sub>OUT</sub> = V <sub>CC</sub>	
V <sub>ID</sub>	Input Leakage Test	74F	4.75			v	0.0	$I_{\rm ID} = 1.9 \mu {\rm A}$ All other pins grounded	
IOD	Output Leakage Circuit Current	74F			3.75	μA	0.0	V <sub>IOD</sub> = 150 mV All other pins grounded	
ΙL	Input LOW Current				-0.6	mA	Max	V <sub>IN</sub> = 0.5V	
los	Output Short-Circuit C	Current	-60		- 150	mA	Max	V <sub>OUT</sub> = 0V	
ССН	Power Supply Current			12	18	mA	Max	V <sub>O</sub> = HIGH	
CCL	Power Supply Current			18	28	mA	Max	$V_0 = LOW$	

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Symbol	查询"54F86	54F		74F		-				
	Parameter	T <sub>A</sub> = +25°C− V <sub>CC</sub> = +5.0V C <sub>L</sub> = 50 pF			T <sub>A</sub> , V <sub>CC</sub> = Mil C <sub>L</sub> = 50 pF		T <sub>A</sub> , V <sub>CC</sub> = Com C <sub>L</sub> = 50 pF		Units	Fig. No.
		Min	Тур	Max	Min	Max	Min	Max		
telh tehl	Propagation Delay A <sub>n</sub> , B <sub>n</sub> to O <sub>n</sub> (Other Input LOW)	3.0 3.0	4.0 4.2	5.5 5.5	2.5 3.0	7.0 7.0	3.0 3.0	6.5 6.5	ns	2-3
tplH tpHL	Propagation Delay A <sub>n</sub> , B <sub>n</sub> to O <sub>n</sub> (Other Input HIGH)	3.5 3.0	5.3 4.7	7.0 6.5	3.5 3.0	8.5 8.0	3.5 3.0	8.0 7.5	ns	2-3

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