

# 1.5SMC6.8AT3 Series

## 1500 Watt Peak Power Zener Transient Voltage Suppressors

### Unidirectional\*

The SMC series is designed to protect voltage sensitive components from high voltage, high energy transients. They have excellent clamping capability, high surge capability, low zener impedance and fast response time. The SMC series is supplied in ON Semiconductor's exclusive, cost-effective, highly reliable Surmetic™ package and is ideally suited for use in communication systems, automotive, numerical controls, process controls, medical equipment, business machines, power supplies and many other industrial/consumer applications.

#### Specification Features:

- Working Peak Reverse Voltage Range – 5.8 to 77.8 V
- Standard Zener Breakdown Voltage Range – 6.8 to 91 V
- Peak Power – 1500 Watts @ 1 ms
- ESD Rating of Class 3 (>16 KV) per Human Body Model
- Maximum Clamp Voltage @ Peak Pulse Current
- Low Leakage < 5  $\mu$ A Above 10 V
- UL 497B for Isolated Loop Circuit Protection
- Maximum Temperature Coefficient Specified
- Response Time is Typically < 1 ns

#### Mechanical Characteristics:

**CASE:** Void-free, transfer-molded, thermosetting plastic

**FINISH:** All external surfaces are corrosion resistant and leads are readily solderable

**MAXIMUM CASE TEMPERATURE FOR SOLDERING PURPOSES:**  
260°C for 10 Seconds

**LEADS:** Modified L-Bend providing more contact area to bond pads

**POLARITY:** Cathode indicated by molded polarity notch

**MOUNTING POSITION:** Any

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Power Dissipation (Note 1.) @ $T_L = 25^\circ\text{C}$ , Pulse Width = 1 ms	$P_{PK}$	1500	W
DC Power Dissipation @ $T_L = 75^\circ\text{C}$ Measured Zero Lead Length (Note 2.) Derate Above $75^\circ\text{C}$	$P_D$	4.0	W
Thermal Resistance from Junction to Lead	$R_{\theta JL}$	54.6 18.3	$\text{mW}/^\circ\text{C}$ $^\circ\text{C}/\text{W}$
DC Power Dissipation (Note 3.) @ $T_A = 25^\circ\text{C}$ Derate Above $25^\circ\text{C}$	$P_D$	0.75	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	6.1 165	$\text{mW}/^\circ\text{C}$ $^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	$T_J, T_{stg}$	-65 to +150	$^\circ\text{C}$

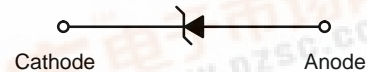
1. 10 X 1000  $\mu$ s, non-repetitive
2. 1" square copper pad, FR-4 board
3. FR-4 board, using ON Semiconductor minimum recommended footprint, as shown in 403B case outline dimensions spec.



ON Semiconductor™

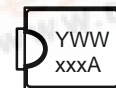
<http://onsemi.com>

### PLASTIC SURFACE MOUNT ZENER OVERVOLTAGE TRANSIENT SUPPRESSORS 5.8–78 VOLTS 1500 WATT PEAK POWER



SMC  
CASE 403  
PLASTIC

#### MARKING DIAGRAM



Y = Year  
WW = Work Week  
xxxA = Specific Device Code  
(See Table Next Page)

#### ORDERING INFORMATION

Device †	Package	Shipping
1.5SMCxxxAT3	SMC	2500/Tape & Reel

Devices listed in **bold, italic** are ON Semiconductor **Preferred** devices. **Preferred** devices are recommended choices for future use and best overall value.

\*Bidirectional devices will not be available in this series.

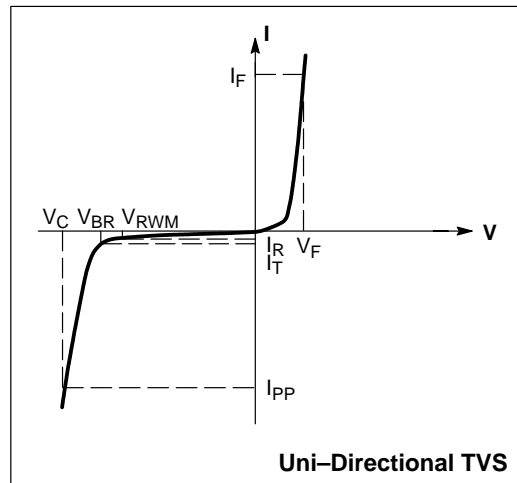
†The "T3" suffix refers to a 13 inch reel.



# 1.5SMC6.8AT3 Series

**ELECTRICAL CHARACTERISTICS** 1.5SMC6.8AT3 SERIES 供应商  
 at  $T_A = 25^\circ\text{C}$  unless otherwise noted,  $V_F = 3.5\text{ V Max. @ } I_F \text{ (Note 4.)} = 100\text{ A}$

Symbol	Parameter
$I_{PP}$	Maximum Reverse Peak Pulse Current
$V_C$	Clamping Voltage @ $I_{PP}$
$V_{RWM}$	Working Peak Reverse Voltage
$I_R$	Maximum Reverse Leakage Current @ $V_{RWM}$
$V_{BR}$	Breakdown Voltage @ $I_T$
$I_T$	Test Current
$\Theta V_{BR}$	Maximum Temperature Coefficient of $V_{BR}$
$I_F$	Forward Current
$V_F$	Forward Voltage @ $I_F$



**ELECTRICAL CHARACTERISTICS** (Devices listed in bold, italic are ON Semiconductor Preferred devices.)

Device	Device Marking	V <sub>RWM</sub>	I <sub>R</sub> @ V <sub>RWM</sub>	Breakdown Voltage				V <sub>C</sub> @ I <sub>PP</sub> (Note 6.)		ΘV <sub>BR</sub>
				V <sub>BR</sub> (Note 5.) Volts			@ I <sub>T</sub>	V <sub>C</sub>	I <sub>PP</sub>	
		Volts	μA	Min	Nom	Max	mA	Volts	Amps	
1.5SMC6.8AT3	6V8A	5.8	1000	6.45	6.8	7.14	10	10.5	143	0.057
1.5SMC7.5AT3	7V5A	6.4	500	7.13	7.5	7.88	10	11.3	132	0.061
1.5SMC8.2AT3	8V2A	7.02	200	7.79	8.2	8.61	10	12.1	124	0.065
1.5SMC9.1AT3	9V1A	7.78	50	8.65	9.1	9.55	1	13.4	112	0.068
1.5SMC10AT3	10A	8.55	10	9.5	10	10.5	1	14.5	103	0.073
1.5SMC11AT3	11A	9.4	5	10.5	11	11.6	1	15.6	96	0.075
1.5SMC12AT3	12A	10.2	5	11.4	12	12.6	1	16.7	90	0.078
1.5SMC13AT3	13A	11.1	5	12.4	13	13.7	1	18.2	82	0.081
<b>1.5SMC15AT3</b>	<b>15A</b>	<b>12.8</b>	<b>5</b>	<b>14.3</b>	<b>15</b>	<b>15.8</b>	<b>1</b>	<b>21.2</b>	<b>71</b>	<b>0.084</b>
1.5SMC16AT3	16A	13.6	5	15.2	16	16.8	1	22.5	67	0.086
1.5SMC18AT3	18A	15.3	5	17.1	18	18.9	1	25.2	59.5	0.088
1.5SMC20AT3	20A	17.1	5	19	20	21	1	27.7	54	0.09
1.5SMC22AT3	22A	18.8	5	20.9	22	23.1	1	30.6	49	0.092
<b>1.5SMC24AT3</b>	<b>24A</b>	<b>20.5</b>	<b>5</b>	<b>22.8</b>	<b>24</b>	<b>25.2</b>	<b>1</b>	<b>33.2</b>	<b>45</b>	<b>0.094</b>
1.5SMC27AT3	27A	23.1	5	25.7	27	28.4	1	37.5	40	0.096
1.5SMC30AT3	30A	25.6	5	28.5	30	31.5	1	41.4	36	0.097
<b>1.5SMC33AT3</b>	<b>33A</b>	<b>28.2</b>	<b>5</b>	<b>31.4</b>	<b>33</b>	<b>34.7</b>	<b>1</b>	<b>45.7</b>	<b>33</b>	<b>0.098</b>
<b>1.5SMC36AT3</b>	<b>36A</b>	<b>30.8</b>	<b>5</b>	<b>34.2</b>	<b>36</b>	<b>37.8</b>	<b>1</b>	<b>49.9</b>	<b>30</b>	<b>0.099</b>
<b>1.5SMC39AT3</b>	<b>39A</b>	<b>33.3</b>	<b>5</b>	<b>37.1</b>	<b>39</b>	<b>41</b>	<b>1</b>	<b>53.9</b>	<b>28</b>	<b>0.1</b>
<b>1.5SMC43AT3</b>	<b>43A</b>	<b>36.8</b>	<b>5</b>	<b>40.9</b>	<b>43</b>	<b>45.2</b>	<b>1</b>	<b>59.3</b>	<b>25.3</b>	<b>0.101</b>
<b>1.5SMC47AT3</b>	<b>47A</b>	<b>40.2</b>	<b>5</b>	<b>44.7</b>	<b>47</b>	<b>49.4</b>	<b>1</b>	<b>64.8</b>	<b>23.2</b>	<b>0.101</b>
1.5SMC51AT3	51A	43.6	5	48.5	51	53.6	1	70.1	21.4	0.102
1.5SMC56AT3	56A	47.8	5	53.2	56	58.8	1	77	19.5	0.103
1.5SMC62AT3	62A	53	5	58.9	62	65.1	1	85	17.7	0.104
1.5SMC68AT3	68A	58.1	5	64.6	68	71.4	1	92	16.3	0.104
<b>1.5SMC75AT3</b>	<b>75A</b>	<b>64.1</b>	<b>5</b>	<b>71.3</b>	<b>75</b>	<b>78.8</b>	<b>1</b>	<b>103</b>	<b>14.6</b>	<b>0.105</b>
1.5SMC82AT3	82A	70.1	5	77.9	82	86.1	1	113	13.3	0.105
1.5SMC91AT3	91A	77.8	5	86.5	91	95.5	1	125	12	0.106

- 1/2 sine wave (or equivalent square wave),  $PW = 8.3\text{ ms}$ , duty cycle = 4 pulses per minute maximum.
- $V_{BR}$  measured at pulse test current  $I_T$  at an ambient temperature of  $25^\circ\text{C}$ .
- Surge current waveform per Figure 2 and derate per Figure 3 of the General Data – 1500 Watt at the beginning of this group.

## 1.5SMC6.8AT3 Series

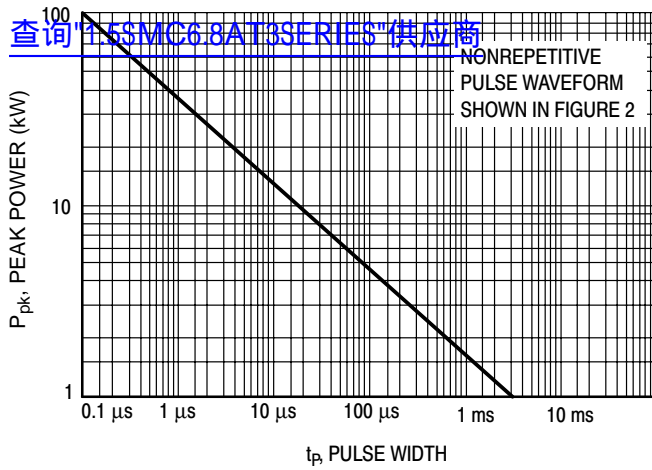


Figure 1. Pulse Rating Curve

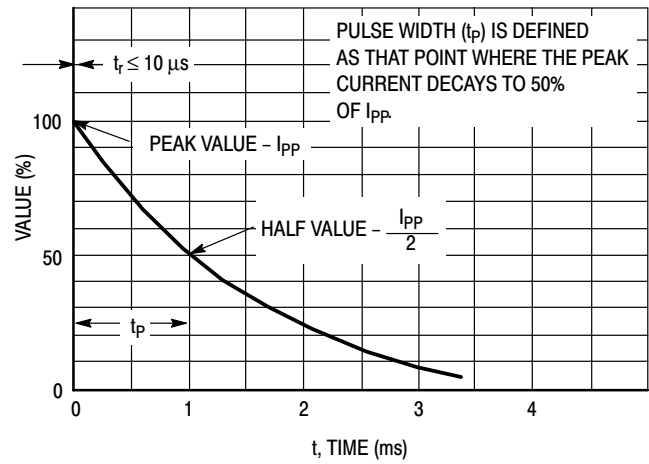


Figure 2. Pulse Waveform

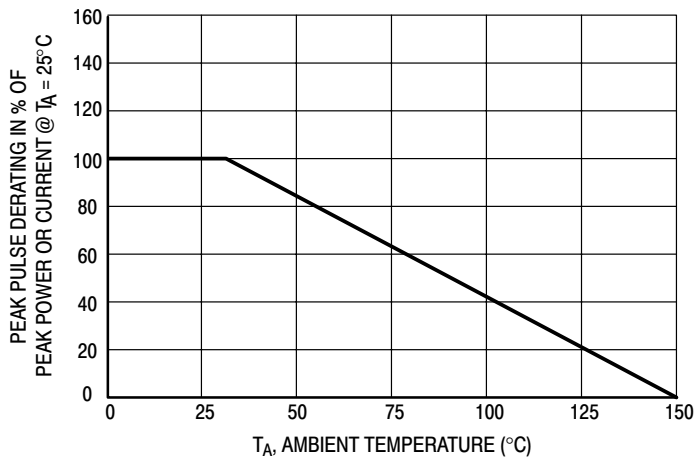


Figure 3. Pulse Derating Curve

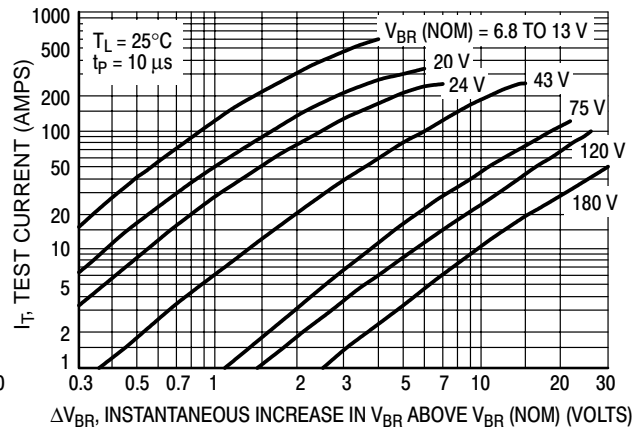


Figure 4. Dynamic Impedance

## UL RECOGNITION

The entire series has *Underwriters Laboratory Recognition* for the classification of protectors (QVGV2) under the UL standard for safety 497B and File #116110. Many competitors only have one or two devices recognized or have recognition in a non-protective category. Some competitors have no recognition at all. With the UL497B recognition, our parts successfully passed several tests

including Strike Voltage Breakdown test, Endurance Conditioning, Temperature test, Dielectric Voltage-Withstand test, Discharge test and several more.

Whereas, some competitors have only passed a flammability test for the package material, we have been recognized for much more to be included in their Protector category.

[查询"1.5SMC6.8AT3SERIES"供应商](#)

### APPLICATION NOTES

#### RESPONSE TIME

In most applications, the transient suppressor device is placed in parallel with the equipment or component to be protected. In this situation, there is a time delay associated with the capacitance of the device and an overshoot condition associated with the inductance of the device and the inductance of the connection method. The capacitive effect is of minor importance in the parallel protection scheme because it only produces a time delay in the transition from the operating voltage to the clamp voltage as shown in Figure 5.

The inductive effects in the device are due to actual turn-on time (time required for the device to go from zero current to full current) and lead inductance. This inductive effect produces an overshoot in the voltage across the equipment or component being protected as shown in Figure 6. Minimizing this overshoot is very important in the application, since the main purpose for adding a transient suppressor is to clamp voltage spikes. The SMC series have a very good response time, typically < 1 ns and negligible inductance. However, external inductive effects could produce unacceptable overshoot. Proper circuit layout,

minimum lead lengths and placing the suppressor device as close as possible to the equipment or components to be protected will minimize this overshoot.

Some input impedance represented by  $Z_{in}$  is essential to prevent overstress of the protection device. This impedance should be as high as possible, without restricting the circuit operation.

#### DUTY CYCLE DERATING

The data of Figure 1 applies for non-repetitive conditions and at a lead temperature of 25°C. If the duty cycle increases, the peak power must be reduced as indicated by the curves of Figure 7. Average power must be derated as the lead or ambient temperature rises above 25°C. The average power derating curve normally given on data sheets may be normalized and used for this purpose.

At first glance the derating curves of Figure 7 appear to be in error as the 10 ms pulse has a higher derating factor than the 10  $\mu$ s pulse. However, when the derating factor for a given pulse of Figure 7 is multiplied by the peak power value of Figure 1 for the same pulse, the results follow the expected trend.

# 1.5SMC6.8AT3 Series

[查询"1.5SMC6.8AT3SERIES"供应商](#)

## TYPICAL PROTECTION CIRCUIT

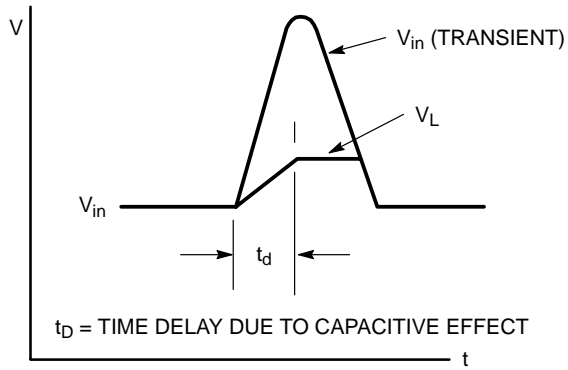
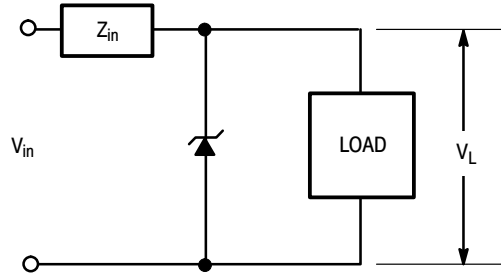


Figure 5.

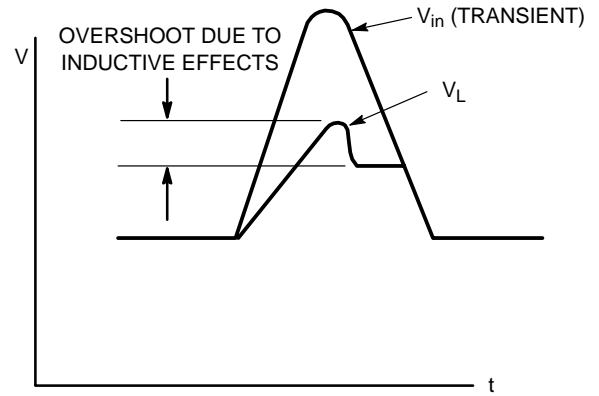


Figure 6.

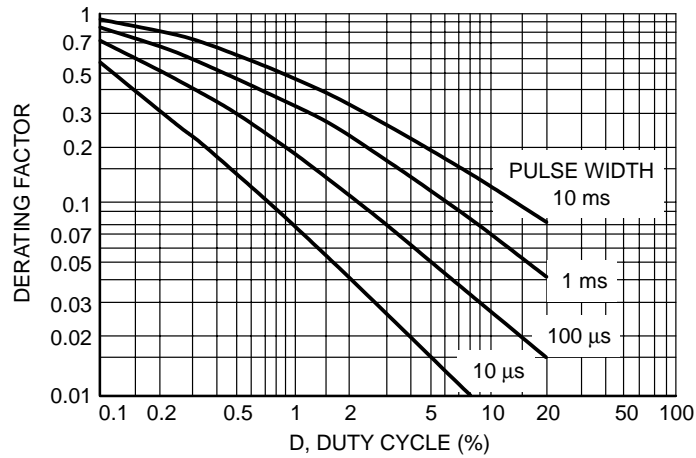
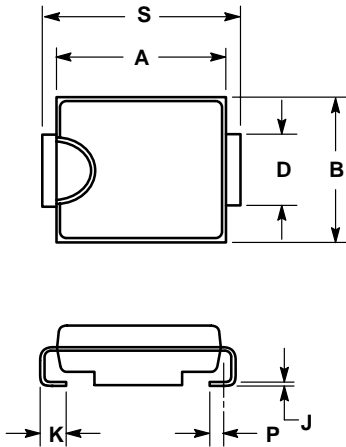


Figure 7. Typical Derating Factor for Duty Cycle

# Transient Voltage Suppressors – Surface Mounted

## 1500 Watt Peak Power

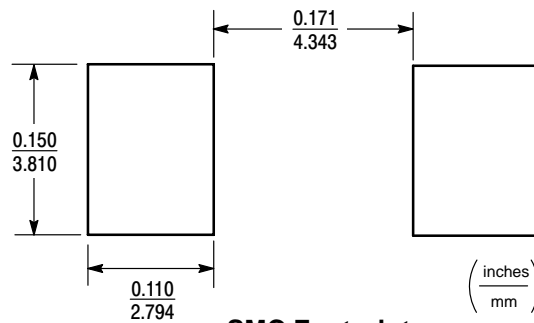
SMC  
CASE 403-03  
ISSUE B



## NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. D DIMENSION SHALL BE MEASURED WITHIN DIMENSION P.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.260	0.280	6.60	7.11
B	0.220	0.240	5.59	6.10
C	0.075	0.095	1.90	2.41
D	0.115	0.121	2.92	3.07
H	0.0020	0.0060	0.051	0.152
J	0.006	0.012	0.15	0.30
K	0.030	0.050	0.76	1.27
P	0.020	REF	0.51	REF
S	0.305	0.320	7.75	8.13




**Notes**  
[1.5SMC6.8AT3SERIES"供应商](#)

## 1.5SMC6.8AT3 Series

[查询"1.5SMC6.8AT3SERIES"供应商](#)

Surmetic is a trademark of Semiconductor Components Industries, LLC.

**ON Semiconductor** and  are trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

### PUBLICATION ORDERING INFORMATION

#### **NORTH AMERICA Literature Fulfillment:**

Literature Distribution Center for ON Semiconductor  
P.O. Box 5163, Denver, Colorado 80217 USA  
**Phone:** 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
**Fax:** 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
**Email:** ONlit@hibbertco.com  
Fax Response Line: 303-675-2167 or 800-344-3810 Toll Free USA/Canada

**N. American Technical Support:** 800-282-9855 Toll Free USA/Canada

**EUROPE:** LDC for ON Semiconductor – European Support

**German Phone:** (+1) 303-308-7140 (Mon-Fri 2:30pm to 7:00pm CET)  
**Email:** ONlit-german@hibbertco.com  
**French Phone:** (+1) 303-308-7141 (Mon-Fri 2:00pm to 7:00pm CET)  
**Email:** ONlit-french@hibbertco.com  
**English Phone:** (+1) 303-308-7142 (Mon-Fri 12:00pm to 5:00pm GMT)  
**Email:** ONlit@hibbertco.com

**EUROPEAN TOLL-FREE ACCESS\*: 00-800-4422-3781**

\*Available from Germany, France, Italy, UK, Ireland

#### **CENTRAL/SOUTH AMERICA:**

**Spanish Phone:** 303-308-7143 (Mon-Fri 8:00am to 5:00pm MST)  
**Email:** ONlit-spanish@hibbertco.com  
**Toll-Free from Mexico:** Dial 01-800-288-2872 for Access –  
then Dial 866-297-9322

**ASIA/PACIFIC:** LDC for ON Semiconductor – Asia Support

**Phone:** 303-675-2121 (Tue-Fri 9:00am to 1:00pm, Hong Kong Time)  
**Toll Free from Hong Kong & Singapore:**  
**001-800-4422-3781**  
**Email:** ONlit-asia@hibbertco.com

**JAPAN:** ON Semiconductor, Japan Customer Focus Center

4-32-1 Nishi-Gotanda, Shinagawa-ku, Tokyo, Japan 141-0031  
**Phone:** 81-3-5740-2700  
**Email:** r14525@onsemi.com

**ON Semiconductor Website:** <http://onsemi.com>

For additional information, please contact your local Sales Representative.