

[查询"20326"供应商](#)

SPECIFICATIONS
DIGITAL TO ANALOG CONVERTER

MODEL 20326

Resolution:	14 binary bits
Output:	
Voltage	0 to +1V
Impedance	Padded to operate with 4.02K
Load	2.5 to ground
Tempco	+15 ppm/°C max
Linearity & Accuracy	20°C to 50°C
Model -1	+1 part in 2^{15}
Model -2	+1 part in 2^{16}
Model -3	+1 part in 2^{17}
Settling Time 10%	30 usec to less than $\pm 1/2$ LSB
	40 usec to ± 1 part in 2^{16}
Full Scale	50 usec
Noise	< uvolts RMS from 5 Hz to 1 megahertz
Power Requirements:	+15 volts @ 50 ma
	-15 volts @ 50 ma
Reference:	+1 volts $\pm 10\%$ @ 0.1 ma
Data Input:	Binary coded
	Logic "0" 2.5 - 5.5V @ .4 ma max.
	Logic "1" 0 - .4V @ 1.6 ma

LANCER ELECTRONICS CORPORATION

20326-X

