



# N-Channel NexFET™ Power MOSFET

Check for Samples: CSD16414Q5

#### **FEATURES**

- **Ultra Low Qg and Qgd**
- **Low Thermal Resistance**
- **Avalanche Rated**
- **Pb Free Terminal Plating**
- **RoHS Compliant**
- **Halogen Free**
- SON 5mm × 6mm Plastic Package

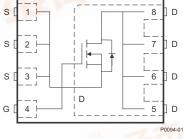
### **APPLICATIONS**

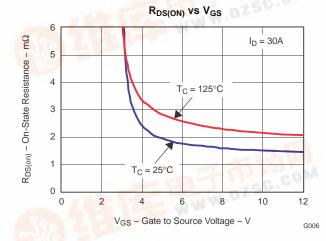
- Point-of-Load Synchronous Buck Converter for Applications in Networking, Telecom and **Computing Systems**
- **Optimized for Synchronous FET Applications**

#### DESCRIPTION

The NexFET™ power MOSFET has been designed to minimize losses in power conversion applications.







#### PRODUCT SUMMARY

V <sub>DS</sub>	Drain to Source Voltage	25	-0.54	V
$Q_g$	Gate Charge Total (4.5V) 16.6			
$Q_{gd}$	Gate Charge Gate to Drain 4.4			nC
	Designate Course On Business	$V_{GS} = 4.5V$	2.1	mΩ
R <sub>DS(on)</sub>	Drain to Source On Resistance	V <sub>GS</sub> = 10V	1.5	mΩ
V <sub>GS(th)</sub>	Threshold Voltage	1.6		V

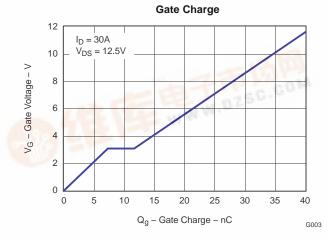
#### ORDERING INFORMATION

Device	Device Package Media		Qty	Ship
CSD16414Q5	SON 5 × 6 Plastic Package	13-inch reel	2500	Tape and Reel

#### ABSOLUTE MAXIMUM RATINGS

T <sub>A</sub> = 2	5°C unless otherwise stated	VALUE	UNIT
V <sub>DS</sub>	Drain to Source Voltage	25	٧
V <sub>GS</sub>	Gate to Source Voltage	+16 / -12	V
	Continuous Drain Current, T <sub>C</sub> = 25°C	100	Α
ID	Continuous Drain Current <sup>(1)</sup>	34	Α
$I_{DM}$	Pulsed Drain Current, T <sub>A</sub> = 25°C <sup>(2)</sup>	213	Α
$P_D$	Power Dissipation <sup>(1)</sup>	3.2	W
$T_J$ , $T_{STG}$	Operating Junction and Storage Temperature Range	-55 to 150	°C
E <sub>AS</sub>	Avalanche Energy, single pulse $I_D = 100A$ , $L = 0.1mH$ , $R_G = 25\Omega$	500	mJ

- (1)  $R_{\theta JA} = 39^{\circ} \text{C/W on 1in}^2 \text{ Cu (2 oz.) on 0.060" thick FR4 PCB.}$
- (2) Pulse width ≤300μs, duty cycle ≤2%"



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet. NexFET is a trademark of Texas Instruments.

df.dzsc.com



## **ELECTRICAL CHARACTERISTICS**

 $(T_{\Delta} = 25^{\circ}C \text{ unless otherwise stated})$ 

· A	°C unless otherwise stated)  PARAMETER	TEST CONDITIONS	MIN 7	ГҮР	MAX	UNIT
Static CI	haracteristics	TEST COMPITIONS	141114	• • •	MAX	
BV <sub>DSS</sub>	Drain to Source Voltage	$V_{GS} = 0V, I_D = 250\mu A$	25			V
I <sub>DSS</sub>	Drain to Source Leakage Current	$V_{GS} = 0V, V_{DS} = 20V$	25		1	μA
	Gate to Source Leakage Current	$V_{DS} = 0V$ , $V_{DS} = 20V$ $V_{DS} = 0V$ , $V_{GS} = +16/-12V$			100	nΑ
Vasuu	Gate to Source Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.3	1.6	2	V
V <sub>GS(th)</sub>	Cate to course Threshold Voltage	$V_{GS} = V_{GS}, I_D = 200\mu$ $V_{GS} = 4.5V, I_D = 30A$	-	2.1	2.6	mΩ
$R_{DS(on)}$	Drain to Source On Resistance	$V_{GS} = 10V, I_D = 30A$		1.5	1.9	mΩ
α.	Transconductance	$V_{DS} = 10V, I_D = 30A$ $V_{DS} = 15V, I_D = 30A$		138	1.9	S
g <sub>fs</sub>	C Characteristics	V <sub>DS</sub> = 13V, I <sub>D</sub> = 30A		130		
			2	810	3650	pF
C <sub>ISS</sub>	Input Capacitance	V 0V V 10 EV 5 1MHz		040		T .
C <sub>OSS</sub>	Output Capacitance	$V_{GS} = 0V, V_{DS} = 12.5V, f = 1MHz$			2650	pF
C <sub>RSS</sub>	Reverse Transfer Capacitance			140	180	pF
R <sub>g</sub>	Series Gate Resistance			1.4	2.8	Ω
Qg	Gate Charge Total (4.5V)			16.6	21	nC
$Q_{gd}$	Gate Charge Gate to Drain	V <sub>DS</sub> = 12.5V, ID = 30A		4.4		nC
$Q_{gs}$	Gate Charge Gate to Source	VDS = 12.5V, 1D = 50A		7.3		nC
Qg(th)	Gate Charge at Vth			4.5		nC
$Q_{OSS}$	Output Charge	$V_{DS} = 13.5V, VGS = 0V$		40		nC
t <sub>d(on)</sub>	Turn On Delay Time			15		ns
t <sub>r</sub>	Rise Time	$V_{DS} = 12.5V, V_{GS} = 4.5V, I_{D} = 30A$		24		ns
t <sub>d(off)</sub>	Turn Off Delay Time	$R_G = 2\Omega$	1	18.4		ns
t <sub>f</sub>	Fall Time		1	11.1		ns
Diode C	haracteristics				*	
V <sub>SD</sub>	Diode Forward Voltage	$I_S = 30A, V_{GS} = 0V$	C	).81	1	٧
Q <sub>rr</sub>	Reverse Recovery Charge	$V_{dd} = 13.5V$ , $I_F = 30A$ , $di/dt = 300A/\mu s$		44		nC
t <sub>rr</sub>	Reverse Recovery Time	$V_{dd} = 13.5V$ , $I_F = 30A$ , $di/dt = 300A/\mu s$		35		ns

#### THERMAL CHARACTERISTICS

 $(T_A = 25^{\circ}C \text{ unless otherwise stated})$ 

	PARAMETER	MIN	TYP	MAX	UNIT			
R <sub>θJC</sub>	Thermal Resistance Junction to Case <sup>(1)</sup>			1.1	°C/W			
R <sub>0JA</sub>	Thermal Resistance Junction to Ambient <sup>(1)</sup> (2)			50	°C/W			

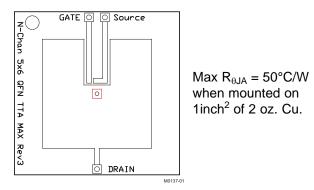
<sup>(1)</sup> R  $_{\theta JC}$  is determined with the device mounted on a 1 inch square 2 oz. Cu pad on a 1.5 x 1.5 in 0.060 inch thick FR4 board. R  $_{\theta JC}$  is specified by design while R  $_{\theta JA}$  is determined by the user's board design.

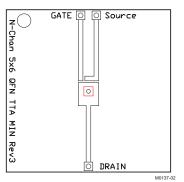
Submit Documentation Feedback

<sup>(2)</sup> Device mounted on FR4 Material with 1 inch<sup>2</sup> of 2 oz. Cu.



# <u>₩豐簡♥CSD16414Q5"供应商</u>





Max  $R_{\theta JA} = 122^{\circ}C/W$  when mounted on minimum pad area of 2 oz. Cu.

## TYPICAL MOSFET CHARACTERISTICS

(T<sub>A</sub> = 25°C unless otherwise stated)

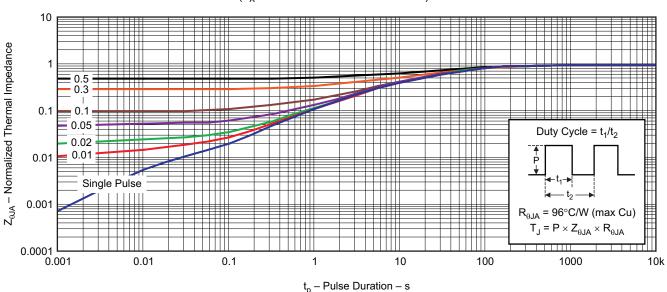


Figure 1. Transient Thermal Impedance



# TYPICAL MOSFET CHARACTERISTICS (continued)

 $(T_A = 25^{\circ}C \text{ unless otherwise stated})$ 

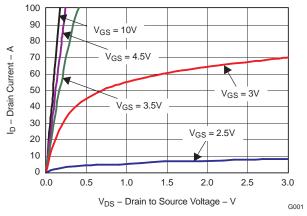


Figure 2. Saturation Characteristics

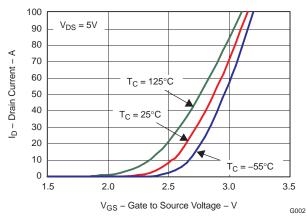


Figure 3. Transfer Characteristics

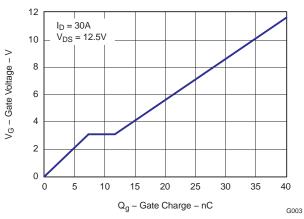


Figure 4. Gate Charge

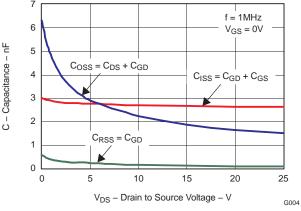


Figure 5. Capacitance

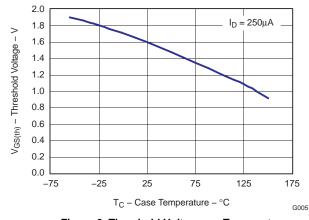


Figure 6. Threshold Voltage vs. Temperature

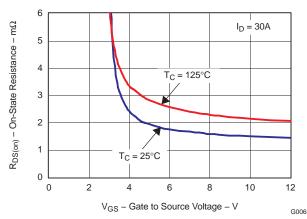


Figure 7. On Resistance vs. Gate Voltage

**ISTRUMENTS** 

# TYPICAL MOSFET CHARACTERISTICS (continued)

 $(T_A = 25^{\circ}C \text{ unless otherwise stated})$ 

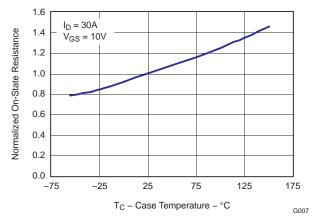


Figure 8. On Resistance vs. Temperature

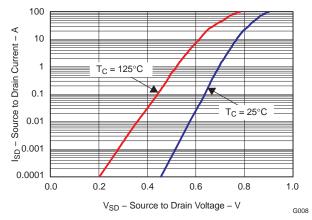


Figure 9. Typical Diode Forward Voltage

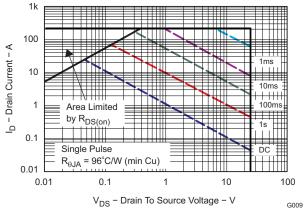


Figure 10. Maximum Safe Operating Area

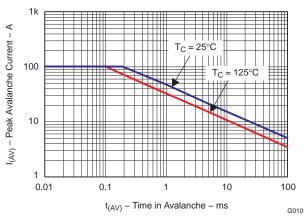


Figure 11. Single Pulse Unclamped Inductive Switching

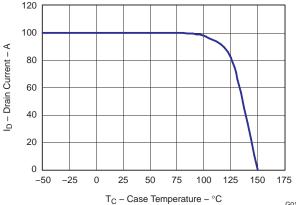
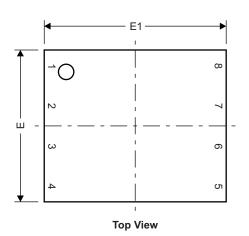


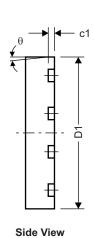
Figure 12. Maximum Drain Current vs. Temperature

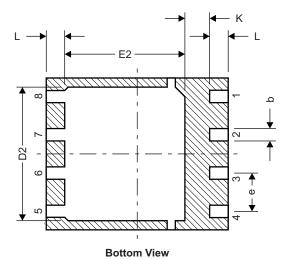


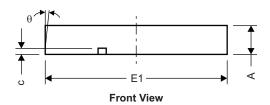
## **MECHANICAL DATA**

# **Q5 Package Dimensions**









M0140-01

DIM	MILLIN	IETERS	INCHES		
DIW	MIN	MAX	MIN	MAX	
А	0.950	1.050	0.037	0.039	
b	0.360	0.460	0.014	0.018	
С	0.150	0.250	0.006	0.010	
c1	0.150	0.250	0.006	0.010	
D1	4.900	5.100	0.193	0.201	
D2	4.320	4.520	0.170	0.178	
E	4.900	5.100	0.193	0.201	
E1	5.900	6.100	0.232	0.240	
E2	3.920	4.12	0.154	0.162	
е	1.27	TYP	0.0	050	
K	0.760		0.030		
L	0.510	0.710	0.020	0.028	
θ	0.00				



# **≌簡<sup>®©</sup>SD16414Q5**"供应商

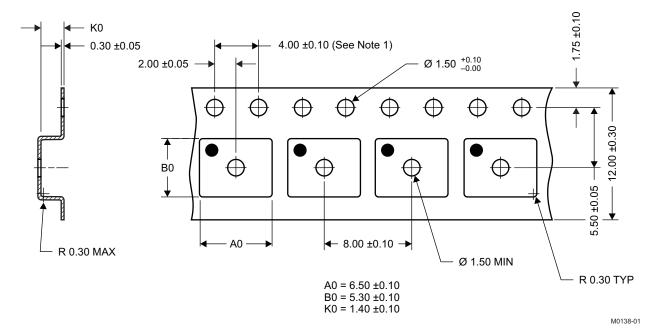
#### SLPS208A -AUGUST 2009-REVISED SEPTEMBER 2010

Recommended PCB Pattern							
F6 - F1-							
F10 —	M0139-01						

DIM	MILLIM	IETERS	INC	HES
DIN	MIN	MAX	MIN	MAX
F1	6.205	6.305	0.244	0.248
F2	4.460	4.560	0.176	0.180
F3	4.460	4.560	0.176	0.180
F4	0.650	0.700	0.026	0.028
F5	0.620	0.670	0.024	0.026
F6	0.630	0.680	0.025	0.027
F7	0.700	0.800	0.028	0.031
F8	0.650	0.700	0.026	0.028
F9	0.620	0.670	0.024	0.026
F10	4.900	5.000	0.193	0.197
F11	4.460	4.560	0.176	0.180

For recommended circuit layout for PCB designs, see application note SLPA005 - Reducing Ringing Through PCB Layout Techniques.

## **Q5 Tape and Reel Information**



#### Notes:

- 1. 10 sprocket hole pitch cumulative tolerance ±0.2
- 2. Camber not to exceed 1mm IN 100mm, noncumulative over 250mm
- 3. Material:black static dissipative polystyrene
- 4. All dimensions are in mm (unless otherwise specified)
- 5. Thickness: 0.30 ±0.05mm
- 6. MSL1 260°C (IR and Convection) PbF Reflow Compatible





www.ti.com

# REVISION HISTORY

CI	hanges from Original (August 2009) to Revision A	Page
•	Deleted the Package Marking Information section	7



### PACKA

#### PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/ Ball Finish	MSL Pea
CSD16414Q5	ACTIVE	SON	DQH	8	2500	Pb-Free (RoHS Exempt)	CU SN	Level-1-2600

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free** (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for **Pb-Free** (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retard in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate in continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical at TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Cu

## 查询"CSD16414Q5"供应商

#### IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

Products		Applications	
Amplifiers	amplifier.ti.com	Audio	www.ti.com/audio
Data Converters	dataconverter.ti.com	Automotive	www.ti.com/automotive
DLP® Products	www.dlp.com	Communications and Telecom	www.ti.com/communications
DSP	<u>dsp.ti.com</u>	Computers and Peripherals	www.ti.com/computers
Clocks and Timers	www.ti.com/clocks	Consumer Electronics	www.ti.com/consumer-apps
Interface	interface.ti.com	Energy	www.ti.com/energy
Logic	logic.ti.com	Industrial	www.ti.com/industrial
Power Mgmt	power.ti.com	Medical	www.ti.com/medical
Microcontrollers	microcontroller.ti.com	Security	www.ti.com/security
RFID	www.ti-rfid.com	Space, Avionics & Defense	www.ti.com/space-avionics-defense
RF/IF and ZigBee® Solutions	www.ti.com/lprf	Video and Imaging	www.ti.com/video
		Wireless	www.ti.com/wireless-apps