

# MOS FIELD EFFECT TRANSISTOR 2SK3902

## **SWITCHING N-CHANNEL POWER MOS FET**

### **DESCRIPTION**

The 2SK3902 is N-channel MOS Field Effect Transistor designed for high current switching applications.

### ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3902-ZK	TO-263 (MP-25ZK)

### **FEATURES**

• Super low On-state resistance

 $R_{DS(on)1} = 21 \text{ m}\Omega \text{ MAX.} (V_{GS} = 10 \text{ V}, I_D = 15 \text{ A})$ 

 $R_{DS(on)2} = 26 \text{ m}\Omega \text{ MAX.} \text{ (V}_{GS} = 4.5 \text{ V}, I_{D} = 15 \text{ A})$ 

- Low Ciss: Ciss = 1200 pF TYP.
- Built-in gate protection diode

(TO-263)



### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (V <sub>GS</sub> = 0 V)	VDSS	60	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±30	Α
Drain Current (pulse) Note1	D(pulse)	±90	Α
Total Power Dissipation (Tc = 25°C)	P <sub>T1</sub>	45	W
Total Power Dissipation (T <sub>A</sub> = 25°C)	P <sub>T2</sub>	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C
Single Avalanche Energy Note2	Eas	40	mJ
Repetitive Avalanche Current Note3	lar	20	Α
Repetitive Avalanche Energy Note3	Ear	40	mJ

**Notes 1.** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

- 2. Starting T<sub>ch</sub> = 25°C, V<sub>DD</sub> = 30 V, R<sub>G</sub> = 25  $\Omega$ , V<sub>GS</sub> = 20  $\rightarrow$  0 V, L = 100  $\mu$ H
- 3. Rg = 25  $\Omega$ , Tch(peak)  $\leq 150^{\circ}$ C

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### **ELECTRICAL CHARACTERISTICS (TA = 25°C)**

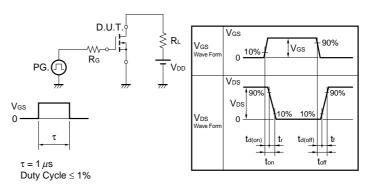
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V			10	μΑ
Gate Leakage Current	Igss	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V			±10	μΑ
Gate Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	2.0	2.5	V
Forward Transfer Admittance Note	<b>y</b> fs	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 15 A	9.5	19		S
Drain to Source On-state Resistance Note	RDS(on)1	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A		16.8	21	mΩ
	RDS(on)2	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 15 A		19.5	26	mΩ
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V		1200		pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V		250		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		85		pF
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 30 V, I <sub>D</sub> = 15 A		10		ns
Rise Time	tr	V <sub>GS</sub> = 10 V		4		ns
Turn-off Delay Time	t <sub>d(off)</sub>	R <sub>G</sub> = 0 Ω		37		ns
Fall Time	tr			4		ns
Total Gate Charge	Q <sub>G</sub>	V <sub>DD</sub> = 48 V		25		nC
Gate to Source Charge	Qgs	V <sub>GS</sub> = 10 V		4.5		nC
Gate to Drain Charge	Q <sub>GD</sub>	I <sub>D</sub> = 30 A		6.0		nC
Body Diode Forward Voltage Note	V <sub>F(S-D)</sub>	IF = 30 A, VGS = 0 V		0.92	1.5	٧
Reverse Recovery Time	trr	IF = 30 A, VGS = 0 V		31		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		34		nC

Note Pulsed

### **TEST CIRCUIT 1 AVALANCHE CAPABILITY**

# $V_{GS} = 20 \rightarrow 0 \text{ V}$ $V_{DD}$ $V_{DD}$

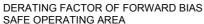
### TEST CIRCUIT 2 SWITCHING TIME

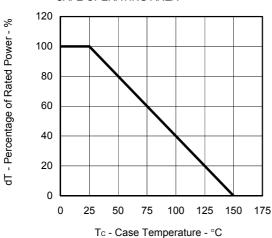


### **TEST CIRCUIT 3 GATE CHARGE**

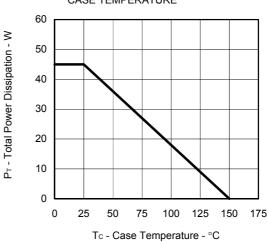
-Starting Tch

### TYPICAL CHARACTERISTICS (TA = 25°C)

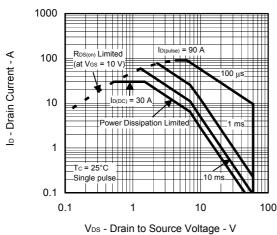




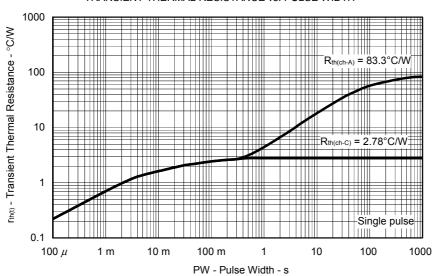
# TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



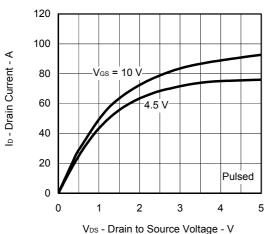
### FORWARD BIAS SAFE OPERATING AREA



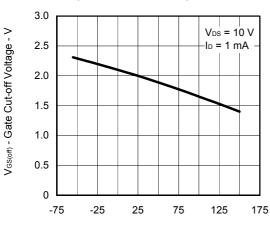






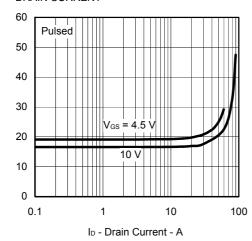


# GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE

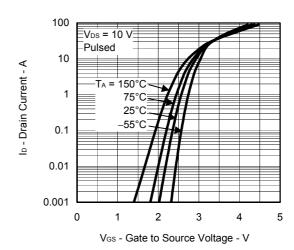


DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

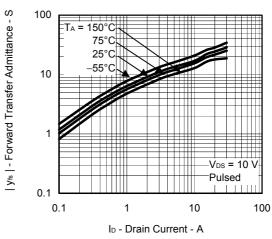
Tch - Channel Temperature - °C



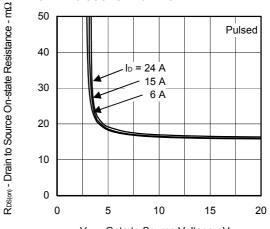
### FORWARD TRANSFER CHARACTERISTICS



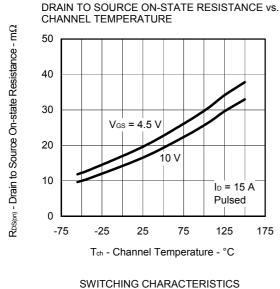
# FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

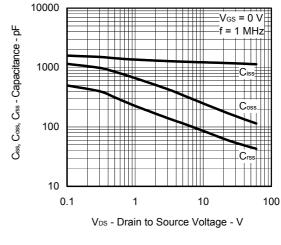


DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

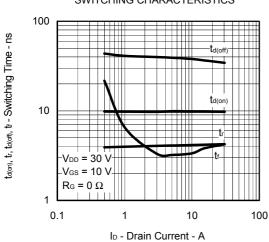


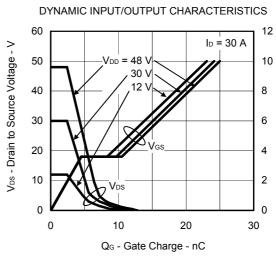
RDS(cn) - Drain to Source On-state Resistance - m\O

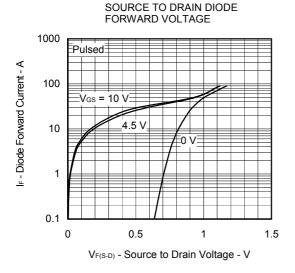


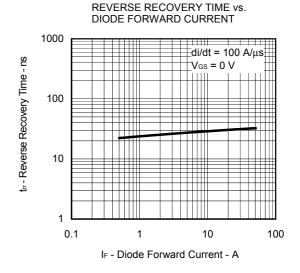


CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE

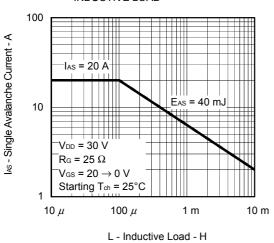




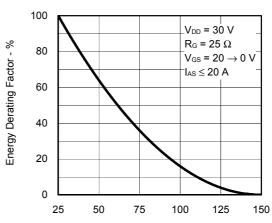




# SINGLE AVALANCHE CURRENT vs. INDUCTIVE LOAD



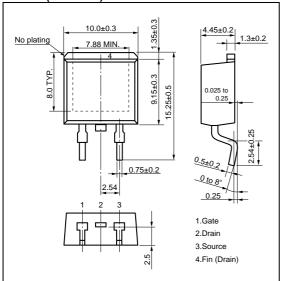
# SINGLE AVALANCHE ENERGY DERATING FACTOR



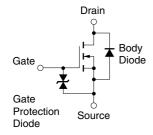
Starting T<sub>ch</sub> - Starting Channel Temperature -  $^{\circ}$ C

### PACKAGE DRAWING (Unit: mm)

### TO-263 (MP-25ZK)



### **EQUIVALENT CIRCUIT**



**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD.

When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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