Silicon N-Channel MOS FET

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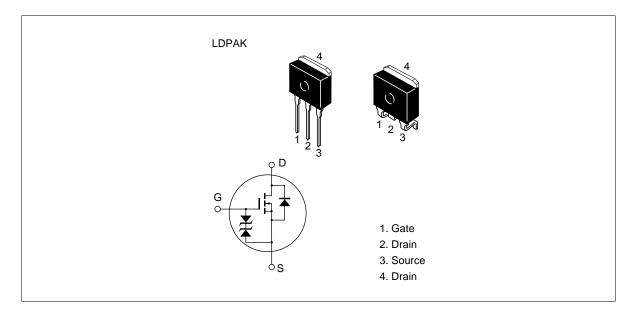
## Application

High speed power switching

### Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

### Outline





## **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	900	V
Gate to source voltage	V <sub>GSS</sub>	±30	V
Drain current	I <sub>D</sub>	4	А
Drain peak current	↓ D(pulse) * <sup>1</sup>	10	А
Body to drain diode reverse drain current	I <sub>DR</sub>	4	А
Channel dissipation	Pch*2	60	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at  $T_c = 25^{\circ}C$ 

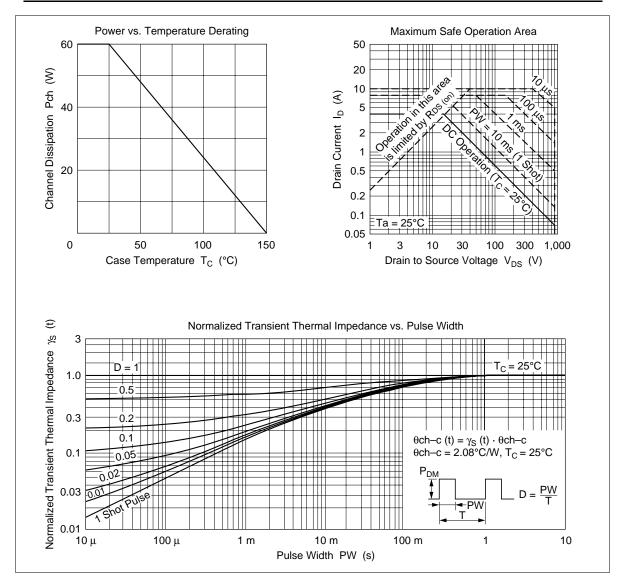
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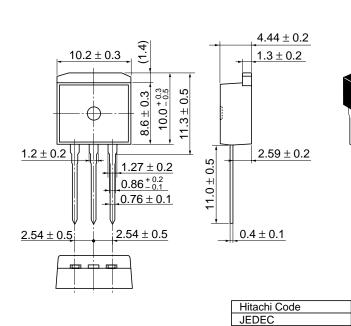
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	900	—	_	V	$I_{D} = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±30	—	—	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>			±10	μA	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>		_	250	μA	$V_{\rm DS} = 720$ V, $V_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	2.0	_	3.0	V	$I_{\rm D} = 1 \text{ mA}, V_{\rm DS} = 10 \text{ V}$
Static Drain to source on state resistance	$R_{\text{DS(on)}}$	—	3.0	4.0	Ω	$I_{D} = 2 \text{ A}, \text{ V}_{GS} = 10 \text{ V}^{*1}$
Forward transfer admittance	yfs	1.7	2.7		S	$I_{\rm D} = 2$ A, $V_{\rm DS} = 20$ V * <sup>1</sup>
Input capacitance	Ciss	_	740	_	pF	$V_{\rm DS} = 10 \ V, \ V_{\rm GS} = 0,$
Output capacitance	Coss	_	305	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss		150	_	pF	
Turn-on delay time	t <sub>d(on)</sub>		15	_	ns	$I_{\rm D} = 2 \text{ A}, \text{ V}_{\rm GS} = 10 \text{ V},$
Rise time	t,		60	_	ns	R <sub>L</sub> = 15 Ω
Turn-off delay time	$t_{d(off)}$	—	100	_	ns	
Fall time	t <sub>r</sub>	—	80	_	ns	
Body to drain diode forward voltage	$V_{\text{DF}}$	—	0.9	—	V	$I_{F} = 4 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	800	—	ns	$I_{F} = 4 \text{ A}, V_{GS} = 0,$ $di_{F}/dt = 100 \text{ A}/\mu\text{s}$
Noto: 1 Dulas test						

## **Electrical Characteristics** (Ta = 25°C)

Note: 1. Pulse test

See characteristic curves of 2SK1340.





EIAJ

Unit: mm

LDPAK (L)

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EIAJ—Weight (reference value)1.4 g

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