SLVS013L-MAY 1987-REVISED SEPTEMBER 2005

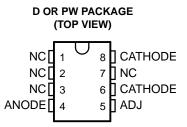


FEATURES

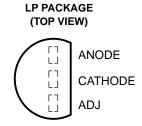
- **Excellent Temperature Stability**
- Initial Tolerance...0.2% Max
- Dynamic Impedance...0.6 Ω Max
- **Wide Operating Current Range**
- **Directly Interchangeable With LM136**
- **Needs No Adjustment for Minimum Temperature Coefficient**

DESCRIPTION/ORDERING INFORMATION

The LT1009 reference circuit is a precision-trimmed 2.5-V shunt regulator featuring low dynamic impedance and a wide operating current range. The maximum initial tolerance is ±5 mV in the LP package and ±10 mV in the D package. The reference tolerance is achieved by on-chip trimming, which minimizes the initial voltage tolerance and the temperature coefficient, $\alpha_{V/7}$.



NC - No internal connection



Although the LT1009 needs no adjustments, a third terminal (ADJ) allows the reference voltage to be adjusted ±5% to eliminate system errors. In many applications, the LT1009 can be used as a terminal-for-terminal replacement for the LM136-2.5, which eliminates the external trim network.

The LT1009 uses include 5-V system references, 8-bit analog-to-digital converter (ADC) and digital-to-analog converter (DAC) references, and power-supply monitors. The device also can be used in applications such as digital voltmeters and current-loop measurement and control systems.

The LT1009C is characterized for operation from 0°C to 70°C. The LT1009I is characterized for operation from -40°C to 85°C.

ORDERING INFORMATION

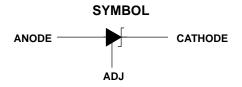
T _A	PAC	KAGE ⁽¹⁾	ORDERABLE PART NUMBER	TOP-SIDE MARKING
0°C to 70°C	SOIC - D	Tube of 75	LT1009CD	10000
	201C – D	Reel of 2500	LT1009CDR	1009C
		Bulk of 1000	LT1009CLP	
	TO-226/TO-92 – LP	Ammo of 2000	LT1009CLPM	LT1009C
		Reel of 2000	LT1009CLPR	
	TSSOP – PW	Tube of 150	LT1009CPW	10000
		Reel of 2000	LT1009CPWR	1009C
	SOIC - D	Tube of 75	LT1009ID	40001
		Reel of 2500	LT1009IDR	10091
		Bulk of 1000	LT1009ILP	
–40°C to 85°C	TO-226/TO-92 - LP	Ammo of 2000	LT1009ILPM	LT1009I
		Reel of 2000	LT1009ILPR	
	TCCOD DW	Tube of 150	LT1009IPW	10001
	TSSOP – PW	Reel of 2000	LT1009IPWR	10091

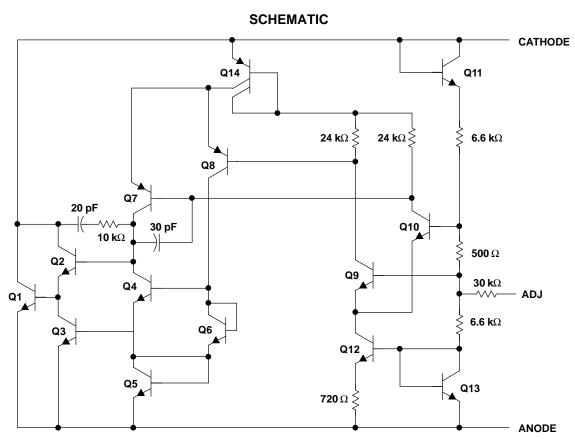
⁽¹⁾ Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.







NOTE: All component values shown are nominal.

Absolute Maximum Ratings(1)

over operating free-air temperature range (unless otherwise noted)

			MIN	MAX	UNIT
I_R	Reverse current			20	mA
IF	Forward current			10	mA
		D package		97	
θ_{JA}	Package thermal impedance (2)(3)	LP package		140	°C/W
		PW package		149	
T_{J}	Operating virtual junction temperature			150	°C
T _{stg}	Storage temperature range		-65	150	°C

Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

Maximum power dissipation is a function of $T_J(max)$, θ_{JA} , and T_A . The maximum allowable power dissipation at any allowable ambient temperature is $P_D = (T_J(max) - T_A)/\theta_{JA}$. Operating at the absolute maximum T_J of 150°C can affect reliability. The package thermal impedance is calculated in accordance with JESD 51-7.

SLVS013L-MAY 1987-REVISED SEPTEMBER 2005

Recommended Operating Conditions

			MIN	MAX	UNIT
_	Operating free air temperature range	LT1009C	0	70	۰.
¹A	Operating free-air temperature range	LT1009I	-40	85	C

Electrical Characteristics

at specified free-air temperature

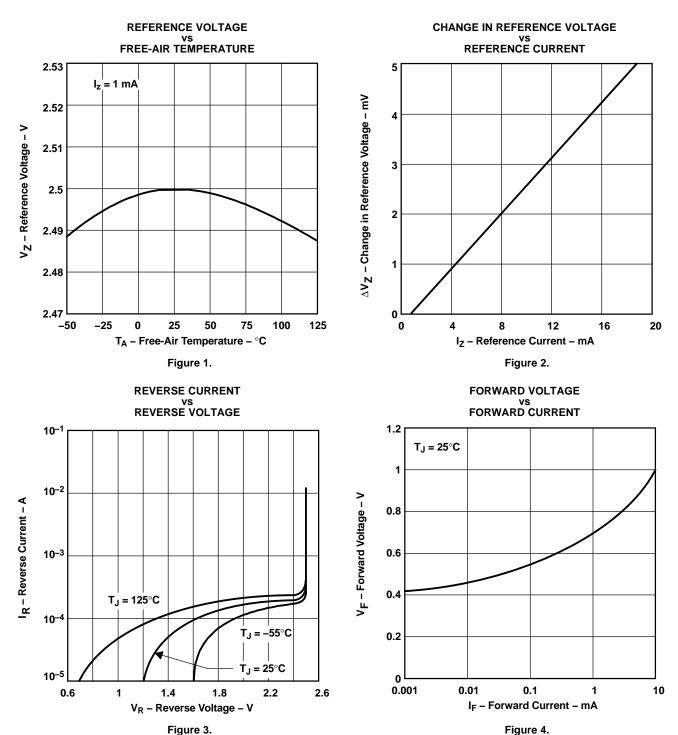
PARAMETER		TEST CONDITIONS		T _A ⁽¹⁾	LT1009C			LT1009I			UNIT	
				'A''	MIN	TYP	MAX	MIN	TYP	MAX	UNII	
			D package	25°C	2.49	2.5	2.51	2.49	2.5	2.51		
V	Reference voltage	I - 1 m 1	LP package	25°C	2.495	2.5	2.505	2.495	2.5	2.505	V	
V _Z	Reference voltage	$I_Z = 1 \text{ mA}$	D package	Full range	2.485		2.515	2.475		2.525	"	
			LP package	Full range	2.491		2.509	2.48		2.52		
V_{F}	Forward voltage	$I_F = 2 \text{ mA}$		25°C	0.4		1	0.4		1	V	
		$I_Z = 1 \text{ mA},$ $V_{ADJ} = \text{GND to } V_Z$		25°C	125			125			m\/	
Adjustment	Adjustment range	$I_Z = 1 \text{ mA},$ $V_{ADJ} = 0.6$	V to V _Z – 0.6 V	25°C	45			45			mV	
	Change in	D package LP package					5			15		
$\Delta V_{Z(temp)}$	reference voltage with temperature			Full range			4			15	mV	
	Average temperature			0°C to 70°C		15	25				ppm/	
α_{VZ}	coefficient of reference voltage (2)			-40°C to 85°C					20	35	°C	
4)/	Change in reference	$I_Z = 400 \mu A \text{ to } 10 \text{ mA}$		25°C		2.6	10		2.6	6	>/	
	voltage with current			Full range			12			10	mV	
$\Delta V_Z/\Delta t$	Long-term change in reference voltage	I _Z = 1 mA		25°C		20			20		ppm/ khr	
7	Deference impedance	1 1		25°C		0.3	1		0.3	1		
Z_Z	Reference impedance	$I_Z = 1 \text{ mA}$		Full range			1.4			1.4	Ω	

 ⁽¹⁾ Full range is 0°C to 70°C for the LT1009C and -40°C to 85°C for the LT1009I.
 (2) The average temperature coefficient of reference voltage is defined as the total change in reference voltage divided by the specified temperature range.

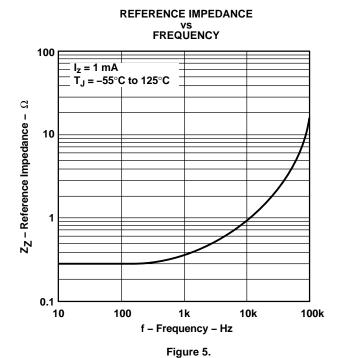


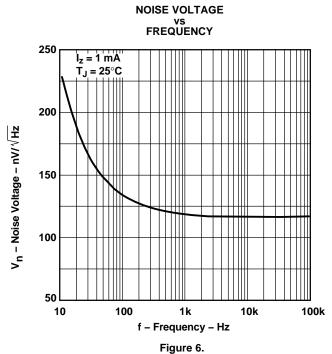
TYPICAL CHARACTERISTICS

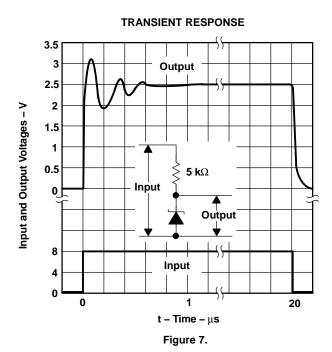
Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.



TYPICAL CHARACTERISTICS (continued)

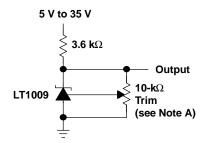








APPLICATION INFORMATION



A. This does not affect temperature coefficient. It provides $\pm 5\%$ trim range.

Figure 8. 2.5-V Reference

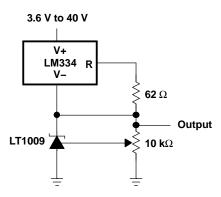


Figure 9. Adjustable Reference With Wide Supply Range

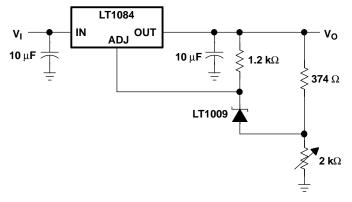


Figure 10. Power Regulator With Low Temperature Coefficient

APPLICATION INFORMATION (continued)

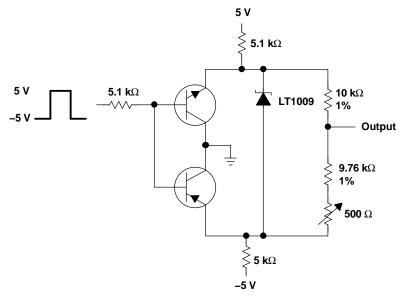


Figure 11. Switchable ±1.25-V Bipolar Reference

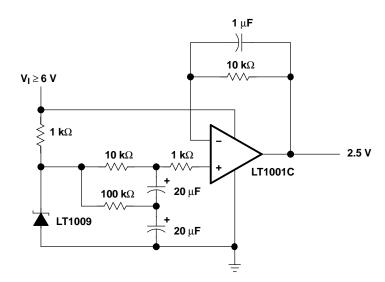


Figure 12. Low-Noise 2.5-V Buffered Reference



PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
LT1009CD	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CDE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CDRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CDRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CLP	ACTIVE	TO-92	LP	3	1000	TBD	CU SNPB	Level-NC-NC-NC
LT1009CLPM	ACTIVE	TO-92	LP	3	2000	TBD	CU SNPB	Level-NC-NC-NC
LT1009CLPR	ACTIVE	TO-92	LP	3	2000	TBD	CU SNPB	Level-NC-NC-NC
LT1009CPK	OBSOLETE	SOT-89	PK	3		TBD	Call TI	Call TI
LT1009CPW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CPWE4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CPWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CPWRE4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009ID	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009IDE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009IDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009IDRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009ILP	ACTIVE	TO-92	LP	3	1000	TBD	CU SNPB	Level-NC-NC-NC
LT1009ILPR	ACTIVE	TO-92	LP	3	2000	TBD	CU SNPB	Level-NC-NC-NC
LT1009IPW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009IPWE4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009IPWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009IPWRE4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009QDR	OBSOLETE	SOIC	D	8		TBD	Call TI	Call TI
LT1009Y	OBSOLETE	XCEPT	Υ	0		TBD	Call TI	Call TI

(1) The marketing status values are defined as follows: **ACTIVE:** Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.





21-Sep-2005

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.



PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp (3)
LT1009CD	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CDE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CDRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CDRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CLP	ACTIVE	TO-92	LP	3	1000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type
LT1009CLPE3	ACTIVE	TO-92	LP	3	1000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type
LT1009CLPM	ACTIVE	TO-92	LP	3	2000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type
LT1009CLPME3	ACTIVE	TO-92	LP	3	2000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type
LT1009CLPR	ACTIVE	TO-92	LP	3	2000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type
LT1009CLPRE3	ACTIVE	TO-92	LP	3	2000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type
LT1009CPK	OBSOLETE	SOT-89	PK	3		TBD	Call TI	Call TI
LT1009CPW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CPWE4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CPWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009CPWRE4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009ID	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009IDE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009IDR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009IDRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009ILP	ACTIVE	TO-92	LP	3	1000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type
LT1009ILPE3	ACTIVE	TO-92	LP	3	1000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type
LT1009ILPR	ACTIVE	TO-92	LP	3	2000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type
LT1009ILPRE3	ACTIVE	TO-92	LP	3	2000	Pb-Free (RoHS)	CU SN	N / A for Pkg Type
LT1009IPW	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM



PACKAGE OPTION ADDENDUM

6-Dec-2006

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	e Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
LT1009IPWE4	ACTIVE	TSSOP	PW	8	150	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009IPWR	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009IPWRE4	ACTIVE	TSSOP	PW	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
LT1009QDR	OBSOLETE	SOIC	D	8		TBD	Call TI	Call TI
LT1009Y	OBSOLETE	XCEPT	Y	0	•	TBD	Call TI	Call TI

⁽¹⁾ The marketing status values are defined as follows:

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LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

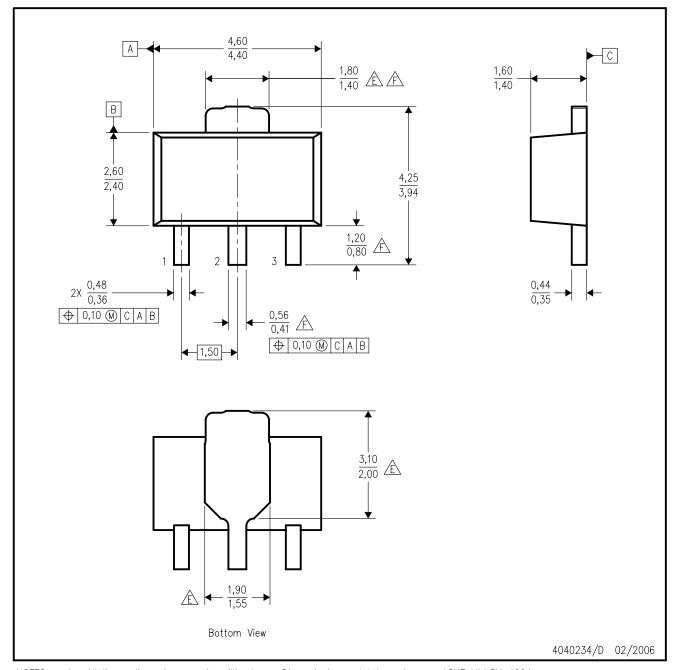
(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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PK (R-PSSO-F3)

PLASTIC SINGLE-IN-LINE PACKAGE



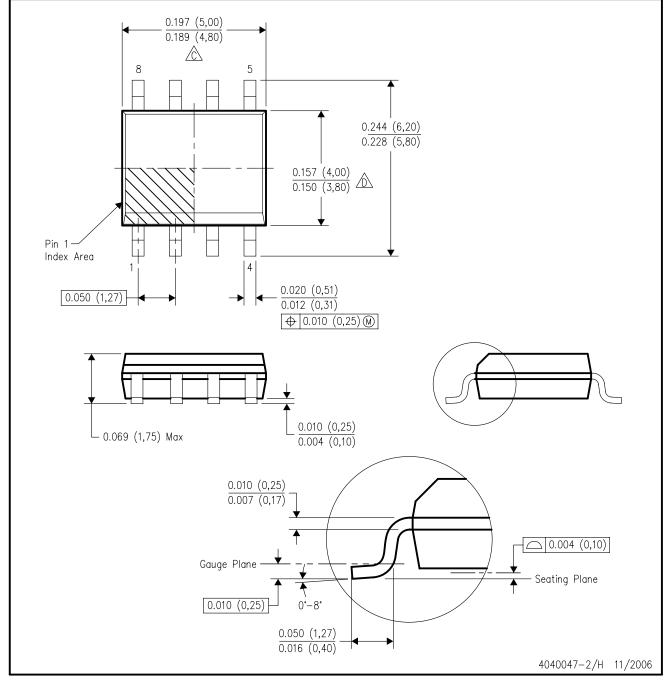
NOTES: A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.

- B. This drawing is subject to change without notice.
- C. The center lead is in electrical contact with the tab.
- D. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion not to exceed 0.15 per side.
- Thermal pad contour optional within these dimensions.
- Falls within JEDEC T0-243 variation AA, except minimum lead length, pin 2 minimum lead width, minimum tab width.



D (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



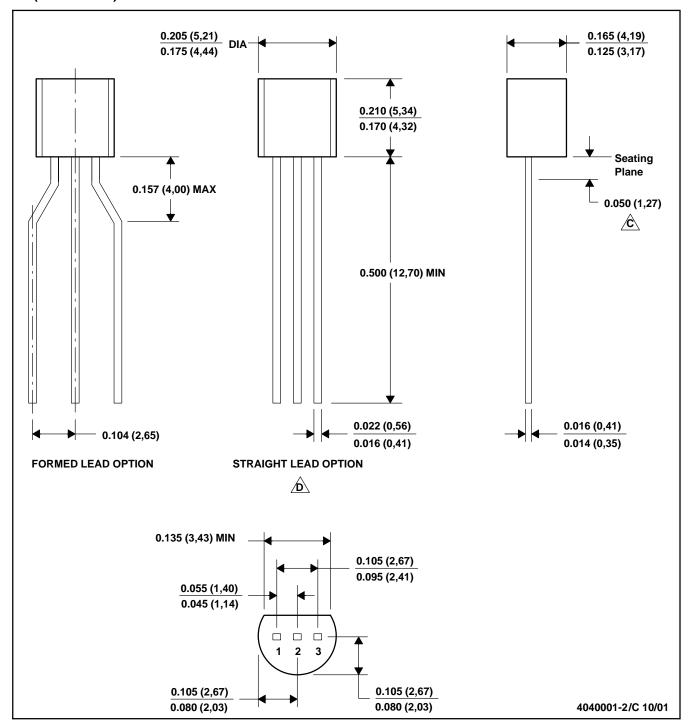
NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.
- Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.
- E. Reference JEDEC MS-012 variation AA.



LP (O-PBCY-W3)

PLASTIC CYLINDRICAL PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).

B. This drawing is subject to change without notice.

C.\ Lead dimensions are not controlled within this area

FAlls within JEDEC TO -226 Variation AA (TO-226 replaces TO-92)

E. Shipping Method:

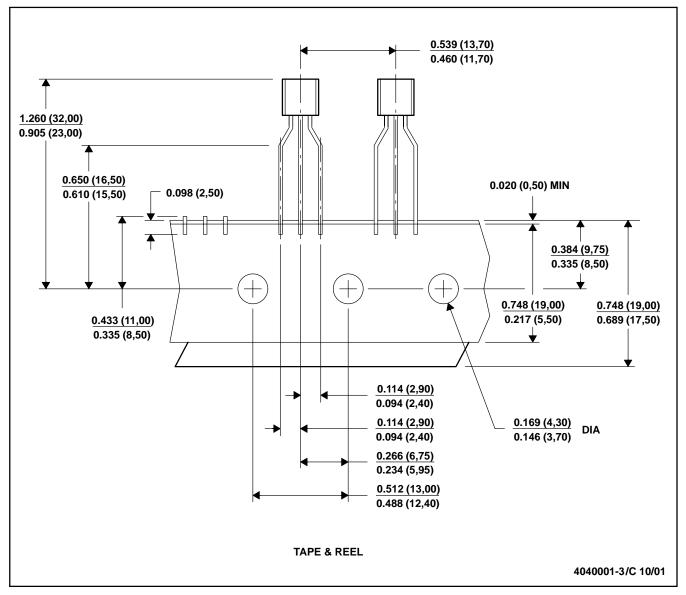
Straight lead option available in bulk pack only.

Formed lead option available in tape & reel or ammo pack.



LP (O-PBCY-W3)

PLASTIC CYLINDRICAL PACKAGE

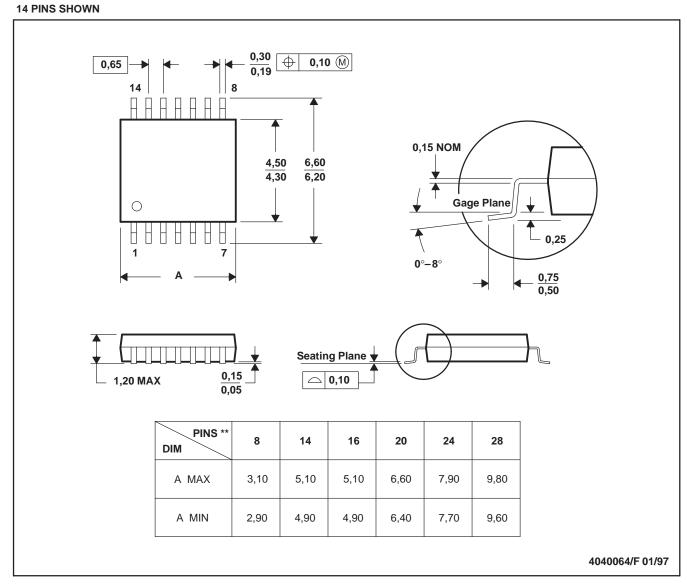


NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. Tape and Reel information for the Format Lead Option package.

PW (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.

D. Falls within JEDEC MO-153

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amplifier.ti.com	Audio	www.ti.com/audio
dataconverter.ti.com	Automotive	www.ti.com/automotive
dsp.ti.com	Broadband	www.ti.com/broadband
interface.ti.com	Digital Control	www.ti.com/digitalcontrol
logic.ti.com	Military	www.ti.com/military
power.ti.com	Optical Networking	www.ti.com/opticalnetwork
microcontroller.ti.com	Security	www.ti.com/security
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	Video & Imaging	www.ti.com/video
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	dataconverter.ti.com dsp.ti.com interface.ti.com logic.ti.com power.ti.com microcontroller.ti.com	amplifier.ti.com dataconverter.ti.com dsp.ti.com dsp.ti.com interface.ti.com logic.ti.com power.ti.com microcontroller.ti.com www.ti.com/lpw Audio Automotive Broadband Digital Control Military Optical Networking Security Telephony Video & Imaging

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