

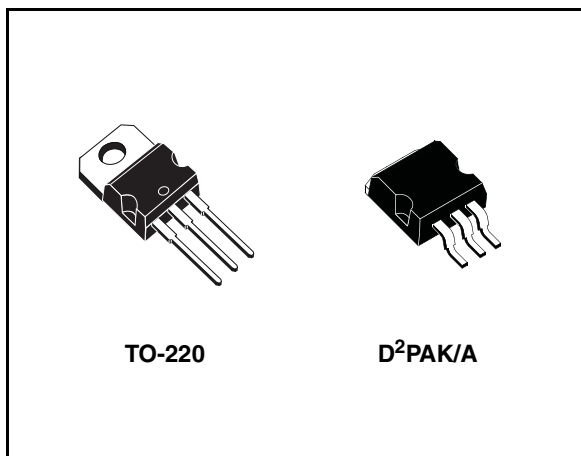


LD1585C series

5A Low dropout fast response positive voltage regulator adjustable and fixed

Features

- Typical dropout 1.2V
- Fast transient response
- Three terminal adjustable or fixed output voltage 1.5V 1.8V, 2.5V, 2.85V, 3.3V, 5V, 8V, 9V
- Guaranteed output current up to 5A
- Output tolerance $\pm 1\%$ at 25°C and $\pm 2\%$ in full temperature range
- Internal power and thermal limit
- Wide operating temperature range 0°C to 125°C
- Package available: TO-220, D²PAK/A
- Pinout compatibility with standard adjustable VREG



Description

The LD1585C is a LOW DROP Voltage Regulator able to provide up to 5A of Output Current. Dropout is guaranteed at a maximum of 1.4V at the maximum output current, decreasing at lower loads. The device has been improved to be utilized in low voltage applications where transient response and minimum input voltage are critical.

The most important feature of the device consist in lower dropout voltage and very fast transient response. A 2.85V output version is suitable for SCSI-2 active termination. Unlike PNP regulators, where a part of the output current is wasted as quiescent current, the LD1585C quiescent current flows into the load, so increase efficiency. Only a 10µF minimum capacitor is need for stability.

The device is supplied in TO-220, D²PAK and D²PAK/A. On chip trimming allows the regulator to reach a very tight output voltage tolerance, within $\pm 1\%$ at 25°C.

Order codes

Part number		
TO-220	D ² PAK/A (T&R)	Output voltage
	LD1585CD2M15R	1.5 V
	LD1585CD2M18R	1.8 V
	LD1585CD2M25R	2.5 V
	LD1585CD2M28R	2.85 V
	LD1585CD2M33R	3.3 V
LD1585CV50	LD1585CD2M50R	5.0 V
	LD1585CD2M80R	8.0 V
	LD1585CD2M90R	9.0 V
LD1585CV	LD1585CD2M-R	ADJ

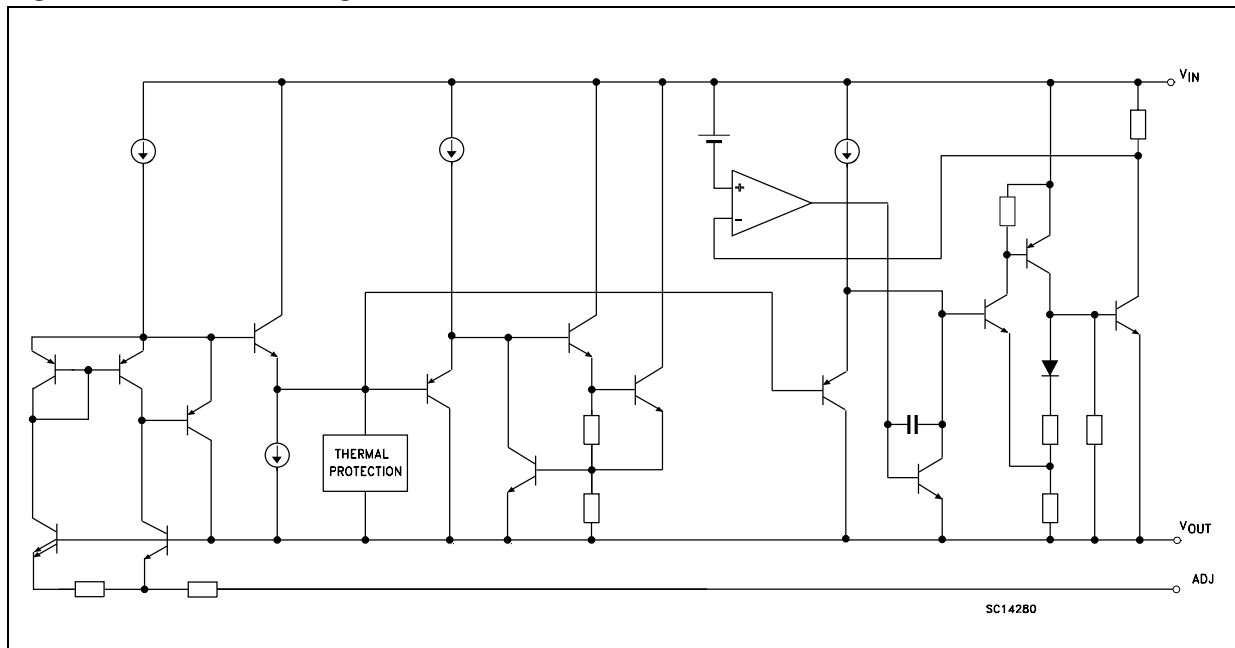
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Contents

1	Diagram	3
2	Pin configuration	4
3	Maximum ratings	5
4	Typical application	6
5	Electrical characteristics	7
6	Typical characteristics	16
7	Package mechanical data	20
8	Revision history	24

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1 Diagram

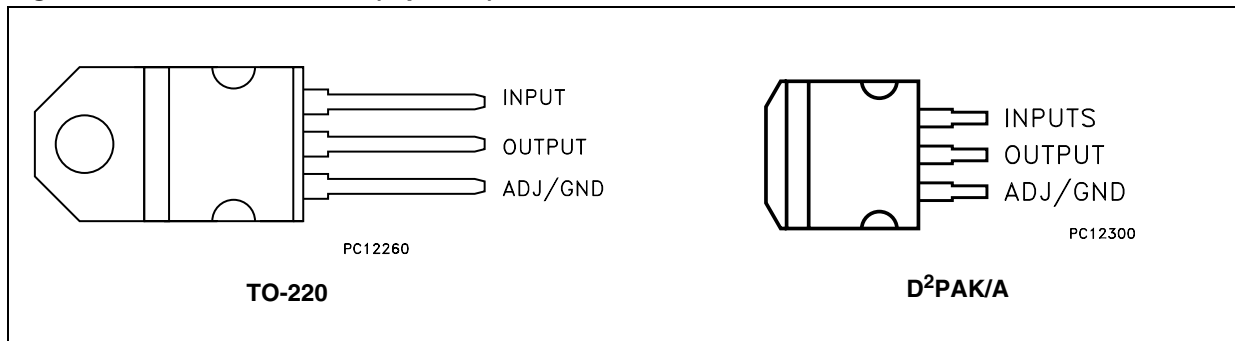
Figure 1. Schematic diagram



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2 Pin configuration

Figure 2. Pin connections (top view)



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3 Maximum ratings

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_I	DC Input Voltage	30	V
I_O	Output Current	Internally Limited	mA
P_D	Power Dissipation	Internally Limited	mW
T_{stg}	Storage Temperature Range	-55 to +150	°C
T_{op}	Operating Junction Temperature Range	0 to +125	°C

Note: Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied

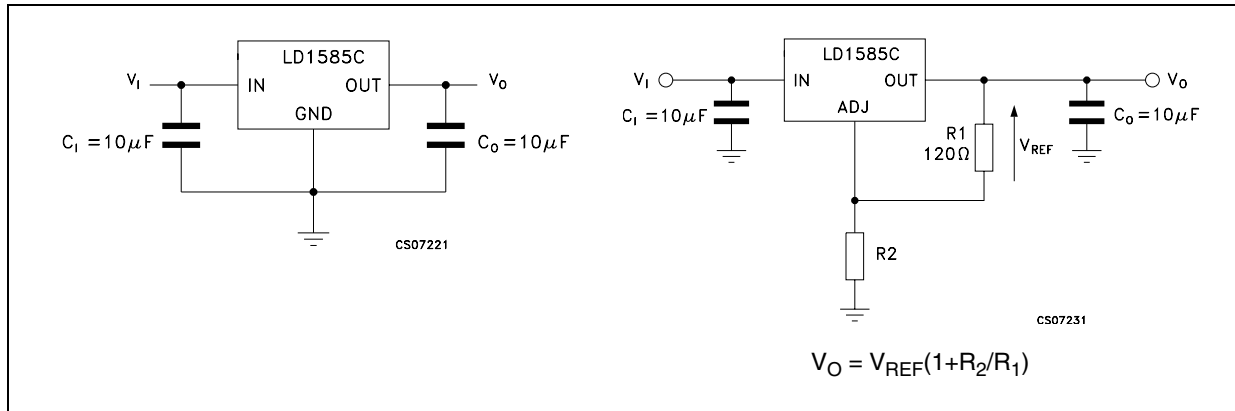
Table 2. Thermal data

Symbol	Parameter	TO-220	D ² PAK/A	Unit
R_{thJC}	Thermal resistance junction-case	3	3	°C/W
R_{thJA}	Thermal resistance junction-ambient	50	62.5	°C/W

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4 Typical application

Figure 3. Application circuits



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5 Electrical characteristics

Table 3. Electrical characteristics of LD1585C#15 ($V_I=4.5V$, $C_I = C_O = 10\mu F$, $T_J = 0$ to $125^\circ C$, unless otherwise specified.)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_O	Output Voltage	$I_O = 0$ mA, $T_J = 25^\circ C$	1.485	1.5	1.515	V
		$I_O = 0$ to 5A, $V_I = 3$ to 25V ⁽¹⁾	1.47	1.5	1.53	V
ΔV_O	Line Regulation	$I_O = 0$ mA, $V_I = 3$ to 15V $T_J = 25^\circ C$		0.005	0.2	%
		$I_O = 0$ mA, $V_I = 3$ to 15V		0.005	0.2	%
ΔV_O	Load Regulation	$I_O = 0$ to 5A, $T_J = 25^\circ C$		0.05	0.3	%
		$I_O = 0$ to 5A		0.05	0.5	%
V_d	Dropout Voltage	$I_O = 5$ A		1.2	1.4	V
I_q	Quiescent Current	$V_I \leq 25V$		5	10	mA
I_{sc}	Short Circuit Current	$V_I - V_O = 5V$	5.5	7		A
	Thermal Regulation	$T_J = 25^\circ C$, 30ms pulse		0.004	0.02	%/W
SVR	Supply Voltage Rejection	$f = 120$ Hz, $C_O = 25\mu F$, $I_O = 5A$ $V_I - V_O = 3 \pm 1V$	60	75		dB
eN	RMS Output Noise Voltage (% of V_O)	$T_J = 25^\circ C$, $f = 10Hz$ to 10KHz		0.003		%
S	Temperature Stability			0.5		%
S	Long Term Stability	$T_J = 125^\circ C$, 1000Hrs		0.03	1	%

1. See short-circuit current curve for available output current at fixed dropout.

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Table 4. Electrical characteristics of LD1585C#18 ($V_I=4.8V$, $C_I = C_O = 10\mu F$, $T_J = 0$ to $125^\circ C$, unless otherwise specified.)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_O	Output Voltage	$I_O = 0$ mA, $T_J = 25^\circ C$	1.782	1.8	1.818	V
		$I_O = 0$ to 5A, $V_I = 3.3$ to 25V ⁽¹⁾	1.764	1.8	1.836	V
ΔV_O	Line Regulation	$I_O = 0$ mA, $V_I = 3.3$ to 15V, $T_J = 25^\circ C$		0.005	0.2	mV
		$I_O = 0$ mA, $V_I = 3.3$ to 15V		0.005	0.2	
ΔV_O	Load Regulation	$I_O = 0$ to 5A, $T_J = 25^\circ C$		0.05	0.3	mV
		$I_O = 0$ to 5A		0.05	0.5	
V_d	Dropout Voltage	$I_O = 5$ A		1.2	1.4	V
I_q	Quiescent Current	$V_I \leq 25V$		5	10	mA
I_{sc}	Short Circuit Current	$V_I - V_O = 5.5V$	5.5	7		A
	Thermal Regulation	$T_J = 25^\circ C$, 30ms pulse		0.004	0.02	%/W
SVR	Supply Voltage Rejection	$f = 120$ Hz, $C_O = 25\mu F$, $I_O = 5A$ $V_I - V_O = 3 \pm 1V$	60	75		dB
eN	RMS Output Noise Voltage (% of V_O)	$T_J = 25^\circ C$, $f = 10Hz$ to 10KHz		0.003		%
S	Temperature Stability			0.5		%
S	Long Term Stability	$T_J = 125^\circ C$, 1000Hrs		0.03	1	%

1. See short-circuit current curve for available output current at fixed dropout.

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Table 5. Electrical characteristics of LD1585C#25 ($V_I=5.5V$, $C_I = C_O = 10\mu F$, $T_J = 0$ to $125^\circ C$, unless otherwise specified.)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_O	Output Voltage	$I_O = 0$ mA, $T_J = 25^\circ C$	2.475	2.5	2.525	V
		$I_O = 0$ to 5A, $V_I = 4$ to 25V ⁽¹⁾	2.45	2.5	2.55	V
ΔV_O	Line Regulation	$I_O = 0$ mA, $V_I = 4$ to 15V, $T_J = 25^\circ C$		0.005	0.2	mV
		$I_O = 0$ mA, $V_I = 4$ to 15V		0.005	0.2	
ΔV_O	Load Regulation	$I_O = 0$ to 5A, $T_J = 25^\circ C$		0.05	0.3	mV
		$I_O = 0$ to 5A		0.05	0.5	
V_d	Dropout Voltage	$I_O = 5$ A		1.2	1.4	V
I_q	Quiescent Current	$V_I \leq 25V$		5	10	mA
I_{sc}	Short Circuit Current	$V_I - V_O = 5.5V$	5.5	7		A
	Thermal Regulation	$T_J = 25^\circ C$, 30ms pulse		0.004	0.02	%/W
SVR	Supply Voltage Rejection	$f = 120$ Hz, $C_O = 25\mu F$, $I_O = 5A$ $V_I - V_O = 3 \pm 1V$	60	75		dB
eN	RMS Output Noise Voltage (% of V_O)	$T_J = 25^\circ C$, $f = 10Hz$ to 10KHz		0.003		%
S	Temperature Stability			0.5		%
S	Long Term Stability	$T_J = 125^\circ C$, 1000Hrs		0.03	1	%

1. See short-circuit current curve for available output current at fixed dropout.

[查询"LD1585CD2M15R"供应商](#)**Table 6. Electrical characteristics of LD1585C#285** ($V_I=5.85V$, $C_I = C_O = 10\mu F$, $T_J = 0$ to $125^\circ C$, unless otherwise specified.)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_O	Output Voltage	$I_O = 0$ mA, $T_J = 25^\circ C$	2.821	2.85	2.879	V
		$I_O = 0$ to 5A, $V_I = 4.5$ to 25V ⁽¹⁾	2.793	2.85	2.907	V
ΔV_O	Line Regulation	$I_O = 0$ mA, $V_I = 4.5$ to 15V, $T_J = 25^\circ C$		0.005	0.2	mV
		$I_O = 0$ mA, $V_I = 4.5$ to 15V		0.005	0.2	
ΔV_O	Load Regulation	$I_O = 0$ to 5A, $T_J = 25^\circ C$		0.05	0.3	mV
		$I_O = 0$ to 5A		0.05	0.5	
V_d	Dropout Voltage	$I_O = 5$ A		1.2	1.4	V
I_q	Quiescent Current	$V_I \leq 25V$		5	10	mA
I_{sc}	Short Circuit Current	$V_I - V_O = 5.5V$	5.5	7		A
	Thermal Regulation	$T_J = 25^\circ C$, 30ms pulse		0.004	0.02	%/W
SVR	Supply Voltage Rejection	$f = 120$ Hz, $C_O = 25\mu F$, $I_O = 5A$ $V_I - V_O = 3 \pm 1V$	60	75		dB
eN	RMS Output Noise Voltage (% of V_O)	$T_J = 25^\circ C$, $f = 10Hz$ to 10KHz		0.003		%
S	Temperature Stability			0.5		%
S	Long Term Stability	$T_J = 125^\circ C$, 1000Hrs		0.03	1	%

1. See short-circuit current curve for available output current at fixed dropout.

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Table 7. Electrical characteristics of LD1585C#33 ($V_I=6.3V$, $C_I = C_O = 10\mu F$, $T_J = 0$ to $125^\circ C$, unless otherwise specified.)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_O	Output Voltage	$I_O = 0$ mA, $T_J = 25^\circ C$	3.267	3.3	3.333	V
		$I_O = 0$ to 5A, $V_I = 4.8$ to 25V ⁽¹⁾	3.234	3.35	3.366	V
ΔV_O	Line Regulation	$I_O = 0$ mA, $V_I = 4.8$ to 15V, $T_J = 25^\circ C$		0.005	0.2	mV
		$I_O = 0$ mA, $V_I = 4.9$ to 15V		0.005	0.2	
ΔV_O	Load Regulation	$I_O = 0$ to 5A, $T_J = 25^\circ C$		0.05	0.3	mV
		$I_O = 0$ to 5A		0.05	0.5	
V_d	Dropout Voltage	$I_O = 5$ A		1.2	1.4	V
I_q	Quiescent Current	$V_I \leq 25V$		5	10	mA
I_{sc}	Short Circuit Current	$V_I - V_O = 5.5V$	5.5	7		A
	Thermal Regulation	$T_J = 25^\circ C$, 30ms pulse		0.004	0.02	%/W
SVR	Supply Voltage Rejection	$f = 120$ Hz, $C_O = 25\mu F$, $I_O = 5A$ $V_I - V_O = 3 \pm 1V$	60	75		dB
eN	RMS Output Noise Voltage (% of V_O)	$T_J = 25^\circ C$, $f = 10Hz$ to 10KHz		0.003		%
S	Temperature Stability			0.5		%
S	Long Term Stability	$T_J = 125^\circ C$, 1000Hrs		0.03	1	%

1. See short-circuit current curve for available output current at fixed dropout.

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Table 8. Electrical characteristics of LD1585C#50 ($V_I=8V$, $C_I = C_O = 10\mu F$, $T_J = 0$ to $125^\circ C$, unless otherwise specified.)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_O	Output Voltage	$I_O = 0$ mA, $T_J = 25^\circ C$	4.95	5	5.05	V
		$I_O = 0$ to 5A, $V_I = 6.5$ to 30V ⁽¹⁾	4.9	5	5.1	V
ΔV_O	Line Regulation	$I_O = 0$ mA, $V_I = 6.5$ to 20V, $T_J = 25^\circ C$		0.005	0.2	mV
		$I_O = 0$ mA, $V_I = 6.5$ to 20V		0.005	0.2	
ΔV_O	Load Regulation	$I_O = 0$ to 5A, $T_J = 25^\circ C$		0.05	0.3	mV
		$I_O = 0$ to 5A		0.05	0.5	
V_d	Dropout Voltage	$I_O = 5$ A		1.2	1.4	V
I_q	Quiescent Current	$V_I \leq 25V$		5	10	mA
I_{sc}	Short Circuit Current	$V_I - V_O = 5.5V$	5.5	7		A
	Thermal Regulation	$T_J = 25^\circ C$, 30ms pulse		0.004	0.02	%/W
SVR	Supply Voltage Rejection	$f = 120$ Hz, $C_O = 25\mu F$, $I_O = 5A$ $V_I - V_O = 3 \pm 1V$	60	75		dB
eN	RMS Output Noise Voltage (% of V_O)	$T_J = 25^\circ C$, $f = 10Hz$ to 10KHz		0.003		%
S	Temperature Stability			0.5		%
S	Long Term Stability	$T_J = 125^\circ C$, 1000Hrs		0.03	1	%

1. See short-circuit current curve for available output current at fixed dropout.

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Table 9. Electrical characteristics of LD1585C#80 ($V_I=11V$, $C_I = C_O = 10\mu F$, $T_J = 0$ to $125^\circ C$, unless otherwise specified.)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_O	Output Voltage	$I_O = 0$ mA, $T_J = 25^\circ C$	7.92	8	8.08	V
		$I_O = 0$ to 5A, $V_I = 9.5$ to 30V ⁽¹⁾	7.84	8	8.16	V
ΔV_O	Line Regulation	$I_O = 0$ mA, $V_I = 9.5$ to 20V, $T_J = 25^\circ C$		0.005	0.2	mV
		$I_O = 0$ mA, $V_I = 9.5$ to 20V		0.005	0.2	
ΔV_O	Load Regulation	$I_O = 0$ to 5A, $T_J = 25^\circ C$		0.05	0.3	mV
		$I_O = 0$ to 5A		0.05	0.5	
V_d	Dropout Voltage	$I_O = 5$ A		1.2	1.4	V
I_q	Quiescent Current	$V_I \leq 25V$		5	10	mA
I_{sc}	Short Circuit Current	$V_I - V_O = 5.5V$	5.5	7		A
	Thermal Regulation	$T_J = 25^\circ C$, 30ms pulse		0.004	0.02	%/W
SVR	Supply Voltage Rejection	$f = 120$ Hz, $C_O = 25\mu F$, $I_O = 5A$ $V_I - V_O = 3 \pm 1V$	60	75		dB
eN	RMS Output Noise Voltage (% of V_O)	$T_J = 25^\circ C$, $f = 10Hz$ to 10KHz		0.003		%
S	Temperature Stability			0.5		%
S	Long Term Stability	$T_J = 125^\circ C$, 1000Hrs		0.03	1	%

1. See short-circuit current curve for available output current at fixed dropout.

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Table 10. Electrical characteristics of LD1585C#90 ($V_I=12V$, $C_I = C_O = 10\mu F$, $T_J = 0$ to $125^\circ C$, unless otherwise specified.)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_O	Output Voltage	$I_O = 0$ mA, $T_J = 25^\circ C$	8.91	9	9.09	V
		$I_O = 0$ to 5A, $V_I = 10.5$ to 30V ⁽¹⁾	8.82	9	9.18	V
ΔV_O	Line Regulation	$I_O = 0$ mA, $V_I = 10.5$ to 20V, $T_J = 25^\circ C$		0.005	0.2	mV
		$I_O = 0$ mA, $V_I = 10.5$ to 20V		0.005	0.2	
ΔV_O	Load Regulation	$I_O = 0$ to 5A, $T_J = 25^\circ C$		0.05	0.3	mV
		$I_O = 0$ to 5A		0.05	0.5	
V_d	Dropout Voltage	$I_O = 5$ A		1.2	1.4	V
I_q	Quiescent Current	$V_I \leq 25V$		5	10	mA
I_{sc}	Short Circuit Current	$V_I - V_O = 5.5V$	5.5	7		A
	Thermal Regulation	$T_J = 25^\circ C$, 30ms pulse		0.004	0.02	%/W
SVR	Supply Voltage Rejection	$f = 120$ Hz, $C_O = 25\mu F$, $I_O = 5A$ $V_I - V_O = 3 \pm 1V$	60	75		dB
eN	RMS Output Noise Voltage (% of V_O)	$T_J = 25^\circ C$, $f = 10Hz$ to 10KHz		0.003		%
S	Temperature Stability			0.5		%
S	Long Term Stability	$T_J = 125^\circ C$, 1000Hrs		0.03	1	%

1. See short-circuit current curve for available output current at fixed dropout.

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Table 11. Electrical characteristics of LD1585C# ($V_I=4.25V$, $C_I = C_O = 10\mu F$, $T_J = 0$ to $125^\circ C$, unless otherwise specified.)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V_O	Output Voltage	$I_O = 10mA$, $V_I - V_O = 3V$, $T_J = 25^\circ C$	1.237	1.25	1.263	V
		$I_O = 10mA$ to $5A$, $V_I - V_O = 1.5$ to $25V$ ⁽¹⁾	1.225	1.25	1.275	V
ΔV_O	Line Regulation	$I_O = 10mA$, $V_I = 2.75$ to $15V$, $T_J = 25^\circ C$		0.015	0.2	%
		$I_O = 10mA$, $V_I = 2.75$ to $15V$		0.1	0.2	%
ΔV_O	Load Regulation	$I_O = 10mA$ to $5A$, $T_J = 25^\circ C$		0.1	0.3	%
		$I_O = 0$ to $5A$		0.25	0.5	%
V_d	Dropout Voltage	$I_O = 5A$		1.2	1.4	V
$I_{O(min)}$	Minimum Load Current	$V_I = 25V$		3	10	mA
I_{sc}	Short Circuit Current	$V_I - V_O = 5.5V$	5.5	7		A
	Thermal Regulation	$T_J = 25^\circ C$, 30ms pulse		0.004	0.02	%/W
SVR	Supply Voltage Rejection	$f = 120$ Hz, $C_O = 25\mu F$, $C_{ADJ} = 25\mu F$, $I_O = 5A$, $V_I - V_O = 3 \pm 1V$	60	75		dB
I_{ADJ}	Adjust Pin Current	$I_O = 10$ mA		50	100	μA
ΔI_{ADJ}	Adjust Pin Current Change	$I_O = 10mA$ to $5A$, $V_I = 3$ to $25V$ ⁽¹⁾		0.2	5	μA
eN	RMS Output Noise Voltage (% of V_O)	$T_J = 25^\circ C$, $f = 10Hz$ to $10KHz$		0.003		%
S	Temperature Stability			0.5		%
S	Long Term Stability	$T_J = 125^\circ C$, 1000Hrs		0.5		%

1. See short-circuit current curve for available output current at fixed dropout.

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6 Typical characteristics

(unless otherwise specified $T_J=25^\circ\text{C}$, $C_I=C_O=10\mu\text{F}$ tant.)

Figure 4. Output voltage vs temperature

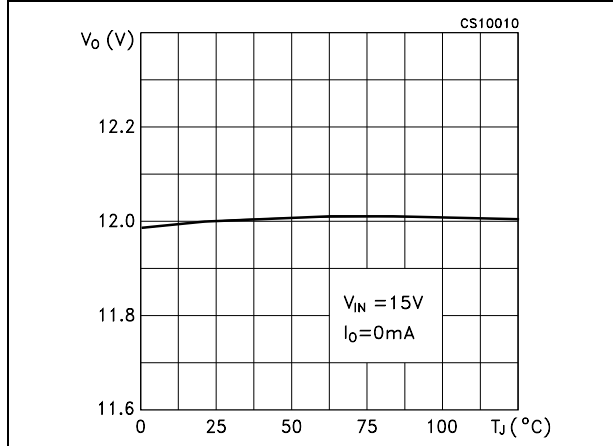


Figure 5. Short circuit current vs dropout voltage

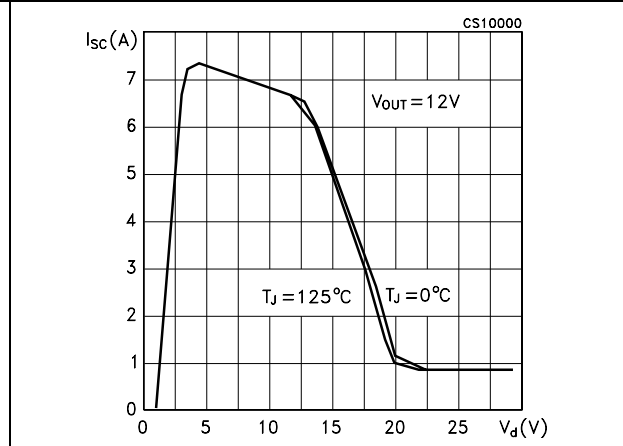


Figure 6. Line regulation vs temperature

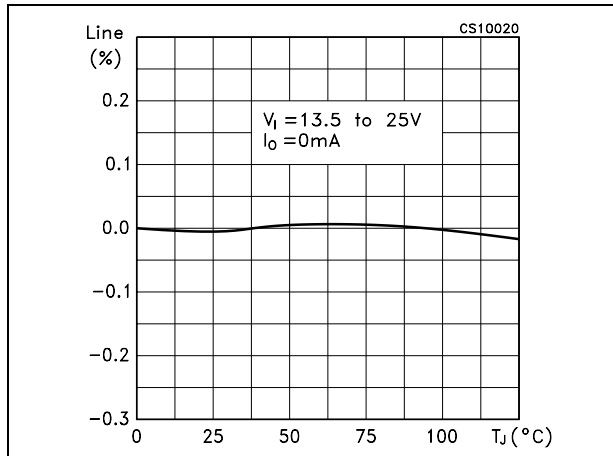


Figure 7. Line regulation vs temperature

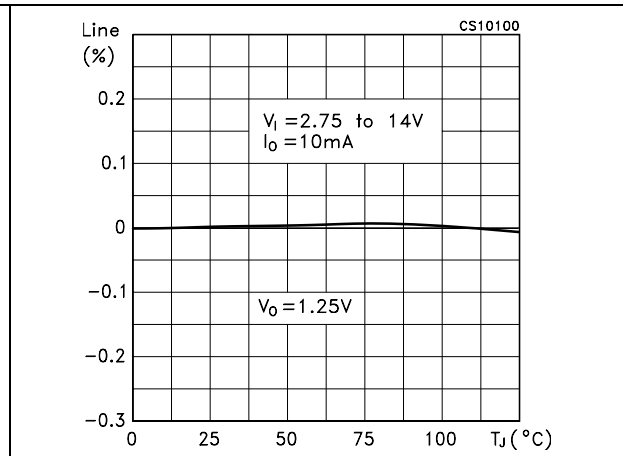


Figure 8. Load regulation vs temperature

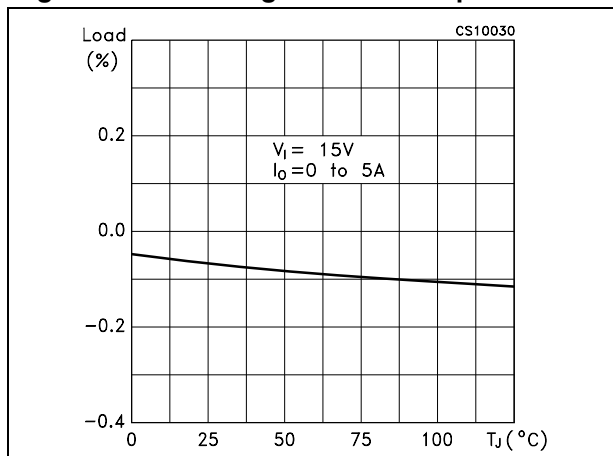
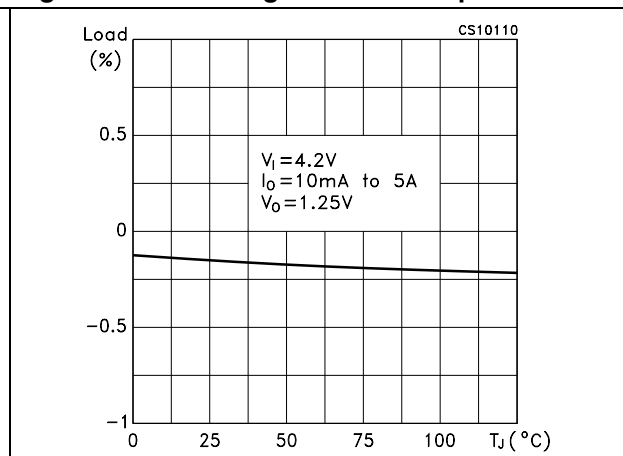


Figure 9. Load regulation vs temperature



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Figure 10. Dropout voltage vs temperature

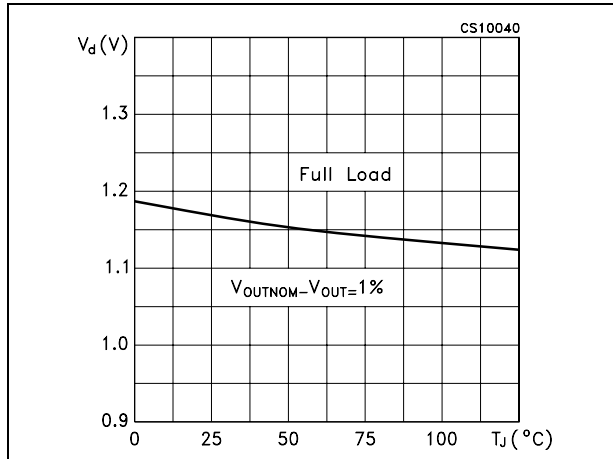


Figure 11. Dropout voltage vs output current

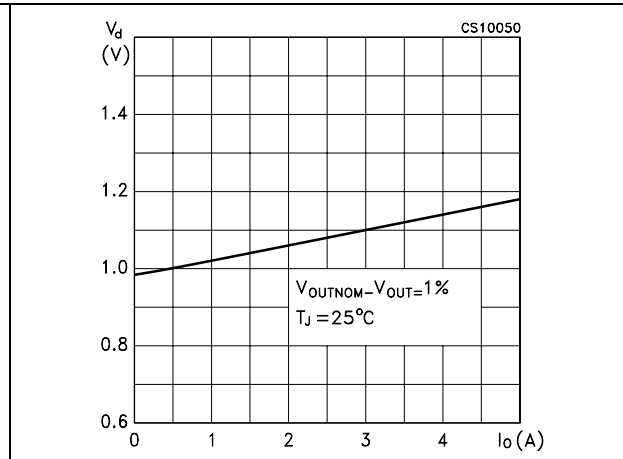


Figure 12. Adjust pin current vs input voltage

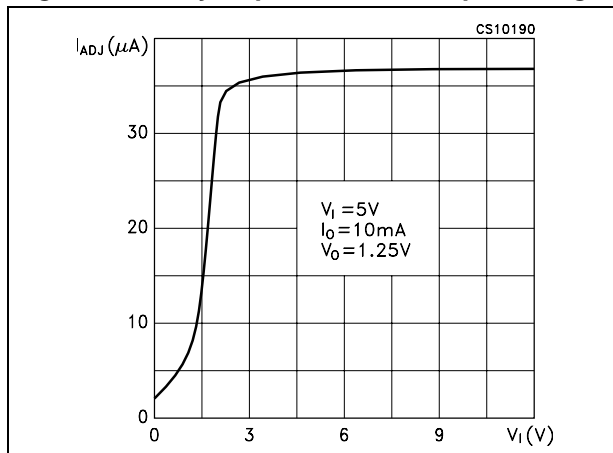


Figure 13. Adjust pin current vs temperature

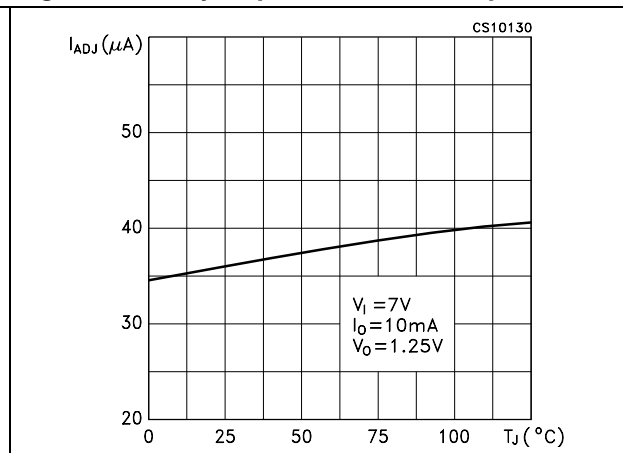


Figure 14. Adjust pin current change vs temperature

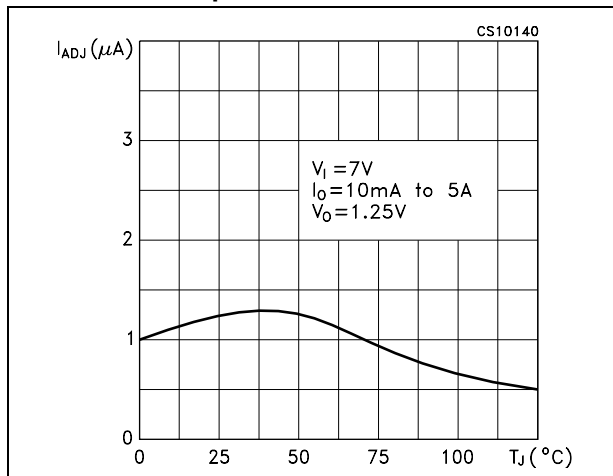
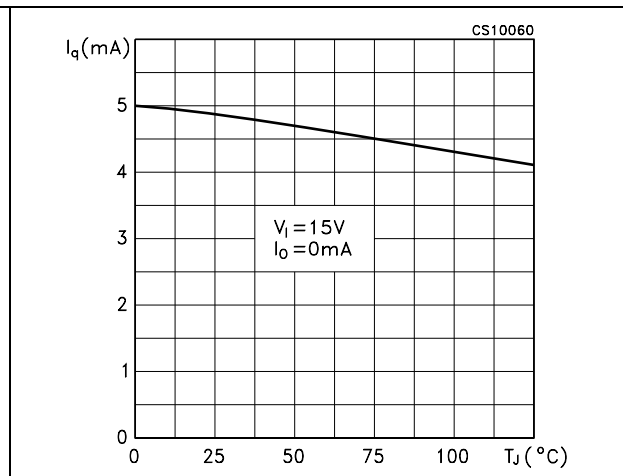


Figure 15. Quiescent current vs temperature



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Figure 16. Reference voltage vs temperature

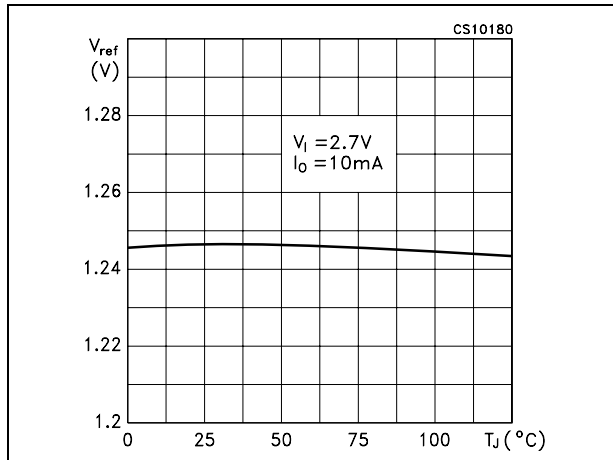


Figure 17. Minimum load current vs temperature

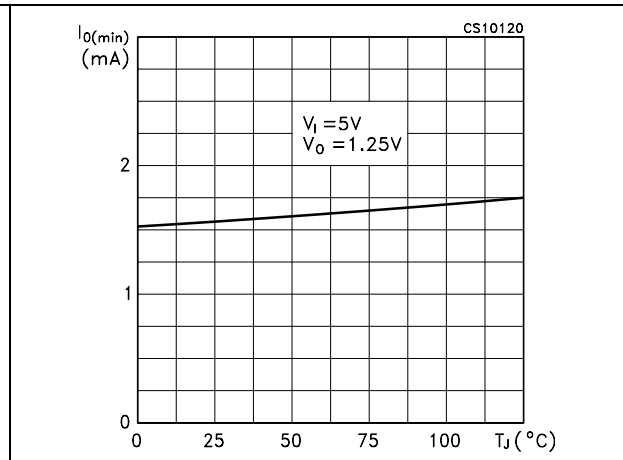


Figure 18. Supply voltage rejection vs output current

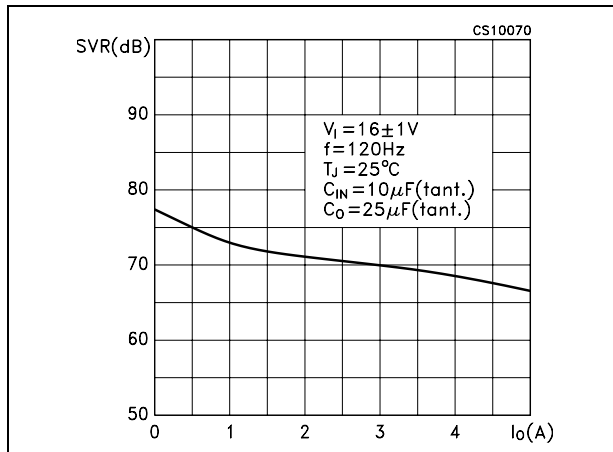


Figure 19. Supply voltage rejection vs output current

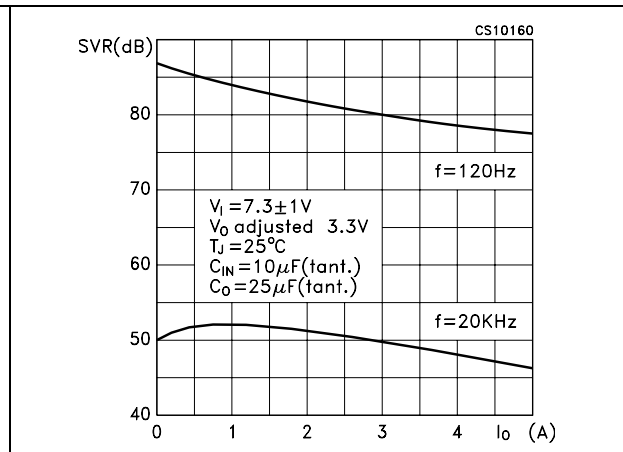


Figure 20. Supply voltage rejection vs frequency

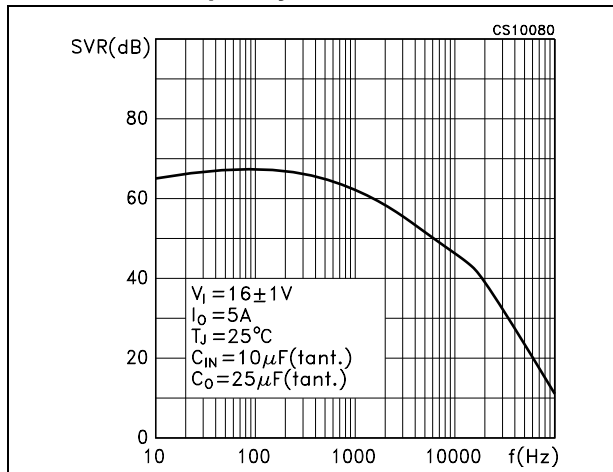
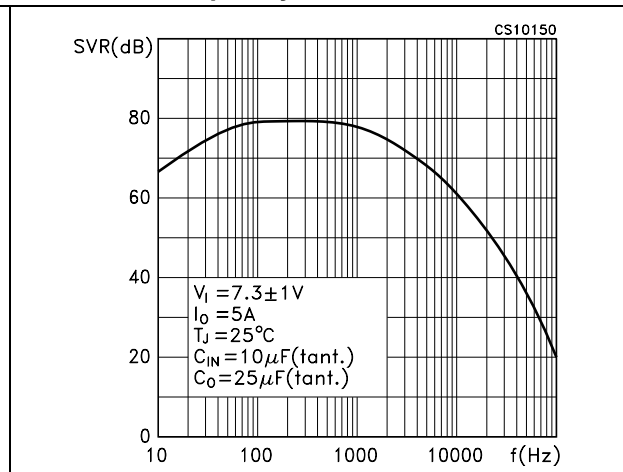


Figure 21. Supply voltage rejection vs frequency



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Figure 22. Supply voltage rejection vs temperature

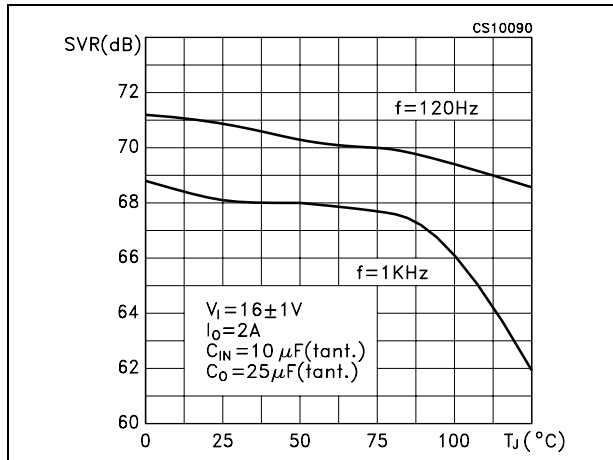


Figure 23. Supply voltage rejection vs temperature

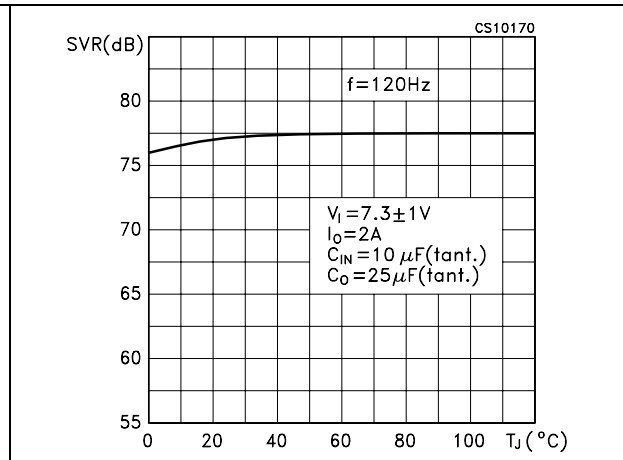


Figure 24. Line transient

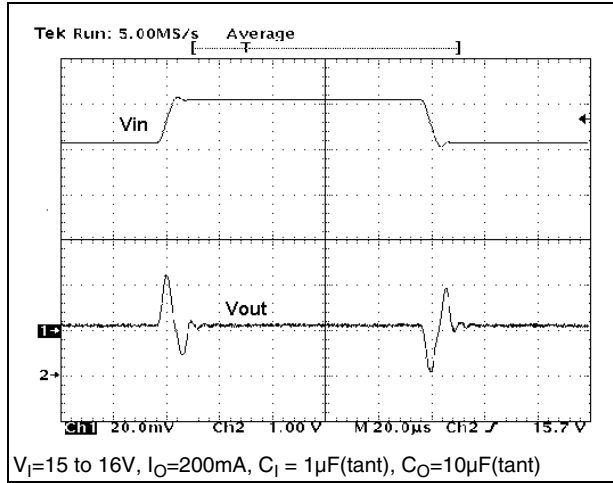


Figure 25. Load transient

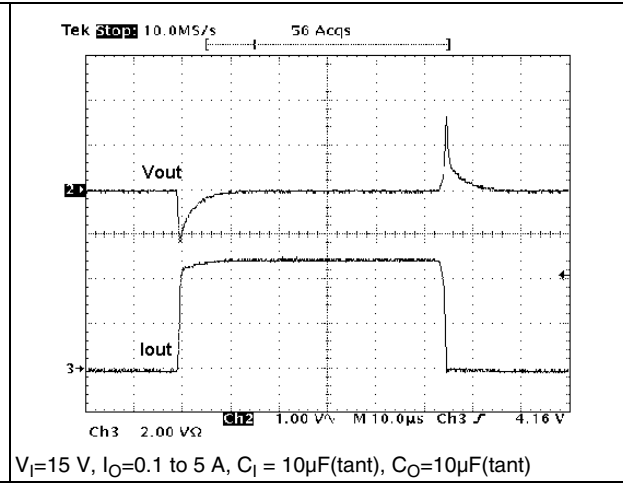
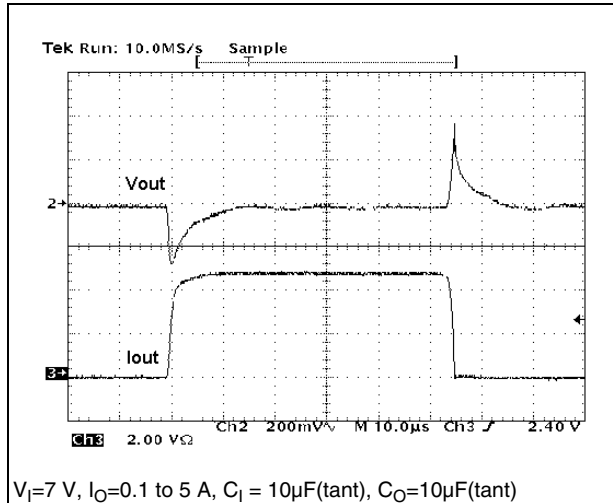


Figure 26. Load transient



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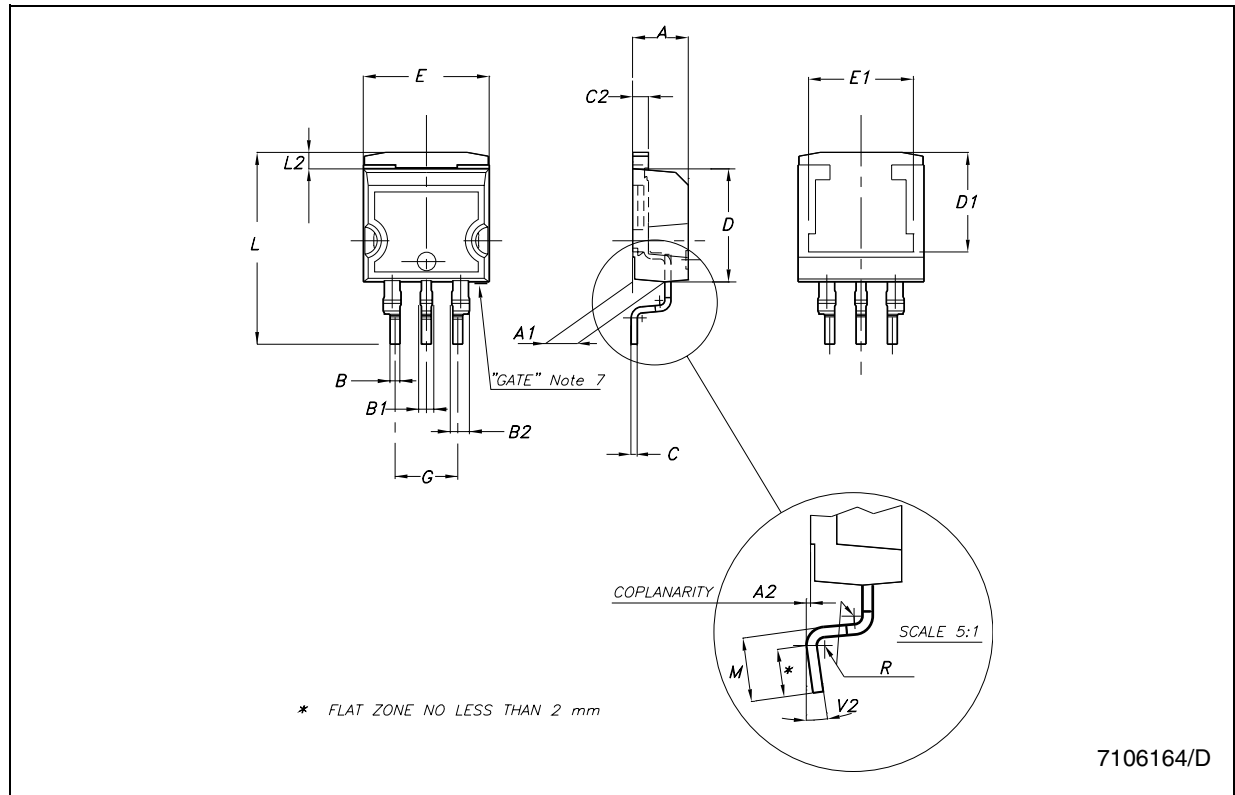
7 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK[®] packages. These packages have a Lead-free second level interconnect. The category of second Level Interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

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D²PAK/A MECHANICAL DATA

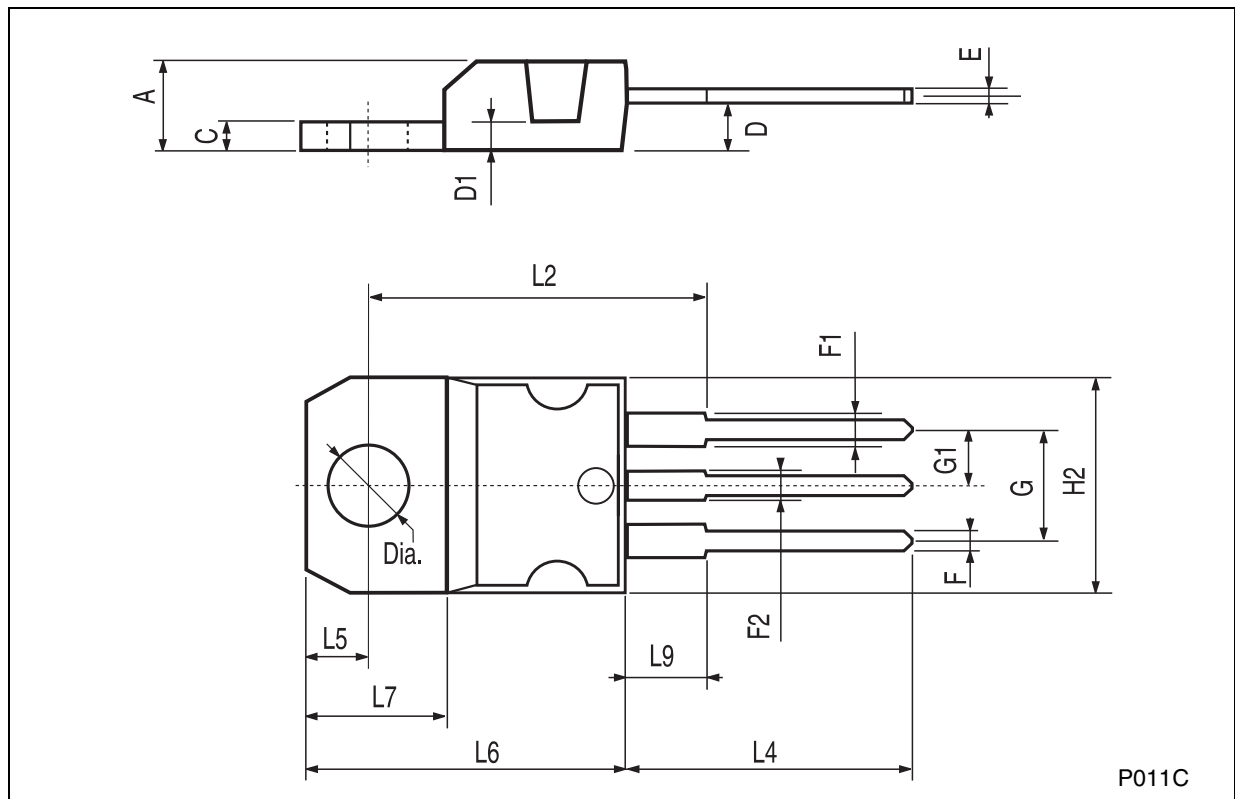
DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	4.40		4.60	0.173		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
B	0.7		0.93	0.028		0.037
B1	0.8		1.3	0.031		0.051
B2	1.14		1.7	0.045		0.067
C	0.45		0.60	0.018		0.024
C2	1.23		1.36	0.048		0.054
D	8.95		9.35	0.352		0.368
D1		8			0.315	
E	10		10.4	0.394		0.409
E1		8.5			0.335	
G	4.88		5.28	0.192		0.208
L	15		15.85	0.591		0.624
L2	1.27		1.4	0.050		0.055
M	2.4		3.2	0.094		0.126
R		0.4			0.016	
V2	0°		8°	0°		8°



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TO-220 MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	4.40		4.60	0.173		0.181
C	1.23		1.32	0.048		0.051
D	2.40		2.72	0.094		0.107
D1		1.27			0.050	
E	0.49		0.70	0.019		0.027
F	0.61		0.88	0.024		0.034
F1	1.14		1.70	0.044		0.067
F2	1.14		1.70	0.044		0.067
G	4.95		5.15	0.194		0.203
G1	2.4		2.7	0.094		0.106
H2	10.0		10.40	0.393		0.409
L2		16.4			0.645	
L4	13.0		14.0	0.511		0.551
L5	2.65		2.95	0.104		0.116
L6	15.25		15.75	0.600		0.620
L7	6.2		6.6	0.244		0.260
L9	3.5		3.93	0.137		0.154
DIA.	3.75		3.85	0.147		0.151

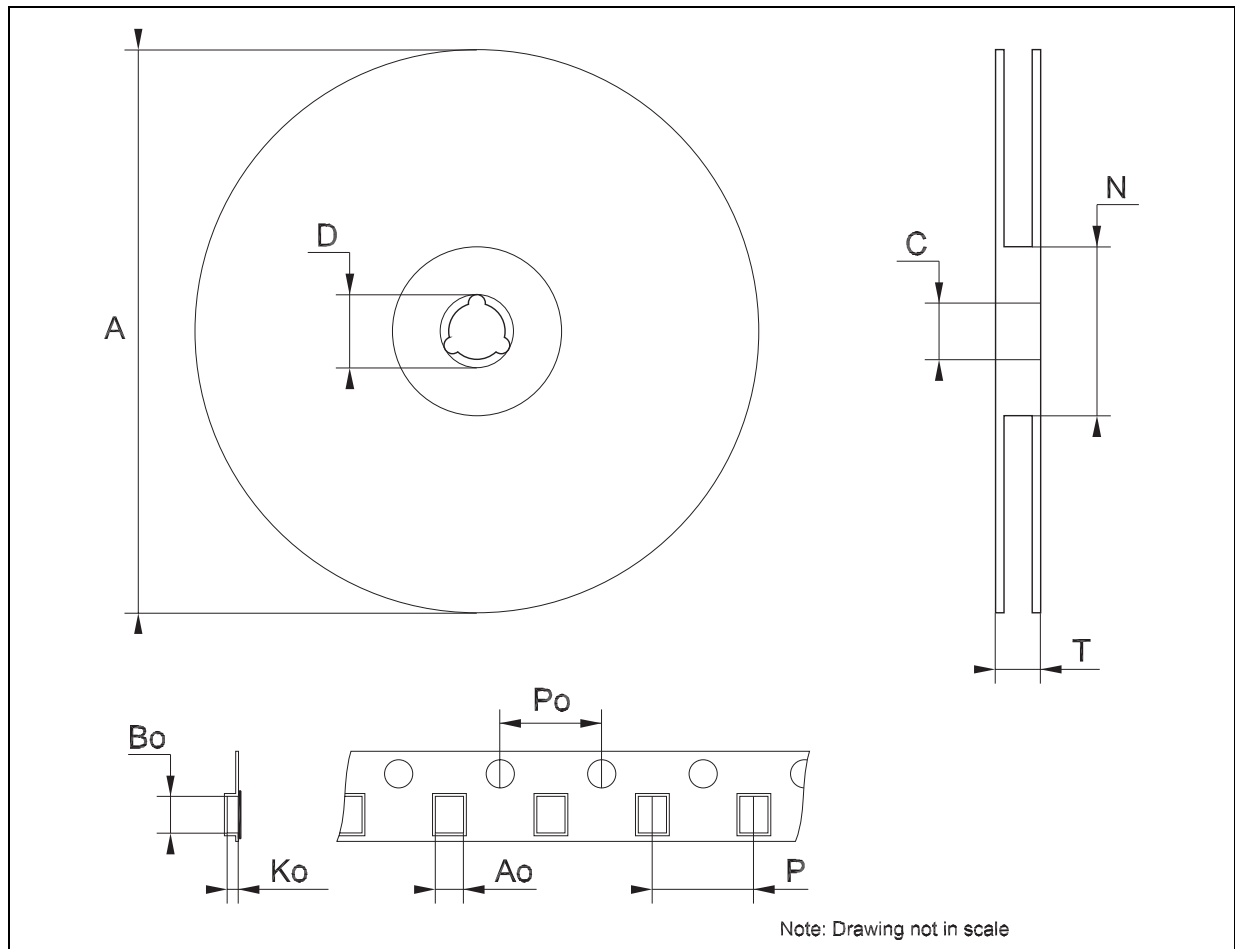


P011C

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Tape & Reel D²PAK-P²PAK-D²PAK/A-P²PAK/A MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A			180			7.086
C	12.8	13.0	13.2	0.504	0.512	0.519
D	20.2			0.795		
N	60			2.362		
T			14.4			0.567
Ao	10.50	10.6	10.70	0.413	0.417	0.421
Bo	15.70	15.80	15.90	0.618	0.622	0.626
Ko	4.80	4.90	5.00	0.189	0.193	0.197
Po	3.9	4.0	4.1	0.153	0.157	0.161
P	11.9	12.0	12.1	0.468	0.472	0.476



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8 Revision history

Table 12. Revision history

Date	Revision	Changes
07-Oct-2004	3	Mistake order codes - Table 1.
20-Oct-2005	4	Order codes has been updated.
08-Jun-2007	5	Order codes has been updated and the document has been reformatted.

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