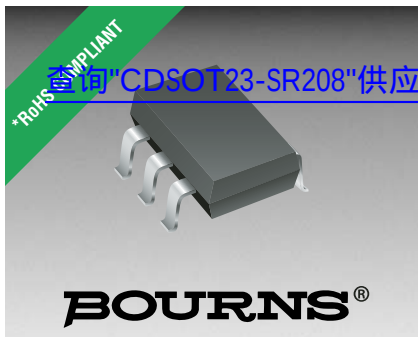




查询"CDSOT23-SR208"供应商

Features

- Lead free device (RoHS Compliant*)
- Protects 4 lines
- ESD & EFT protection
- Low capacitance <5 pF

Applications

- Portable electronics
- Cell phones
- Video/graphics card
- USB 2.0 interface
- Sensor interface

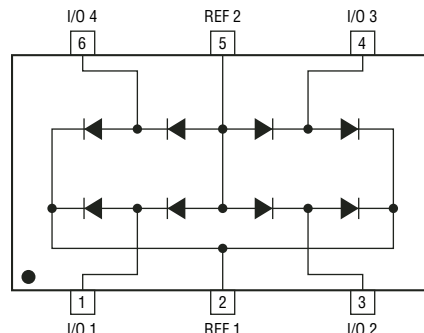
CDSOT23-SR208 – Steering Diode Array

General Information

The markets of portable communications, computing and video equipment are challenging the semiconductor industry to develop increasingly smaller electronic components.

Bourns offers Transient Voltage Suppressor Array diodes for EFT and ESD protection applications, in compact chip package SOT23-6 size format. Bourns® Chip Diodes conform to JEDEC standards, are easy to handle on standard pick and place equipment and their flat configuration minimizes roll away.

The Bourns® device will meet IEC 61000-4-2 (ESD) and IEC 61000-4-4 (EFT) requirements.



Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	Min.	Nom.	Max.	Unit
Repetitive Peak Forward Current ($t_p = 5 \mu\text{s}$, $F = 50 \text{ KHz}$)	I_{FRM}			700	mA
Continuous Power Dissipation	P_{PC}			500	mW
Forward Voltage @ 5 mA	V_F			1.2	V
Repetitive Peak Reverse Voltage @ 10 μA	V_{RRM}			20	V
Reverse Leakage Current @ V_{RRM} 5V	I_R			1	μA
Capacitance ¹	$C_{j(SD)}$			5	pF
Forward Surge Rating (1/20 s @ 25°C , $I_F = 10 \text{ mA}$)	V_F			1.5	V

Note:

1. Apply a 5 V bias between pins 2 (REF 1) to pin 5 (REF 2). Measure C_j between any I/O pins to pin 5 (REF 2) and divide by two.

Thermal Characteristics (@ $T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	Min.	Nom.	Max.	Unit
Operating Temperature	T_J	-55	+25	+150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55	+25	+150	$^\circ\text{C}$

*RoHS Directive 2002/95/EC Jan 27 2003 including Annex.

Specifications are subject to change without notice.

Customers should verify actual device performance in their specific applications.

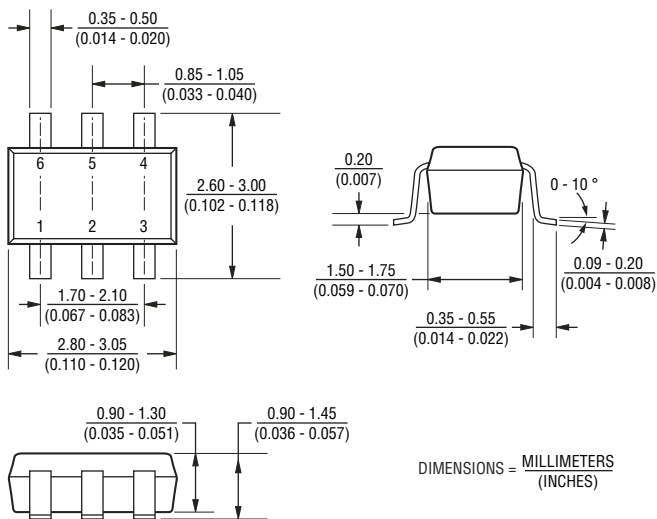
CDSOT23-SR208 – Steering Diode Array

BOURNS®

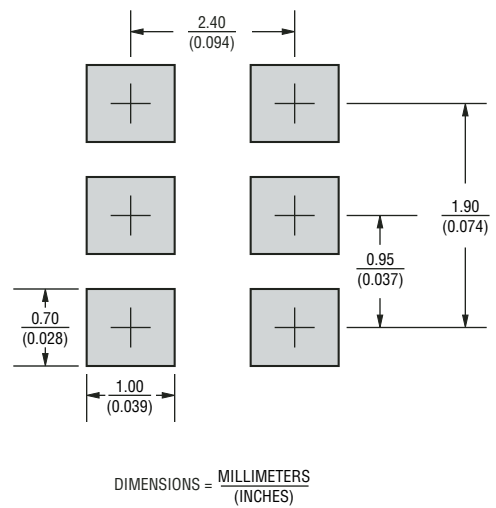
Mechanical Characteristics

This is a molded JEDEC SOT23-6 package with lead free 100 % Tin (Sn) on the lead frame. It weighs approximately 15 mg and has a flammability rating of UL 94V-0.

Product Dimensions



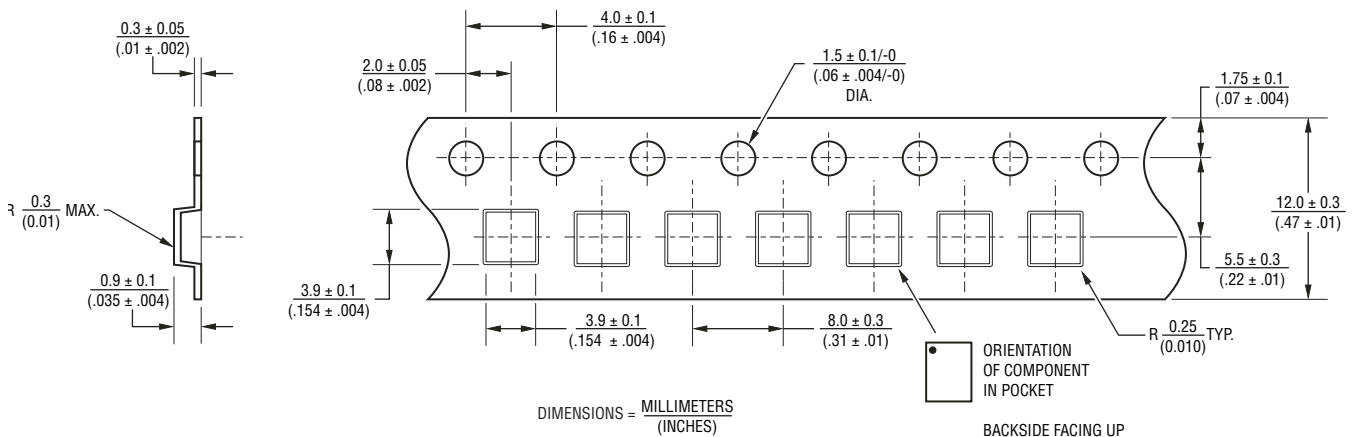
Recommended Footprint



Packaging Information

The product will be dispensed in Tape and Reel format (see diagram below).

TOP SIDE VIEW
(INTO COMPONENT POCKET)



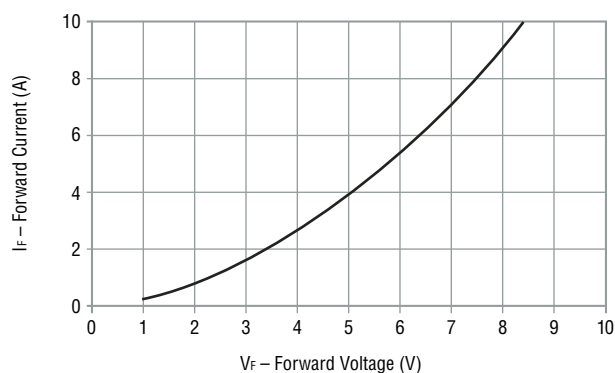
Devices are packed in accordance with EIA standard RS-481-A and specifications shown here.

CDSOT23-SR208 – Steering Diode Array

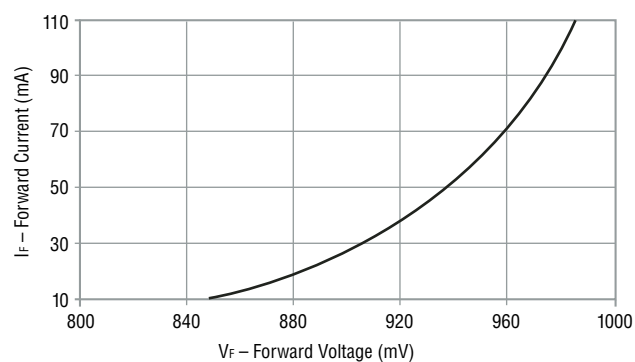
BOURNS®

Performance Graphs

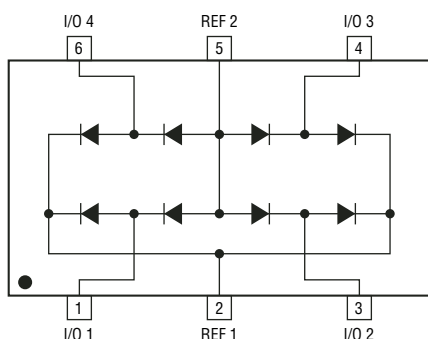
Peak Forward Current vs Forward Voltage



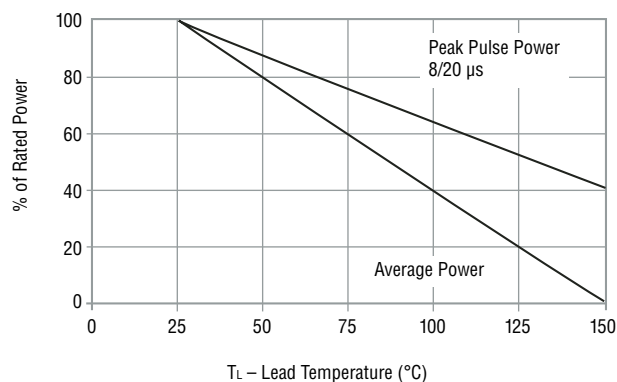
Low Forward Current vs Forward Voltage



Block Diagram



Power Derating Curve



How To Order

CD SOT23 - SR 208

Common Code _____
 CD = Chip Diode
 Package _____
 SOT23 = SOT23-6 Package
 Model _____
 SR = Steering Diode
 Model Number _____
 208

Typical Part Marking

CDSOT23-SR208 PSB



Reliable Electronic Solutions

Asia-Pacific:

Tel: +886-2 2562-4117 • Fax: +886-2 2562-4116

Europe:

Tel: +41-41 768 5555 • Fax: +41-41 768 5510

The Americas:

Tel: +1-951 781-5500 • Fax: +1-951 781-5700

www.bourns.com