

REVISIONS

| LTR | DESCRIPTION | DATE (YR-MO-DA) | APPROVED |
|-----|--|-----------------|--------------------|
| A | Increase maximum power dissipation in 1.3. Editorial changes throughout. | 92-02-10 | <i>[Signature]</i> |

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| SHEET | | | | | | | | | | | | | | | | | | | | |
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| REV STATUS OF SHEETS | REV | A | A | A | A | A | A | A | A | A | A | A | | | | | | | | |
| | SHEET | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 10 | | | | | | | | | |

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| PMIC N/A | PREPARED BY Monica L Grose | DEFENSE ELECTRONICS SUPPLY CENTER DAYTON, OHIO 45444 | | |
| <p>STANDARDIZED MILITARY DRAWING</p> <p>THIS DRAWING IS AVAILABLE FOR USE BY ALL DEPARTMENTS AND AGENCIES OF THE DEPARTMENT OF DEFENSE</p> <p>AMSC N/A</p> | CHECKED BY D.A. DiCenzo | MICROCIRCUITS, DIGITAL, BIPOLAR ADVANCED SCHOTTKY TTL, NOR DRIVER, MONOLITHIC SILICON | | |
| | APPROVED BY Robert P Evans | | | |
| | DRAWING APPROVAL DATE 27 NOVEMBER 1987 | SIZE A | CAGE CODE 67268 | 5962-87794 |
| | REVISION LEVEL A | SHEET | | 1 |

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Use previous edition until exhausted.

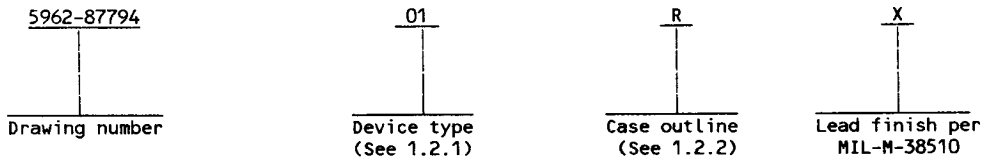
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DISTRIBUTION STATEMENT A. Approved for public release; distribution is unlimited.

1. SCOPE

1.1 Scope. This drawing describes device requirements for class B microcircuits in accordance with 1.2.1 of MIL-STD-883, Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices".

1.2 Part or Identifying Number (PIN). The complete PIN shall be as shown in the following example:



1.2.1 Device type(s). The device type(s) shall identify the circuit function as follows:

| <u>Device type</u> | <u>Generic number</u> | <u>Circuit function</u> |
|--------------------|-----------------------|-------------------------|
| 01 | 54AS805 | Hex 2-input NOR drivers |

1.2.2 Case outline(s). The case outline(s) shall be as designated in appendix C of MIL-M-38510, and as follows:

| <u>Outline letter</u> | <u>Case outline</u> |
|-----------------------|---|
| R | D-8 (20-lead, 1.060" x .310" x .200"), dual in line package |
| S | F-9 (20-lead, .540" x .300" x .100"), flat package |
| 2 | C-2 (20-terminal, .358" x .358" x .100"), square chip carrier package |

1.3 Absolute maximum ratings.

| | | |
|--|-------|-----------------------------|
| Supply voltage range | ----- | -0.5 V dc to +7.0 V dc |
| Input voltage range | ----- | -1.2 V at -18 mA to +7.0 V |
| Storage temperature range | ----- | -65°C to +150°C |
| Maximum power dissipation (P_D) per device ^{1/} | ----- | 176 mW |
| Lead temperature (soldering, 10 seconds) | ----- | +300°C |
| Thermal resistance, junction-to-case (θ_{JC}): | | |
| Cases R, S, and 2 | ----- | See MIL-M-38510, appendix C |
| Junction temperature (T_J) | ----- | +175°C |

1.4 Recommended operating conditions.

| | | |
|---|-------|-----------------------------------|
| Supply voltage range (V_{CC}) | ----- | +4.5 V minimum to +5.5 V maximum. |
| Minimum high level input voltage (V_{IH}) | ----- | 2.0 V |
| Maximum low level input voltage (V_{IL}) | ----- | 0.8 V |
| Case operating temperature range (T_C) | ----- | -55°C to +125°C |

^{1/} Must withstand the added P_D due to short circuit test (e.g., I_{OS}).

| | | |
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2. APPLICABLE DOCUMENTS

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 specification, standard, and bulletin. Unless otherwise specified, the following specification, standard, and bulletin of the issue listed in that issue of the Department of Defense Index of Specifications and Standards specified in the solicitation, form a part of this drawing to the extent specified herein.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microelectronics.

BULLETIN

MILITARY

MIL-BUL-103 - List of Standardized Military Drawings (SMD's).

(Copies of the specification, standard, and bulletin required by manufacturers in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this drawing and the references cited herein, the text of this drawing shall take precedence.

3. REQUIREMENTS

3.1 Item requirements. The individual item requirements shall be in accordance with 1.2.1 of MIL-STD-883, "Provisions for the use of MIL-STD-883 in conjunction with compliant non-JAN devices" and as specified herein.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Case outline(s). The case outline(s) shall be in accordance with 1.2.2 herein.

3.2.2 Terminal connections. The terminal connections shall be as specified on figure 1.

3.2.3 Truth table. The truth table shall be as specified on figure 2.

3.2.4 Logic diagram. The logic diagram shall be as specified on figure 3.

3.2.5 Test circuit and waveforms. The test circuit and waveforms shall be as specified on figure 4.

3.3 Electrical performance characteristics. Unless otherwise specified herein, the electrical performance characteristics are as specified in table I and shall apply over the full case operating temperature range.

3.4 Electrical test requirements. The electrical test requirements shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table I.

3.5 Marking. Marking shall be in accordance with MIL-STD-883 (see 3.1 herein). The part shall be marked with the PIN listed in 1.2 herein. In addition, the manufacturer's PIN may also be marked as listed in MIL-BUL-103 (see 6.6 herein).

3.6 Certificate of compliance. A certificate of compliance shall be required from a manufacturer in order to be listed as an approved source of supply in MIL-BUL-103 (see 6.6 herein). The certificate of compliance submitted to DESC-ECC prior to listing as an approved source of supply shall affirm that the manufacturer's product meets the requirements of MIL-STD-883 (see 3.1 herein) and the requirements herein.

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TABLE I. Electrical performance characteristics.

| Test | Symbol | Conditions -55°C ≤ T _C ≤ +125°C unless otherwise specified | Group A subgroups | Limits | | Unit |
|---------------------------------------|------------------|---|--|---------|-------------------|------|
| | | | | Min | Max | |
| High level output voltage | V _{OH} | V _{CC} = 4.5 V V _{IL} = 0.8 V V _{IH} = 2.0 V <u>3/</u> | I _{OH} = -2.0 mA I _{OH} = -3.0 mA I _{OH} = -40.0 mA | 1,2,3 | 2.5 2.4 2.0 | V |
| Low level output voltage | V _{OL} | V _{CC} = 4.5 V V _{IL} = 0.8 V V _{IH} = 2.0 V <u>3/</u> | I _{OL} = 40 mA | 1,2,3 | 0.5 | V |
| Input clamp voltage | V _{IC} | V _{CC} = 4.5 V | I _{IN} = -18 mA | 1,2,3 | -1.2 | V |
| Low Level input current | I _{IL} | V _{CC} = 5.5 V V _{IL} = 0.4 V All other inputs = 4.5 V | | 1,2,3 | -0.5 | mA |
| High Level input current | I _{IH1} | V _{CC} = 5.5 V V _{IH} = 2.7 V | All other inputs = 0.0 V | 1,2,3 | 20 | μA |
| | I _{IH2} | V _{CC} = 5.5 V V _{IH} = 7.0 V | | 1,2,3 | 0.1 | mA |
| Output current | I _O | V _{CC} = 5.5 V V _{OUT} = 2.25 V <u>4/</u> | | 1,2,3 | -50 -200 | mA |
| Supply current | I _{CCH} | V _{CC} = 5.5 V V _{IN} = 0.0 V | | 1,2,3 | 10 | mA |
| | I _{CCL} | V _{CC} = 5.5 V V _{IN} = 4.5 V | | | 32 | |
| Functional tests | | V _{CC} = 4.5 V and 5.5 V; see 4.3.1c | | 7,8 | | |
| Propagation delay time A or B to Y | t _{PLH} | V _{CC} = 4.5 V and 5.5 V C _L = 50 pF ±10% R _L = 500Ω ±5% See figure 4 <u>5/</u> | | 9,10,11 | 1 4.8 | ns |
| | t _{PHL} | | | 9,10,11 | 1 4.8 | ns |

- 1/ Unused inputs that do not directly control the pin under test must be put at ≥ 2.5 V or ≤ 0.4 V.
- 2/ Unused inputs shall not exceed 5.5 V or go less than 0.0 V. No inputs shall be floated.
- 3/ All outputs must be tested. In the case where only one input at V_{IL} maximum or V_{IH} minimum produces the proper output state, the test must be performed with each input being selected as the V_{IL} maximum or V_{IH} minimum input.
- 4/ The output conditions have been chosen to produce a current that closely approximates one-half of the true short circuit output current, I_{OS}. Not more than one output should be tested at a time and the duration of the test should not exceed one second.
- 5/ Propagation delay limits are based on single output switching. Unused outputs = 3.5 V or ≤ 0.3 V.

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| Device type | 01 | |
|-----------------|-----------------|-----------------|
| Case outlines | R and S | 2 |
| Terminal number | Terminal symbol | |
| 1 | 1A | 1A |
| 2 | 1B | 1B |
| 3 | 1Y | 1Y |
| 4 | 2A | 2A |
| 5 | 2B | 2B |
| 6 | 2Y | 2Y |
| 7 | 3A | 3A |
| 8 | 3B | 3B |
| 9 | 3Y | 3Y |
| 10 | GND | GND |
| 11 | 4Y | 4Y |
| 12 | 4A | 4A |
| 13 | 4B | 4B |
| 14 | 5Y | 5Y |
| 15 | 5A | 5A |
| 16 | 5B | 5B |
| 17 | 6Y | 6Y |
| 18 | 6A | 6A |
| 19 | 6B | 6B |
| 20 | V _{CC} | V _{CC} |

FIGURE 1. Terminal connections.

| | | | |
|---|-------------------|-----------------------------|--------------------|
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| Inputs | | Output |
|--------|---|--------|
| A | B | Y |
| H | X | L |
| X | H | L |
| L | L | H |

H = High level voltage
L = Low level voltage
X = Irrelevant

FIGURE 2. Truth table.

| | | | |
|---|-------------------|-----------------------------|--------------------|
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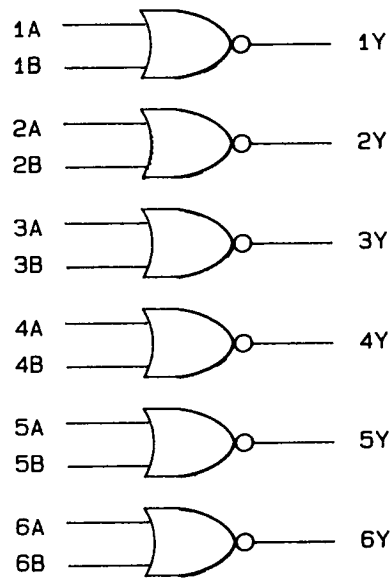
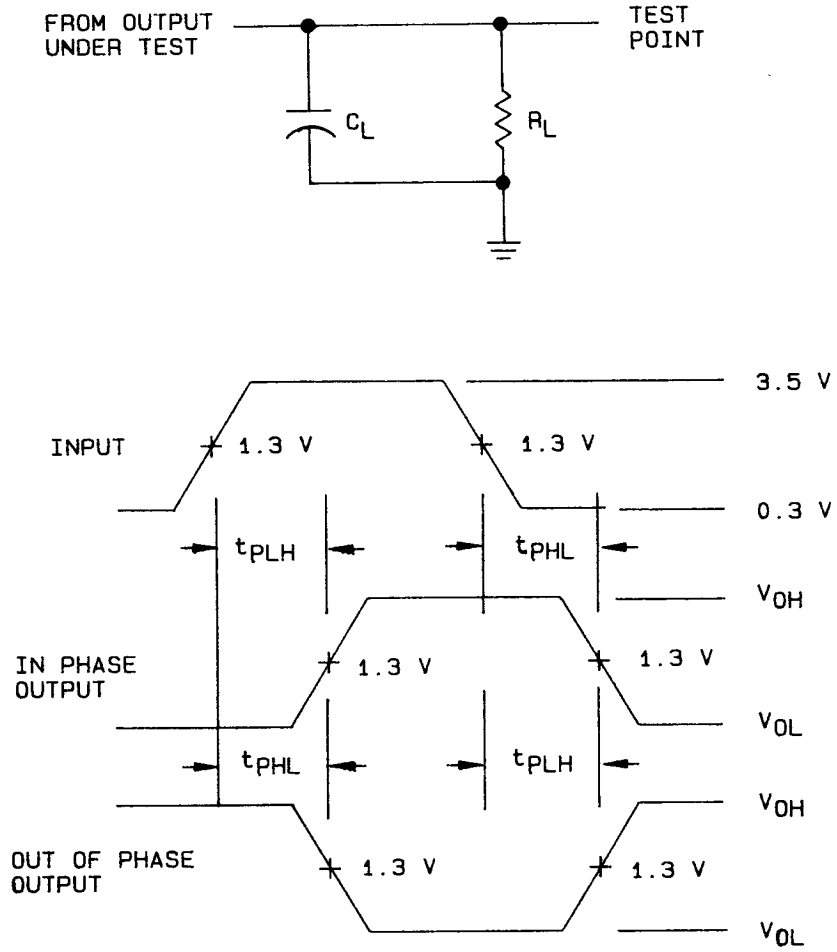


FIGURE 3. Logic diagram.

| | | | |
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NOTES:

1. C_L includes probe and jig capacitance.
2. All input pulses have the following characteristics: $PRR \leq 10$ MHz, $t_r = t_f = 3$ ns ± 1 ns, duty cycle = 50 percent.
3. The outputs are measured one at a time with one input transition per measurement.

FIGURE 4. Test circuit and waveforms.

| | | |
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3.7 Certificate of conformance. A certificate of conformance as required in MIL-STD-883 (see 3.1 herein) shall be provided with each lot of microcircuits delivered to this drawing.

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3.8 Notification of change. Notification of change to DESC-ECC shall be required in accordance with MIL-STD-883 (see 3.1 herein).

3.9 Verification and review. DESC, DESC's agent, and the acquiring activity retain the option to review the manufacturer's facility and applicable required documentation. Offshore documentation shall be made available onshore at the option of the reviewer.

4. QUALITY ASSURANCE PROVISIONS

4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with section 4 of MIL-M-38510 to the extent specified in MIL-STD-883 (see 3.1 herein).

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on all devices prior to quality conformance inspection. The following additional criteria shall apply:

a. Burn-in test, method 1015 of MIL-STD-883.

(1) Test condition A or D using the circuit submitted with the certificate of compliance (see 3.6 herein).

(2) $T_A = +125^\circ\text{C}$, minimum.

b. Interim and final electrical test parameters shall be as specified in table II herein, except interim electrical parameter tests prior to burn-in are optional at the discretion of the manufacturer.

4.3 Quality conformance inspection. Quality conformance inspection shall be in accordance with method 5005 of MIL-STD-883 including groups A, B, C, and D inspections. The following additional criteria shall apply.

4.3.1 Group A inspection.

a. Tests shall be as specified in table II herein.

b. Subgroups 4, 5, and 6 in table I, method 5005 of MIL-STD-883 shall be omitted.

c. Subgroup 7 shall include verification of the truth table.

4.3.2 Groups C and D inspections.

a. End-point electrical parameters shall be as specified in table II herein.

b. Steady-state life test conditions, method 1005 of MIL-STD-883.

(1) Test condition A or D using the circuit submitted with the certificate of compliance (see 3.6 herein).

(2) $T_A = +125^\circ\text{C}$, minimum.

(3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

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TABLE II. Electrical test requirements.

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| | |
|--|--|
| MIL-STD-883 test requirements | Subgroups (per method 5005, table I) |
| Interim electrical parameters (method 5004) | --- |
| Final electrical test parameters (method 5004) | 1*, 2, 3, 7, 8, 9, 10, 11 |
| Group A test requirements (method 5005) | 1, 2, 3, 7, 8, 9, 10, 11 |
| Groups C and D end-point electrical parameters (method 5005) | 1, 2, 3 |

* PDA applies to subgroup 1.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this drawing are intended for use when military specifications do not exist and qualified military devices that will perform the required function are not available for OEM application. When a military specification exists and the product covered by this drawing has been qualified for listing on QPL-38510, the device specified herein will be inactivated and will not be used for new design. The QPL-38510 product shall be the preferred item for all applications.

6.2 Replaceability. Microcircuits covered by this drawing will replace the same generic device covered by a contractor-prepared specification or drawing.

6.3 Configuration control of SMD's. All proposed changes to existing SMD's will be coordinated with the users of record for the individual documents. This coordination will be accomplished in accordance with MIL-STD-481 using DD Form 1693, Engineering Change Proposal (Short Form).

6.4 Record of users. Military and industrial users shall inform Defense Electronics Supply Center when a system application requires configuration control and the applicable SMD. DESC will maintain a record of users and this list will be used for coordination and distribution of changes to the drawings. Users of drawings covering microelectronics devices (FSC 5962) should contact DESC-ECC, telephone (513) 296-6022.

6.5 Comments. Comments on this drawing should be directed to DESC-ECC, Dayton, Ohio 45444, or telephone (513) 296-8525.

6.6 Approved sources of supply. Approved sources of supply are listed in MIL-BUL-103. The vendors listed in MIL-BUL-103 have agreed to this drawing and a certificate of compliance (see 3.6 herein) has been submitted to and accepted by DESC-ECC.

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