TOSHIBA Transistor Silicon NPN Epitaxial Type

2SC5810

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

- High DC current gain: $h_{FE} = 400 \text{ to } 1000 \text{ (IC} = 0.1 \text{ A)}$
- Low collector-emitter saturation voltage: VCE (sat) = 0.17 V (max)
- High-speed switching: tf = 85 ns (typ.)

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V _{CBO}	100	V	
Collector-emitter voltage		V _{CEX}	80	٧	
		V _{CEO}	50		
Emitter-base voltage		V _{EBO}	7	V	
Collector current	DC	Ic	1.0	Α	
	Pulse	I _{CP}	2.0		
Base current		Ι _Β	0.1	Α	
Collector power dissipation	DC	P _C (Note)	2.0	W	
	t = 10 s	FC (Note)	1.0		
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	−55 to 150	°C	

Note: Mounted on an FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm^2)

Unit: mm 1.6MAX. 4.6MAX 1.7MAX 0.4 ± 0.05 + 0.08 0.4 - 0.05 + 0.08 0.4 - 0.05 1.5 ± 0.1 1.5 ± 0.1 1. Base 2. Collector (heat sink) 3. Emitter **JEDEC** JEITA SC-62 **TOSHIBA** 2-5K1A

Weight: 0.05 g (typ.)

Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	V _{CB} = 100 V, I _E = 0	_	_	100	nA
Emitter cut-off current		I _{EBO}	V _{EB} = 7 V, I _C = 0	_	_	100	nA
Collector-emitter breakdown voltage		V (BR) CEO	I _C = 10 mA, I _B = 0	50	_	_	V
DC current gain		h _{FE} (1)	V _{CE} = 2 V, I _C = 0.1 A	400	_	1000	
		h _{FE} (2)	V _{CE} = 2 V, I _C = 0.3 A	200	_	_	
Collector-emitter saturation voltage		V _{CE (sat)}	$I_C = 300 \text{ mA}, I_B = 6 \text{ mA}$		_	0.17	٧
Base-emitter saturation voltage		V _{BE (sat)}	I _C = 300 mA, I _B = 6 mA	_	_	1.10	٧
Collector output capacitance		C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz	_	5	_	pF
Switching time	Rise time	t _r	See Figure 1. V _{CC} ≈ 30 V, R _L = 100 Ω	_	35	_	ns
	Storage time	t _{stg}		_	680	_	
	Fall time	t _f	$I_{B1} = -I_{B2} = 10 \text{ mA}$	_	85	_	

VCC 20 μs Input Inp

Marking

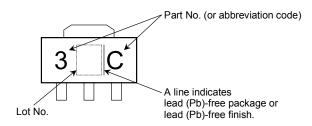
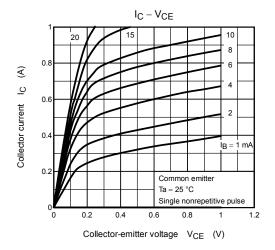
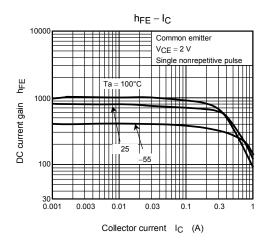
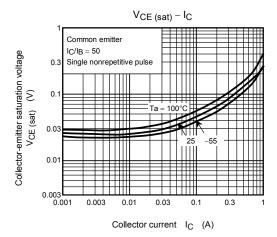
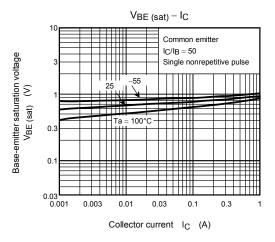


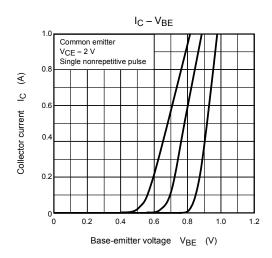
Figure 1 Switching Time Test Circuit & Timing Chart

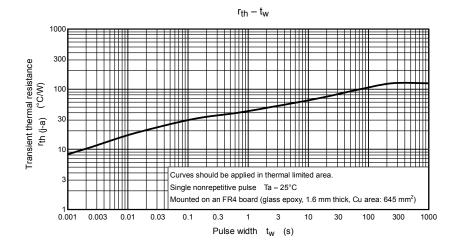


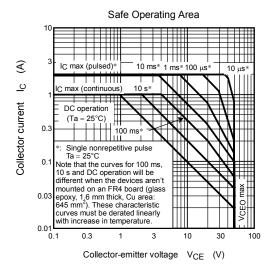












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