2SK3419

Silicon N Channel MOS FET
High Speed Power Switching

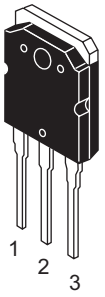
REJ03G1099-0200
(Previous: ADE-208-942)
Rev.2.00
Sep 07, 2005

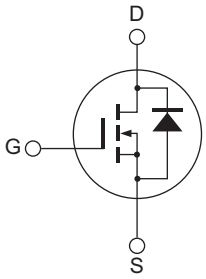
Features

- Low on-resistance
  $R_{DS\ (on)}$ = 4.3 mΩ typ.
- 4 V gate drive device
- High speed switching

Outline

RENESAS Package code: PRSS0004ZE-A
(Package name: TO-3P)





1. Gate
2. Drain (Flange)
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Drain to source voltage	V_{DSS}	60	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	90	A
Drain peak current	$I_{D\ (pulse)}$ Note 1	360	A
Body-drain diode reverse drain current	I_{DR}	90	A
Avalanche current	I_{AP} Note 3	65	A
Avalanche energy	E_{AR} Note 3	362	mJ
Channel dissipation	Pch Note 2	150	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 µs, duty cycle ≤ 1%
2. Value at Tc = 25°C
3. Value at Tch = 25°C, Rg ≥ 50 Ω

Electrical Characteristics

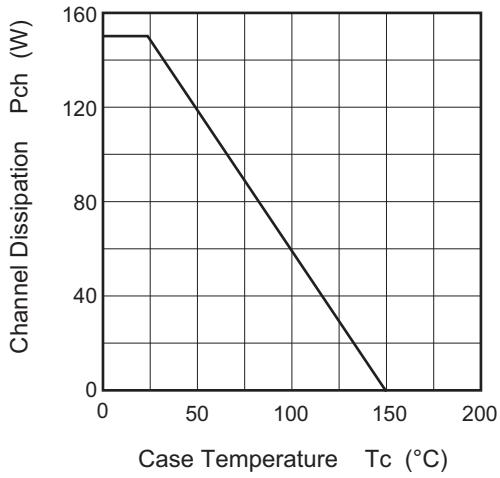
(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)\ DSS}$	60	—	—	V	I_D = 10 mA, V_{GS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	10	µA	V_{DS} = 60 V, V_{GS} = 0
Gate to source leak current	I_{GSS}	—	—	±0.1	µA	V_{GS} = ±20 V, V_{DS} = 0
Gate to source cutoff voltage	$V_{GS\ (off)}$	1.0	—	2.5	V	V_{DS} = 10 V, I_D = 1 mA Note 4
Forward transfer admittance	$\|y_{fs}\|$	55	90	—	S	I_D = 45 A, V_{DS} = 10 V Note 4
Static drain to source on state resistance	$R_{DS\ (on)}$	—	4.3	5.5	mΩ	I_D = 45 A, V_{GS} = 10 V Note 4
	$R_{DS\ (on)}$	—	6.0	9.0	mΩ	I_D = 45 A, V_{GS} = 4 V Note 4
Input capacitance	Ciss	—	9770	—	pF	V_{DS} = 10 V
Output capacitance	Coss	—	1340	—	pF	V_{GS} = 0
Reverse transfer capacitance	Crss	—	470	—	pF	f = 1 MHz
Total gate charge	Qg	—	180	—	nC	V_{DD} = 50 V
Gate to source charge	Qgs	—	32	—	nC	V_{GS} = 10 V
Gate to drain charge	Qgd	—	36	—	nC	I_D = 90 A
Turn-on delay time	$t_{d\ (on)}$	—	53	—	ns	V_{GS} = 10 V
Rise time	t_r	—	320	—	ns	I_D = 45 A
Turn-off delay time	$t_{d\ (off)}$	—	700	—	ns	R_L = 0.67 Ω
Fall time	t_f	—	380	—	ns	
Body-drain diode forward voltage	V_{DF}	—	1.0	—	V	I_F = 90 A, V_{GS} = 0
Body-drain diode reverse recovery time	t_{rr}	—	75	—	ns	I_F = 90 A, V_{GS} = 0 di_F/dt = 50 A/µs

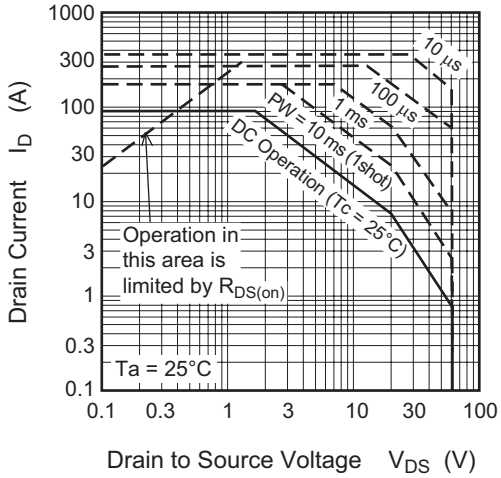
Note: 4. Pulse test

Main Characteristics

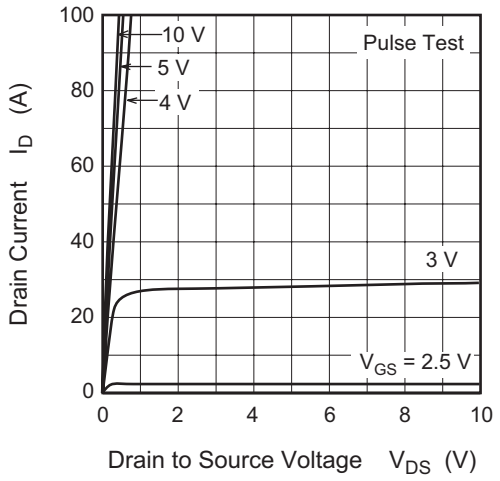
Power vs. Temperature Derating



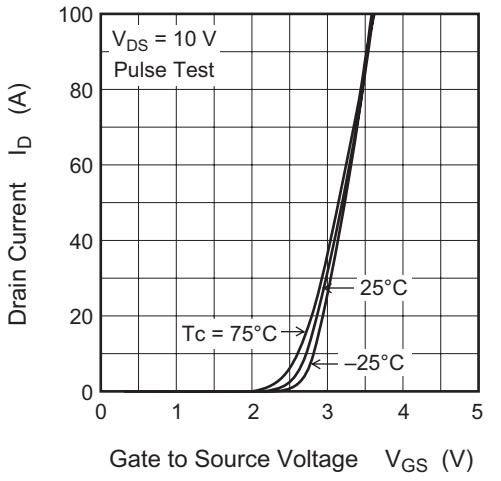
Maximum Safe Operation Area



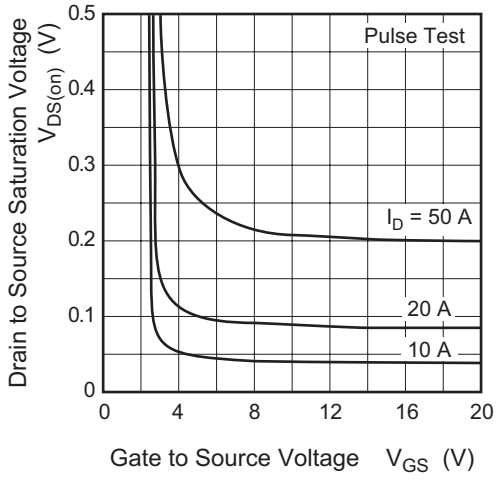
Typical Output Characteristics



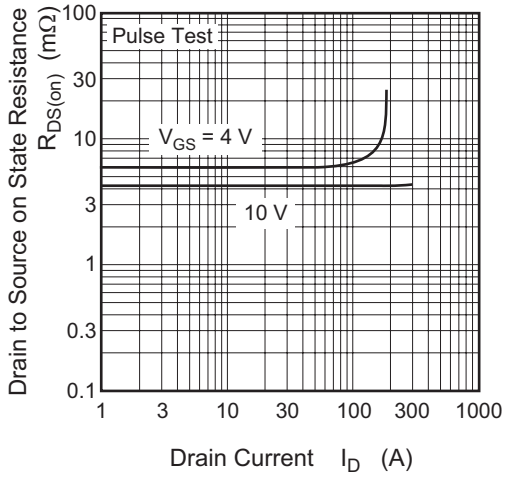
Typical Transfer Characteristics



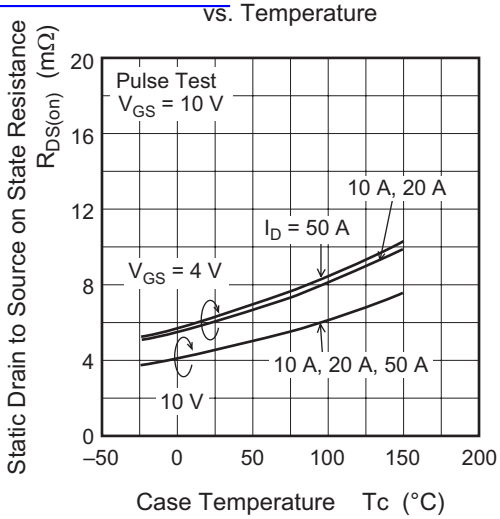
Drain to Source Saturation Voltage vs. Gate to Source Voltage



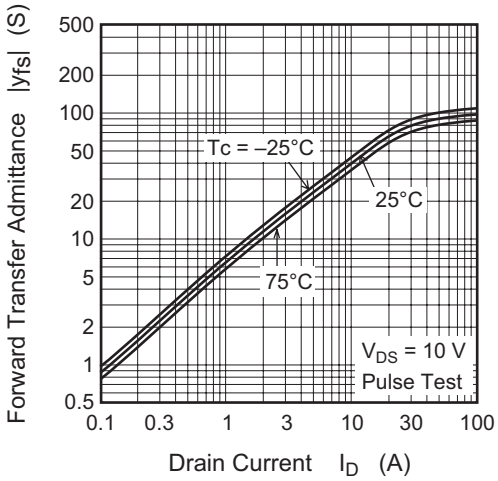
Static Drain to Source on State Resistance vs. Drain Current



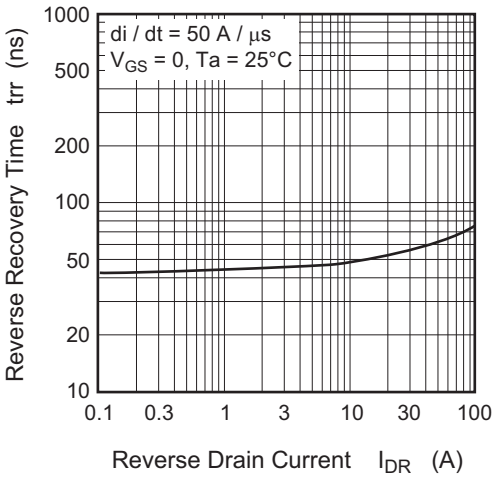
Static Drain to Source on State Resistance vs. Temperature



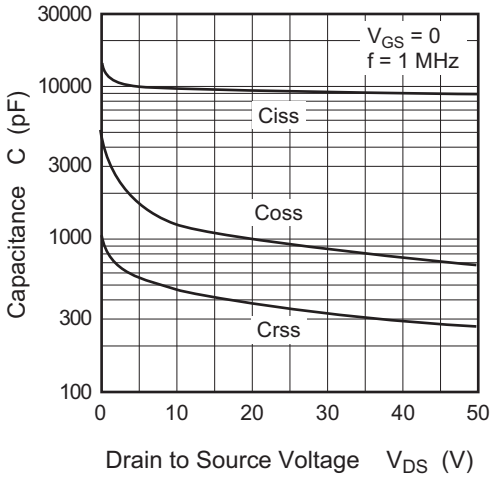
Forward Transfer Admittance vs. Drain Current



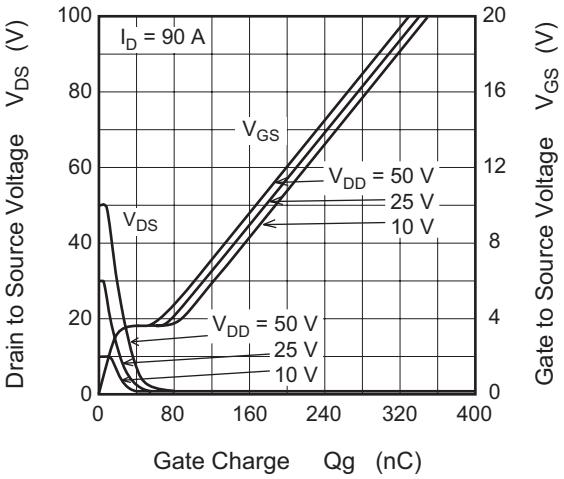
Body-Drain Diode Reverse Recovery Time



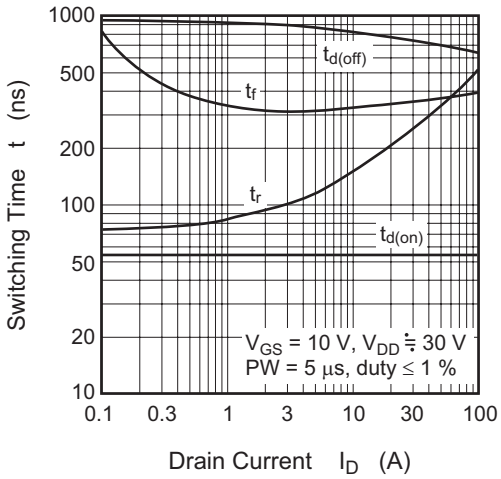
Typical Capacitance vs. Drain to Source Voltage



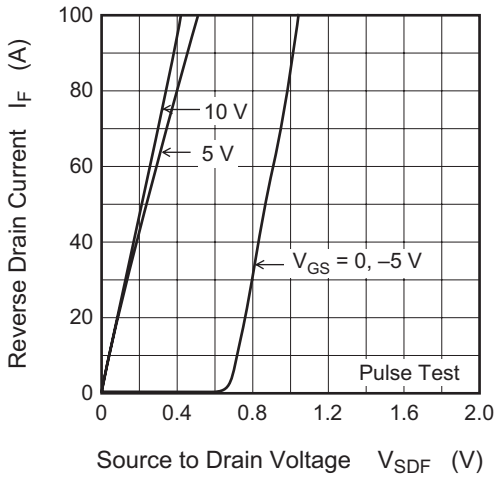
Dynamic Input Characteristics



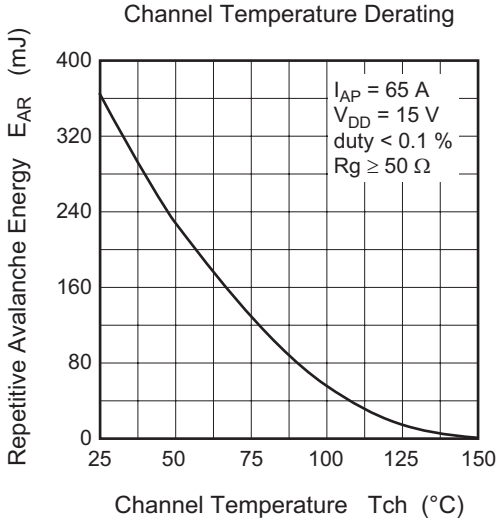
Switching Characteristics



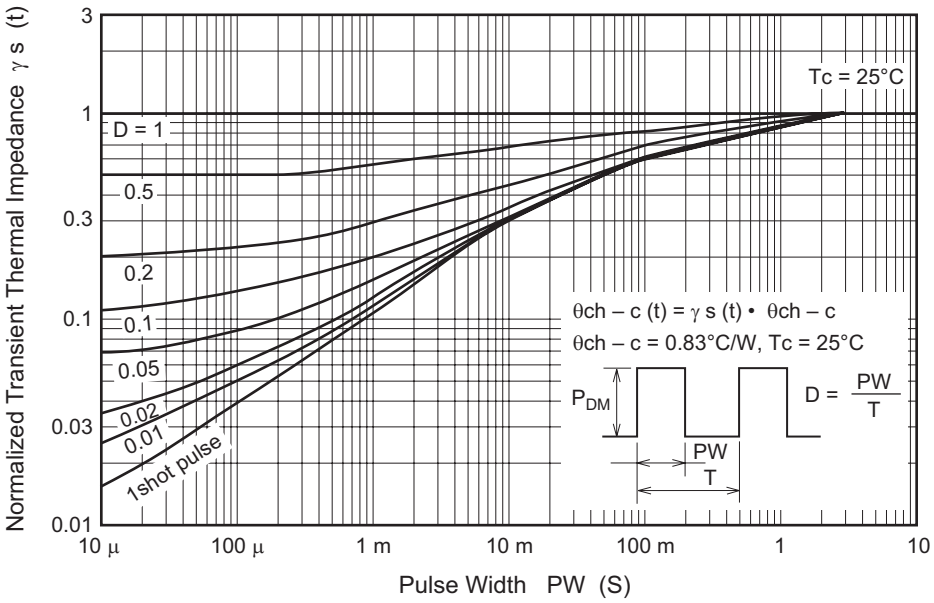
Reverse Drain Current vs.
Source to Drain Voltage



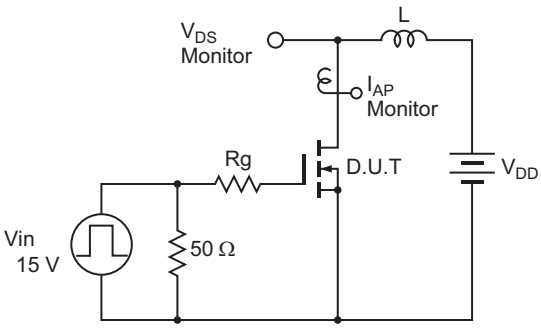
Maximum Avalanche Energy vs.
Channel Temperature Derating



Normalized Transient Thermal Impedance vs. Pulse Width

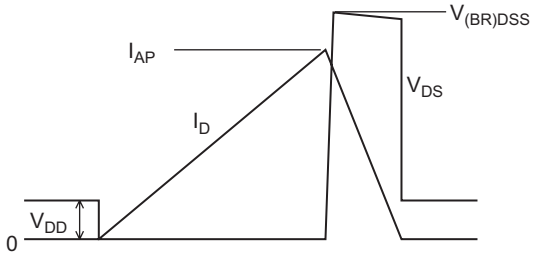


Avalanche Test Circuit

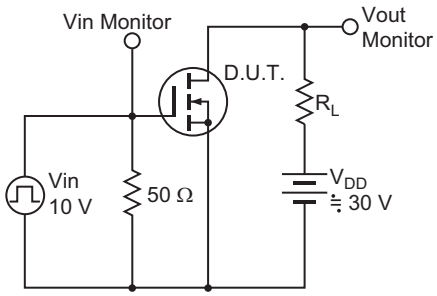


Avalanche Waveform

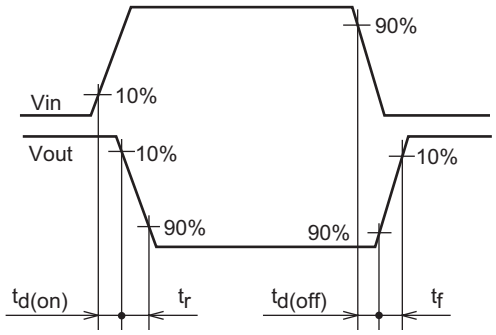
$$E_{AR} = \frac{1}{2} \cdot L \cdot I_{AP}^2 \cdot \frac{V_{DSS}}{V_{DSS} - V_{DD}}$$



Switching Time Test Circuit



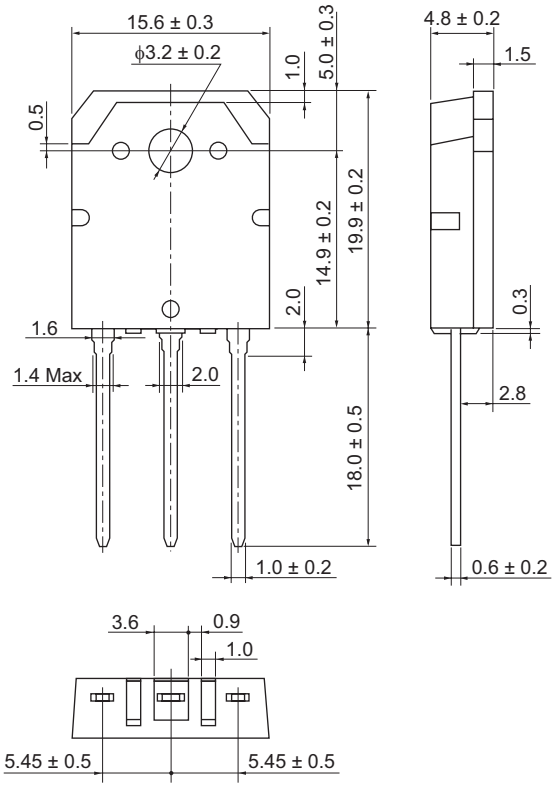
Switching Time Waveform



Package Dimensions

JEITA Package Code	RENESAS Code	Package Name	MASS[Typ.]
SC-65	PRSS0004ZE-A	TO-3P / TO-3PV	5.0g

Unit: mm



Ordering Information

Part Name	Quantity	Shipping Container
2SK3419-E	30 pcs	Plastic magazine

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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Renesas Technology America, Inc.
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.
Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> 2-796-3115, Fax: <82> 2-796-2145

Renesas Technology Malaysia Sdn. Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510

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