

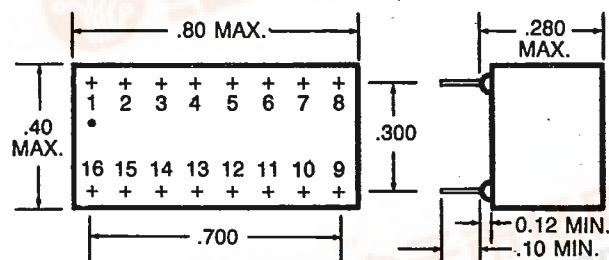
YCL
[查询"V01100"供应商](#)

ECL DIGITAL DELAY LINES

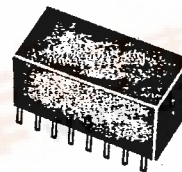
16 PIN PACKAGE

5 TAPS • SINGLE • DUAL • TRIPLE

SERIES (V05, V01, V02, V03)



White Dot locates Pin 1



ONLY ACTIVE PINS ARE SUPPLIED

Intermediate delay values available upon request.

SERIES V05 (5 TAP)		
MODEL NO. (Fig. 1)	Delay (ns)	Delay/ Tap (ns)
V05010	10	2 ± .5
V05015	15	3 ± .5
V05020	20	4 ± .5
V05025	25	5 ± 1.0
V05030	30	6 ± 1.0
V05040	40	8 ± 1.0
V05050	50	10 ± 1.0
V05060	60	12 ± 1.0
V05080	80	16 ± 1.5
V05100	100	20 ± 2.0

Delay/ line (ns)	MODEL NUMBERS		
	Series V01 One output (Fig. 2)	Series V02 Dual output (Fig. 3)	Series V03 Triple output (Fig. 4)
10	V01010	V02010	V03010
15	V01015	V02015	V03015
20	V01020	V02020	V03020
25	V01025	V02025	V03025
30	V01030	V02030	V03030
40	V01040	V02040	V03040
50	V01050	V02050	V03050
60	V01060	V02060	V03060
80	V01080	V02080	V03080
100	V01100	V02100	V03100

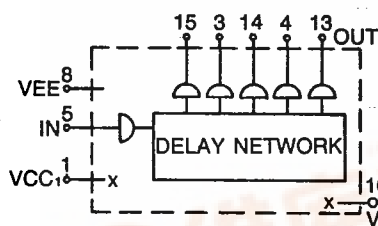


FIG. 1

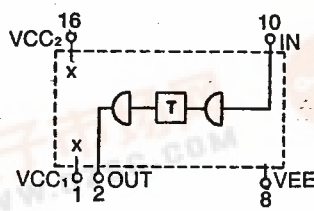


FIG. 2

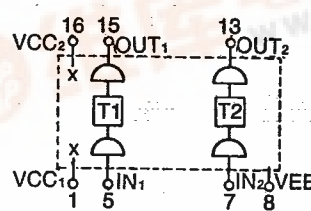


FIG. 3

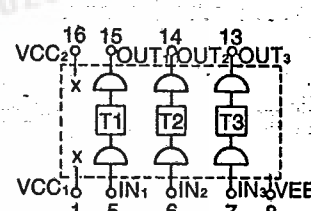


FIG. 4

SPECIFICATIONS:

- Cumulative delay tolerances: $\pm 1\text{ns}$ or $\pm 5\%$ wig
- Logic 1 (output): $- .96\text{V}$ min
- Logic 0 (output): $- 1.65\text{V}$ max
- Logic 1 (input): $- .98\text{V}$ min
- Logic 0 (input): $- 1.63\text{V}$ max
- Output rise time: 3ns max
- Supply voltage: $- 5.2\text{V} \pm 5\%$
- Supply current: V05 series: 65mA max
V01 series: 70mA max
V02 series: 100mA max
V03 series: 140mA max

- Minimum Pulse Width: 40% of Total Delay
- Maximum Duty Cycle: 50%
- Operating temperature: 0°C to $+70^\circ\text{C}$
- Delay temp. coefficient: $1.0\text{ns} + 500\text{ppm}/^\circ\text{C}$
- Terminals: Electro tin plated alloy 42
.020w x .010th

TEST CONDITIONS:

- Temp. 25°C $V_{ee} = 5.2\text{VDC}$ $V_{cc} = \text{Grd}$
- Input pulse amplitude: $- 1.0\text{V}$ ($- .75\text{V}$ H to $- 1.75\text{V}$ L)
- Pulse width: $3 \times \text{max } T_d$
- Pulse spacing: $10 \times \text{max } T_d$
- Time delay measured at $- 1.3\text{V}$ level
- Output connected through an external pulldown resistor of 100 ohms to 2.0VDC