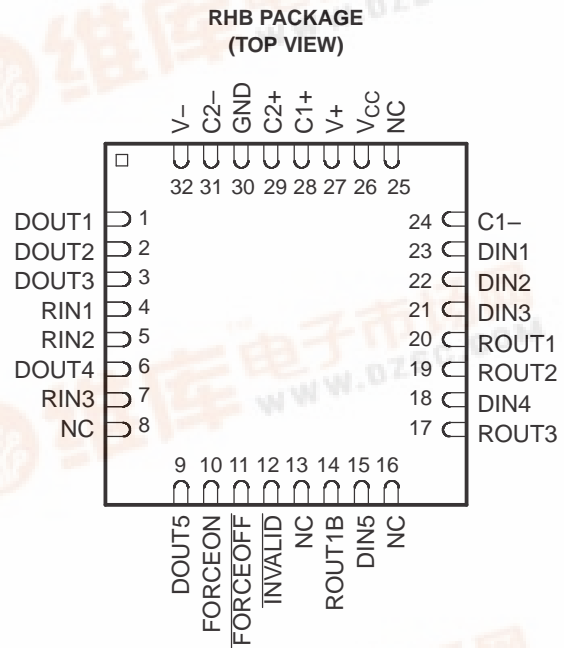
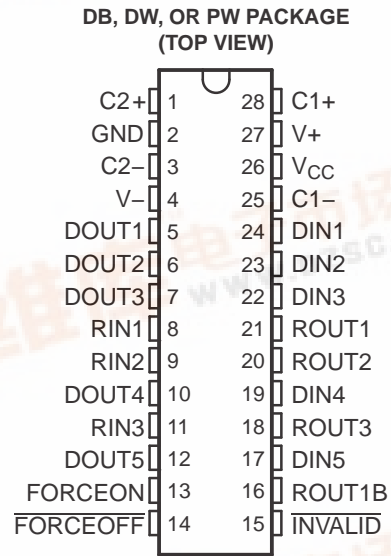


**FEATURES**

- RS-232 Bus-Pin ESD Protection Exceeds ±15 kV Using Human-Body Model (HBM)
- Meet or Exceed the Requirements of TIA/EIA-232-F and ITU v.28 Standards
- Operate With 3-V to 5.5-V V<sub>CC</sub> Supply
- Operate up to 1000 kbit/s
- Five Drivers and Three Receivers
- Auto-Powerdown Plus Feature Enables Flexible Power-Down Mode
- Low Standby Current . . . 1 μA Typical
- External Capacitors . . . 4 × 0.1 μF
- Accept 5-V Logic Input With 3.3-V Supply
- Always-Active Noninverting Receiver Output (ROUT1B)
- ESD Protection for RS-232 Interface Pins
  - ±15 kV – Human-Body Model (HBM)
  - ±8 kV – IEC61000-4-2, Contact Discharge
  - ±15 kV – IEC61000-4-2, Air-Gap Discharge

**APPLICATIONS**

- Battery-Powered Systems
- PDAs
- Notebooks
- Subnotebooks
- Laptops
- Palmtop PCs
- Hand-Held Equipment
- Modems
- Printers



**DESCRIPTION/ORDERING INFORMATION**

The SN65C3238E and SN75C3238E consist of five line drivers, three line receivers, and a dual charge-pump circuit with ±15-kV ESD (HBM) protection on the driver output (DOUT) and receiver input (RIN) terminals. The devices meet the requirements of TIA/EIA-232-F and provide the electrical interface between notebook and subnotebook computer applications. The charge pump and four small external capacitors allow operation from a single 3-V to 5.5-V supply. In addition, the devices include an always-active noninverting output (ROUT1B), which allows applications using the ring indicator to transmit data while the device is powered down. These devices operate at data signaling rates up to 1000 kbit/s.

**PRODUCT PREVIEW**



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**SN65C3238E, SN75C3238E**  
**3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVERS/RECEIVERS**  
**WITH  $\pm 15$ -kV ESD (HBM) PROTECTION**



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**DESCRIPTION/ORDERING INFORMATION (CONTINUED)**

Flexible control options for power management are featured when the serial port and driver inputs are inactive. The auto-powerdown plus feature functions when FORCEON is low and FORCEOFF is high. During this mode of operation, if the devices do not sense valid signal transitions on all receiver and driver inputs for approximately 30 s, the built-in charge pump and drivers are powered down, reducing the supply current to 1  $\mu$ A. By disconnecting the serial port or placing the peripheral drivers off, auto-powerdown plus occurs if there is no activity in the logic levels for the driver inputs. Auto-powerdown plus can be disabled when FORCEON and FORCEOFF are high. With auto-powerdown plus enabled, the devices activate automatically when a valid signal is applied to any receiver or driver input. INVALID is high (valid data) if any receiver input voltage is greater than 2.7 V or less than  $-2.7$  V, or has been between  $-0.3$  V and 0.3 V for less than 30  $\mu$ s. INVALID is low (invalid data) if all receiver input voltages are between  $-0.3$  V and 0.3 V for more than 30  $\mu$ s. Refer to Figure 5 for receiver input levels.

**ORDERING INFORMATION**

T <sub>A</sub>	PACKAGE <sup>(1)</sup>		ORDERABLE PART NUMBER	TOP-SIDE MARKING
0°C to 70°C	SSOP – DB	Tube of 50	SN75C3238EDB	75C3238E
		Reel of 2000	SN75C3238EDBR	
	TSSOP – PW	Tube of 50	SN75C3238EPW	Preview
		Reel of 2000	SN75C3238EPWR	
	SOIC – DW	Tube of 50	SN75C3238EDW	75C3238E
		Reel of 2000	SN75C3238EDWR	
	QFN – RHB	Reel of 2000	SN75C3238ECRHBR	Preview
	$-40^{\circ}\text{C}$ to $85^{\circ}\text{C}$	SSOP – DB	Tube of 50	SN65C3238EDB
Reel of 2000			SN65C3238EDBR	
TSSOP – PW		Tube of 50	SN65C3238EPW	Preview
		Reel of 2000	SN65C3238EPWR	
SOIC – DW		Tube of 50	SN65C3238EDW	65C3238E
		Reel of 2000	SN65C3238EDWR	
QFN – RHB		Reel of 2000	SN65C3238EIRHBR	Preview

(1) Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at [www.ti.com/sc/package](http://www.ti.com/sc/package).

PRODUCT PREVIEW

**FUNCTION TABLES**
**Each Driver<sup>(1)</sup>**

INPUTS				OUTPUT DOUT	DRIVER STATUS
DIN	FORCEON	FORCEOFF	TIME ELAPSED SINCE LAST RIN OR DIN TRANSITION		
X	X	L	X	Z	Powered off
L	H	H	X	H	Normal operation with auto-powerdown plus disabled
H	H	H	X	L	
L	L	H	<30 s	H	Normal operation with auto-powerdown plus enabled
H	L	H	<30 s	L	
L	L	H	>30 s	Z	Powered off by auto-powerdown plus feature
H	L	H	>30 s	Z	

(1) H = high level, L = low level, X = irrelevant, Z = high impedance

**Each Receiver<sup>(1)</sup>**

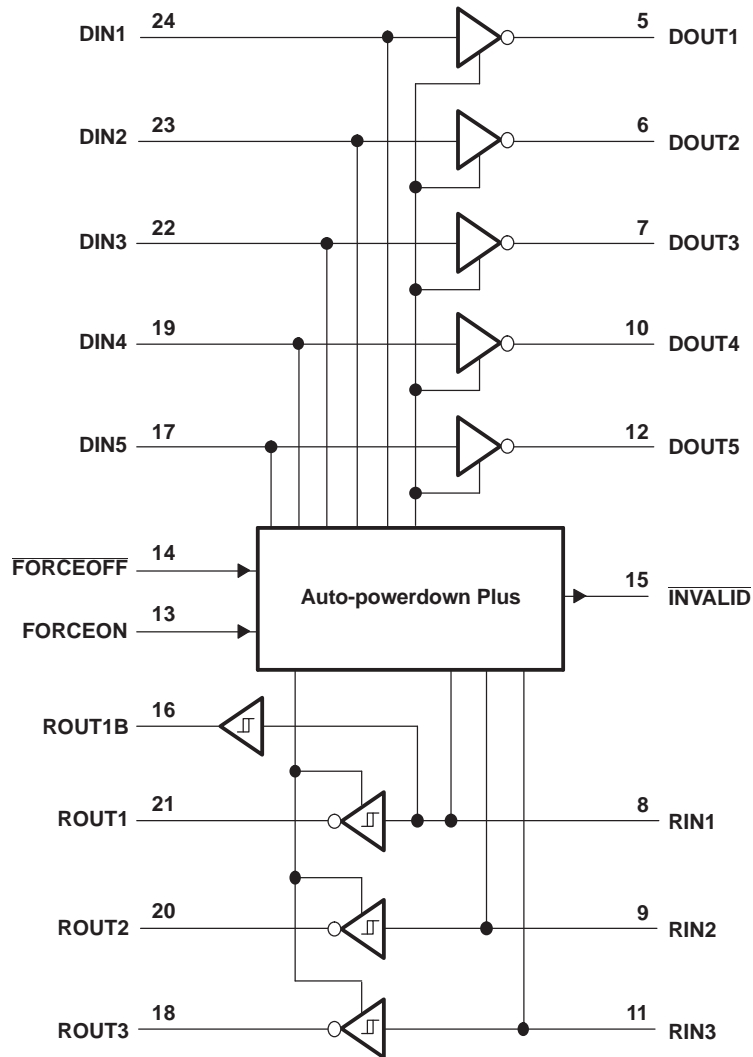
INPUTS				OUTPUTS		RECEIVER STATUS
RIN1	RIN2–RIN3	FORCEOFF	TIME ELAPSED SINCE LAST RIN OR DIN TRANSITION	ROUT1B	ROUT2 AND ROUT3	
L	X	L	X	L	Z	Powered off while ROUT1B is active
H	X	L	X	H	Z	
L	L	H	<30 s	L	H	Normal operation with auto-powerdown plus disabled/enabled
L	H	H	<30 s	L	L	
H	L	H	<30 s	H	H	
H	H	H	<30 s	H	L	
Open	Open	H	<30 s	L	H	

(1) H = high level, L = low level, X = irrelevant, Z = high impedance (off), Open = input disconnected or connected driver off

SN65C3238E, SN75C3238E  
 3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVERS/RECEIVERS  
 WITH  $\pm 15$ -kV ESD (HBM) PROTECTION

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LOGIC DIAGRAM (POSITIVE LOGIC)



PRODUCT PREVIEW

### Absolute Maximum Ratings<sup>(1)</sup>

over operating free-air temperature range (unless otherwise noted)

		MIN	MAX	UNIT	
V <sub>CC</sub>	Supply voltage range <sup>(2)</sup>	–0.3	6	V	
V+	Positive-output supply voltage range <sup>(2)</sup>	–0.3	7	V	
V–	Negative-output supply voltage range <sup>(2)</sup>	0.3	–7	V	
V+ – V–	Supply voltage difference <sup>(2)</sup>		13	V	
V <sub>I</sub>	Input voltage range	Driver (FORCEOFF, FORCEON)	–0.3	6	V
		Receiver	–25	25	
V <sub>O</sub>	Output voltage range	Driver	–13.2	13.2	V
		Receiver (INVALID)	–0.3	V <sub>CC</sub> + 0.3	
θ <sub>JA</sub>	Package thermal impedance <sup>(3)(4)</sup>	DB package		62	°C/W
		DW package		46	
		PW package		62	
		RHB package		TBD	
T <sub>J</sub>	Operating virtual junction temperature		150	°C	
T <sub>stg</sub>	Storage temperature range	–65	150	°C	

- Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
- All voltages are with respect to network GND.
- Maximum power dissipation is a function of T<sub>J</sub>(max), θ<sub>JA</sub>, and T<sub>A</sub>. The maximum allowable power dissipation at any allowable ambient temperature is P<sub>D</sub> = (T<sub>J</sub>(max) – T<sub>A</sub>)/θ<sub>JA</sub>. Operating at the absolute maximum T<sub>J</sub> of 150°C can affect reliability.
- The package thermal impedance is calculated in accordance with JESD 51-7.

### Recommended Operating Conditions<sup>(1)</sup>

See Figure 6

		MIN	NOM	MAX	UNIT
Supply voltage	V <sub>CC</sub> = 3.3 V	3	3.3	3.6	V
	V <sub>CC</sub> = 5 V	4.5	5	5.5	
V <sub>IH</sub> Driver and control high-level input voltage	DIN, FORCEOFF, FORCEON	V <sub>CC</sub> = 3.3 V	2	5.5	V
		V <sub>CC</sub> = 5 V	2.4	5.5	
V <sub>IL</sub> Driver and control low-level input voltage	DIN, FORCEOFF, FORCEON	0		0.8	V
V <sub>I</sub> Receiver input voltage		–25		25	V
T <sub>A</sub> Operating free-air temperature	SN75C3238E	0		70	°C
	SN65C3238E	–40		85	

- Testing supply conditions are C1–C4 = 0.1 μF at V<sub>CC</sub> = 3.3 V ± 0.15 V; C1–C4 = 0.22 μF at V<sub>CC</sub> = 3.3 V ± 0.3 V; and C1 = 0.047 μF and C2–C4 = 0.33 μF at V<sub>CC</sub> = 5 V ± 0.5 V.

### Electrical Characteristics<sup>(1)</sup>

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 6)

PARAMETER		TEST CONDITIONS	MIN	TYP <sup>(2)</sup>	MAX	UNIT
I <sub>I</sub>	Input leakage current	FORCEOFF, FORCEON		±0.01	±1	μA
I <sub>CC</sub>	Supply current (T <sub>A</sub> = 25°C)	Auto-powerdown plus disabled	No load, FORCEOFF and FORCEON at V <sub>CC</sub>	0.5	2	mA
		Powered off	No load, FORCEOFF at GND	1	10	
		Auto-powerdown plus enabled	No load, FORCEOFF at V <sub>CC</sub> , FORCEON at GND, All RIN are open or grounded	1	10	μA

- Testing supply conditions are C1–C4 = 0.1 μF at V<sub>CC</sub> = 3.3 V ± 0.15 V; C1–C4 = 0.22 μF at V<sub>CC</sub> = 3.3 V ± 0.3 V; and C1 = 0.047 μF and C2–C4 = 0.33 μF at V<sub>CC</sub> = 5 V ± 0.5 V.
- All typical values are at V<sub>CC</sub> = 3.3 V or V<sub>CC</sub> = 5 V, and T<sub>A</sub> = 25°C.

SN65C3238E, SN75C3238E  
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上海"SN65C3238E"供应商

## DRIVER SECTION

### Electrical Characteristics<sup>(1)</sup>

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 6)

PARAMETER	TEST CONDITIONS	MIN	TYP <sup>(2)</sup>	MAX	UNIT
V <sub>OH</sub>	High-level output voltage	All DOUT at R <sub>L</sub> = 3 k $\Omega$ to GND			V
V <sub>OL</sub>	Low-level output voltage	All DOUT at R <sub>L</sub> = 3 k $\Omega$ to GND			V
I <sub>IH</sub>	High-level input current	V <sub>I</sub> = V <sub>CC</sub>			$\mu$ A
I <sub>IL</sub>	Low-level input current	V <sub>I</sub> at GND			$\mu$ A
I <sub>OS</sub>	Short-circuit output current <sup>(3)</sup>	V <sub>CC</sub> = 3.6 V,	V <sub>O</sub> = 0 V		mA
		V <sub>CC</sub> = 5.5 V,	V <sub>O</sub> = 0 V		
r <sub>o</sub>	Output resistance	V <sub>CC</sub> , V <sub>+</sub> , and V <sub>-</sub> = 0 V, V <sub>O</sub> = $\pm 2$ V			$\Omega$
I <sub>OZ</sub>	Output leakage current	FORCEOFF = GND		V <sub>O</sub> = $\pm 12$ V, V <sub>CC</sub> = 3 V to 3.6 V	$\mu$ A
				V <sub>O</sub> = $\pm 10$ V, V <sub>CC</sub> = 4.5 V to 5.5 V	$\mu$ A

(1) Testing supply conditions are C1–C4 = 0.1  $\mu$ F at V<sub>CC</sub> = 3.3 V  $\pm$  0.15 V; C1–C4 = 0.22  $\mu$ F at V<sub>CC</sub> = 3.3 V  $\pm$  0.3 V; and C1 = 0.047  $\mu$ F and C2–C4 = 0.33  $\mu$ F at V<sub>CC</sub> = 5 V  $\pm$  0.5 V.

(2) All typical values are at V<sub>CC</sub> = 3.3 V or V<sub>CC</sub> = 5 V, and T<sub>A</sub> = 25°C.

(3) Short-circuit durations should be controlled to prevent exceeding the device absolute power dissipation ratings, and not more than one output should be shorted at a time.

### Switching Characteristics<sup>(1)</sup>

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 6)

PARAMETER	TEST CONDITIONS	MIN	TYP <sup>(2)</sup>	MAX	UNIT		
Maximum data rate (see Figure 1)	R <sub>L</sub> = 3 k $\Omega$ , One DOUT switching	C <sub>L</sub> = 1000 pF		250	kbit/s		
		C <sub>L</sub> = 250 pF,	V <sub>CC</sub> = 3 V to 4.5 V	1000			
		C <sub>L</sub> = 1000 pF,	V <sub>CC</sub> = 4.5 V to 5.5 V	1000			
t <sub>sk(p)</sub>	Pulse skew <sup>(3)</sup>	C <sub>L</sub> = 150 pF to 2500 pF,	R <sub>L</sub> = 3 k $\Omega$ to 7 k $\Omega$ ,	See Figure 2	25	ns	
SR(tr)	Slew rate, transition region (see Figure 1)	C <sub>L</sub> = 150 pF to 1000 pF,	R <sub>L</sub> = 3 k $\Omega$ to 7 k $\Omega$ ,	V <sub>CC</sub> = 3.3 V	18	150	V/ $\mu$ s

(1) Testing supply conditions are C1–C4 = 0.1  $\mu$ F at V<sub>CC</sub> = 3.3 V  $\pm$  0.15 V; C1–C4 = 0.22  $\mu$ F at V<sub>CC</sub> = 3.3 V  $\pm$  0.3 V; and C1 = 0.047  $\mu$ F and C2–C4 = 0.33  $\mu$ F at V<sub>CC</sub> = 5 V  $\pm$  0.5 V.

(2) All typical values are at V<sub>CC</sub> = 3.3 V or V<sub>CC</sub> = 5 V, and T<sub>A</sub> = 25°C.

(3) Pulse skew is defined as |t<sub>PLH</sub> – t<sub>PHL</sub>| of each channel of the same device.

### ESD Protection

PARAMETER	TEST CONDITIONS	TYP	UNIT
DOUT	HBM	$\pm 15$	kV
	IEC 61000-4-2, Air-Gap Discharge	$\pm 15$	
	IEC 61000-4-2, Contact Discharge	$\pm 8$	

## RECEIVER SECTION

### Electrical Characteristics<sup>(1)</sup>

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 6)

PARAMETER		TEST CONDITIONS	MIN	TYP <sup>(2)</sup>	MAX	UNIT
V <sub>OH</sub>	High-level output voltage	I <sub>OH</sub> = -1 mA	V <sub>CC</sub> - 0.6	V <sub>CC</sub> - 0.1		V
V <sub>OL</sub>	Low-level output voltage	I <sub>OL</sub> = 1.6 mA			0.4	V
V <sub>IT+</sub>	Positive-going input threshold voltage	V <sub>CC</sub> = 3.3 V		1.5	2.4	V
		V <sub>CC</sub> = 5 V		1.8	2.4	
V <sub>IT-</sub>	Negative-going input threshold voltage	V <sub>CC</sub> = 3.3 V	0.6	1.2		V
		V <sub>CC</sub> = 5 V	0.8	1.5		
V <sub>hys</sub>	Input hysteresis (V <sub>IT+</sub> - V <sub>IT-</sub> )			0.3		V
I <sub>OZ</sub>	Output leakage current (except ROUT1B)	FORCEOFF = 0 V		±0.05	±10	µA
r <sub>i</sub>	Input resistance	V <sub>i</sub> = ±3 V to ±25 V	3	5	7	kΩ

(1) Testing supply conditions are C1–C4 = 0.1 µF at V<sub>CC</sub> = 3.3 V ± 0.15 V; C1–C4 = 0.22 µF at V<sub>CC</sub> = 3.3 V ± 0.3 V; and C1 = 0.047 µF and C2–C4 = 0.33 µF at V<sub>CC</sub> = 5 V ± 0.5 V.

(2) All typical values are at V<sub>CC</sub> = 3.3 V or V<sub>CC</sub> = 5 V, and T<sub>A</sub> = 25°C.

### Switching Characteristics<sup>(1)</sup>

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted)

PARAMETER		TEST CONDITIONS	TYP <sup>(2)</sup>	UNIT
t <sub>PLH</sub>	Propagation delay time, low- to high-level output	C <sub>L</sub> = 150 pF, See Figure 3	150	ns
t <sub>PHL</sub>	Propagation delay time, high- to low-level output	C <sub>L</sub> = 150 pF, See Figure 3	150	ns
t <sub>en</sub>	Output enable time	C <sub>L</sub> = 150 pF, R <sub>L</sub> = 3 kΩ, See Figure 4	200	ns
t <sub>dis</sub>	Output disable time	C <sub>L</sub> = 150 pF, R <sub>L</sub> = 3 kΩ, See Figure 4	200	ns
t <sub>sk(p)</sub>	Pulse skew <sup>(3)</sup>	See Figure 3	50	ns

(1) Testing supply conditions are C1–C4 = 0.1 µF at V<sub>CC</sub> = 3.3 V ± 0.15 V; C1–C4 = 0.22 µF at V<sub>CC</sub> = 3.3 V ± 0.3 V; and C1 = 0.047 µF and C2–C4 = 0.33 µF at V<sub>CC</sub> = 5 V ± 0.5 V.

(2) All typical values are at V<sub>CC</sub> = 3.3 V or V<sub>CC</sub> = 5 V, and T<sub>A</sub> = 25°C.

(3) Pulse skew is defined as |t<sub>PLH</sub> - t<sub>PHL</sub>| of each channel of the same device.

### ESD Protection

PARAMETER	TEST CONDITIONS	TYP	UNIT
RIN	HBM	±15	kV
	IEC 61000-4-2, Air-Gap Discharge	±15	
	IEC 61000-4-2, Contact Discharge	±8	

**SN65C3238E, SN75C3238E**  
**3-V TO 5.5-V MULTICHANNEL RS-232 LINE DRIVERS/RECEIVERS**  
**WITH  $\pm 15$ -kV ESD (HBM) PROTECTION**

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**AUTO-POWERDOWN PLUS SECTION**

**Electrical Characteristics**

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 5)

PARAMETER		TEST CONDITIONS	MIN	MAX	UNIT
$V_{T+(valid)}$	Receiver input threshold for $\overline{INVALID}$ high-level output voltage	FORCEON = GND, FORCEOFF = $V_{CC}$		2.7	V
$V_{T-(valid)}$	Receiver input threshold for $\overline{INVALID}$ high-level output voltage	FORCEON = GND, FORCEOFF = $V_{CC}$	-2.7		V
$V_{T(invalid)}$	Receiver input threshold for $\overline{INVALID}$ low-level output voltage	FORCEON = GND, FORCEOFF = $V_{CC}$	-0.3	0.3	V
$V_{OH}$	$\overline{INVALID}$ high-level output voltage	$I_{OH} = -1$ mA, FORCEON = GND, FORCEOFF = $V_{CC}$	$V_{CC} - 0.6$		V
$V_{OL}$	$\overline{INVALID}$ low-level output voltage	$I_{OL} = 1.6$ mA, FORCEON = GND, FORCEOFF = $V_{CC}$		0.4	V

**Switching Characteristics**

over recommended ranges of supply voltage and operating free-air temperature (unless otherwise noted) (see Figure 5)

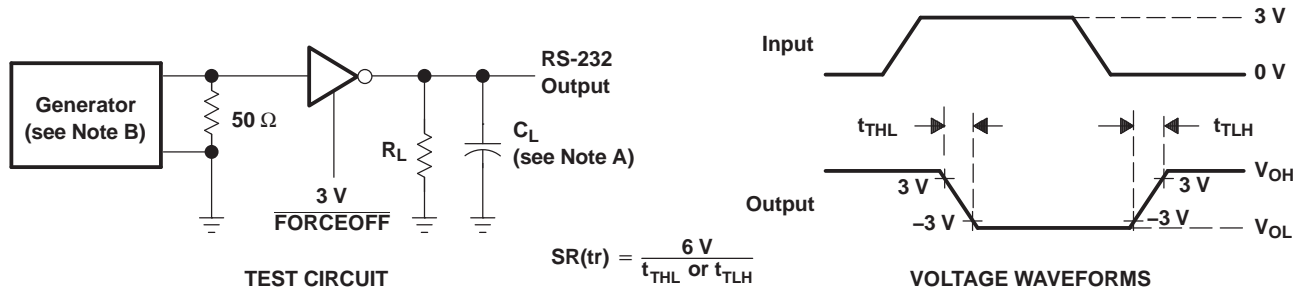
PARAMETER		MIN	TYP <sup>(1)</sup>	MAX	UNIT
$t_{valid}$	Propagation delay time, low- to high-level output		0.1		$\mu$ s
$t_{invalid}$	Propagation delay time, high- to low-level output		50		$\mu$ s
$t_{en}$	Supply enable time		25		$\mu$ s
$t_{dis}$	Receiver or driver edge to auto-powerdown plus	15	30	60	s

(1) All typical values are at  $V_{CC} = 3.3$  V or  $V_{CC} = 5$  V, and  $T_A = 25^\circ\text{C}$ .

PRODUCT PREVIEW

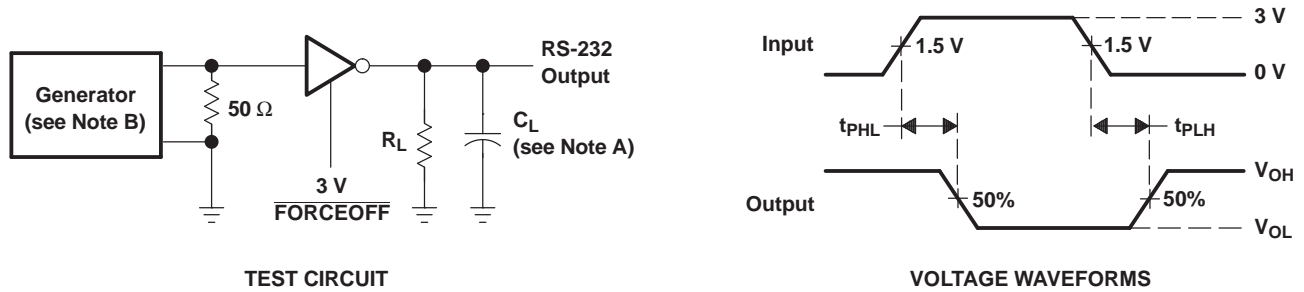


PARAMETER MEASUREMENT INFORMATION



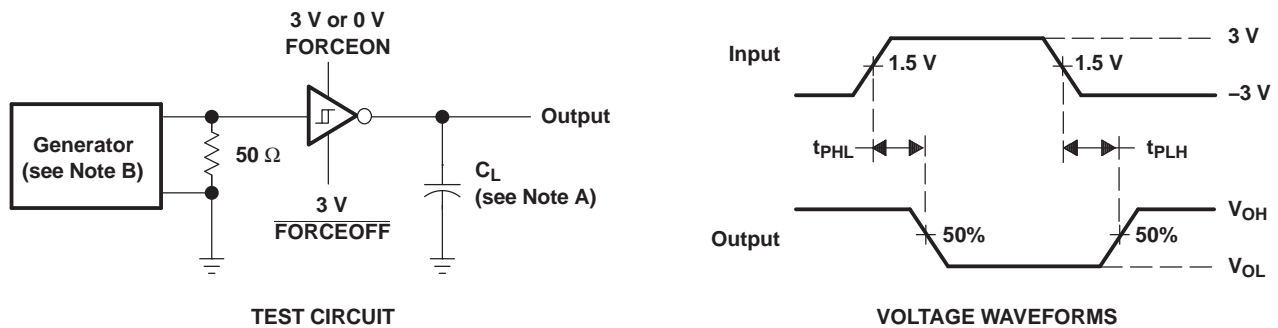
- A. C<sub>L</sub> includes probe and jig capacitance.
- B. The pulse generator has the following characteristics: PRR = 250 kbit/s, Z<sub>O</sub> = 50 Ω, 50% duty cycle, t<sub>r</sub> ≤ 10 ns, t<sub>f</sub> ≤ 10 ns.

Figure 1. Driver Slew Rate



- A. C<sub>L</sub> includes probe and jig capacitance.
- B. The pulse generator has the following characteristics: PRR = 250 kbit/s, Z<sub>O</sub> = 50 Ω, 50% duty cycle, t<sub>r</sub> ≤ 10 ns, t<sub>f</sub> ≤ 10 ns.

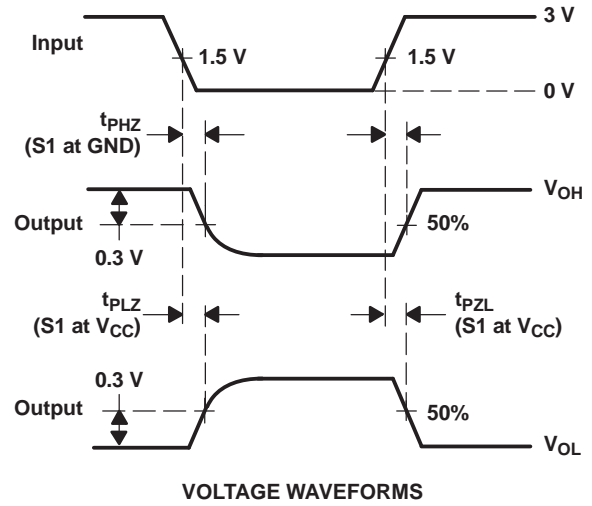
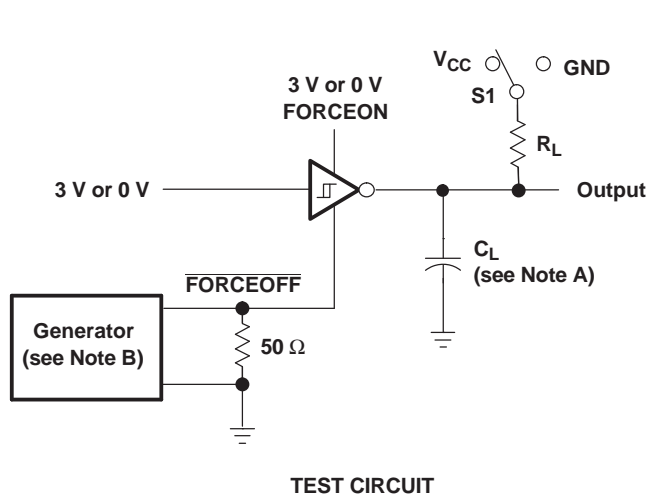
Figure 2. Driver Pulse Skew



- A. C<sub>L</sub> includes probe and jig capacitance.
- B. The pulse generator has the following characteristics: Z<sub>O</sub> = 50 Ω, 50% duty cycle, t<sub>r</sub> ≤ 10 ns, t<sub>f</sub> ≤ 10 ns.

Figure 3. Receiver Propagation Delay Times

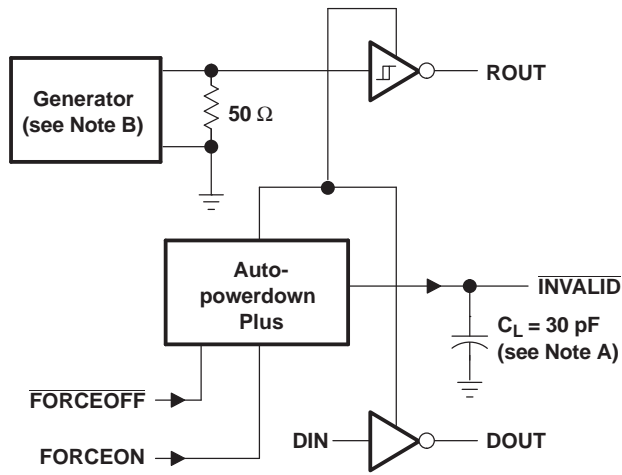
PARAMETER MEASUREMENT INFORMATION (continued)



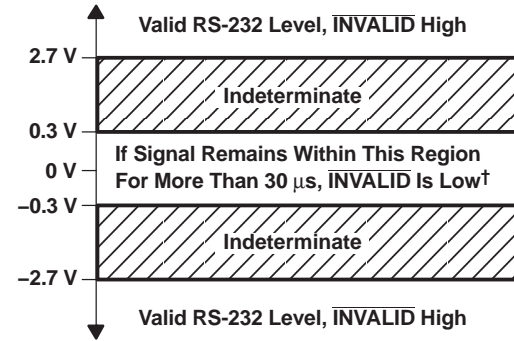
- A.  $C_L$  includes probe and jig capacitance.
- B. The pulse generator has the following characteristics:  $Z_O = 50 \Omega$ , 50% duty cycle,  $t_r \leq 10$  ns,  $t_f \leq 10$  ns.
- C.  $t_{PLZ}$  and  $t_{PHZ}$  are the same as  $t_{dis}$ .
- D.  $t_{PZL}$  and  $t_{PZH}$  are the same as  $t_{en}$ .

Figure 4. Receiver Enable and Disable Times

PARAMETER MEASUREMENT INFORMATION (continued)



TEST CIRCUIT



† Auto-powerdown plus disables drivers and reduces supply current to 1  $\mu$ A.

- NOTES: A.  $C_L$  includes probe and jig capacitance.  
B. The pulse generator has the following characteristics: PRR = 5 kbit/s,  $Z_O = 50 \Omega$ , 50% duty cycle,  $t_r \leq 10$  ns,  $t_f \leq 10$  ns.

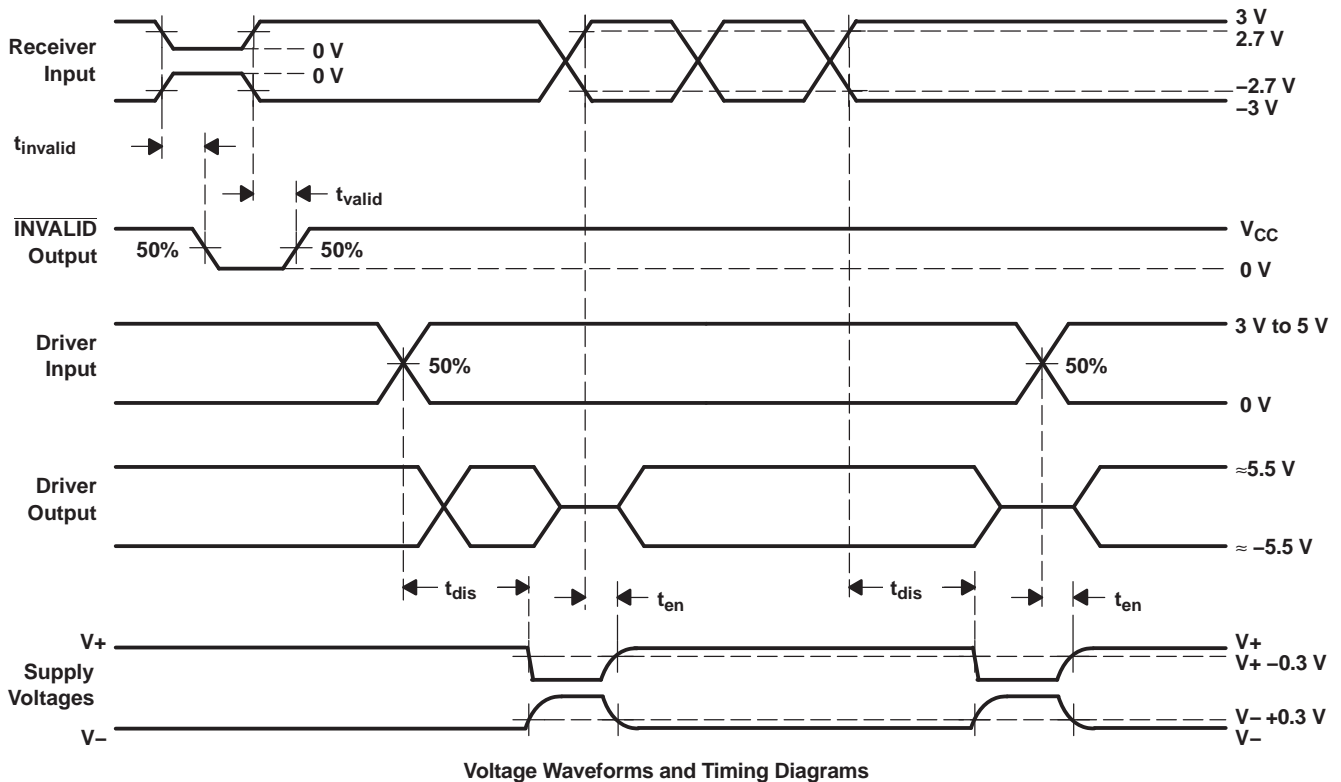
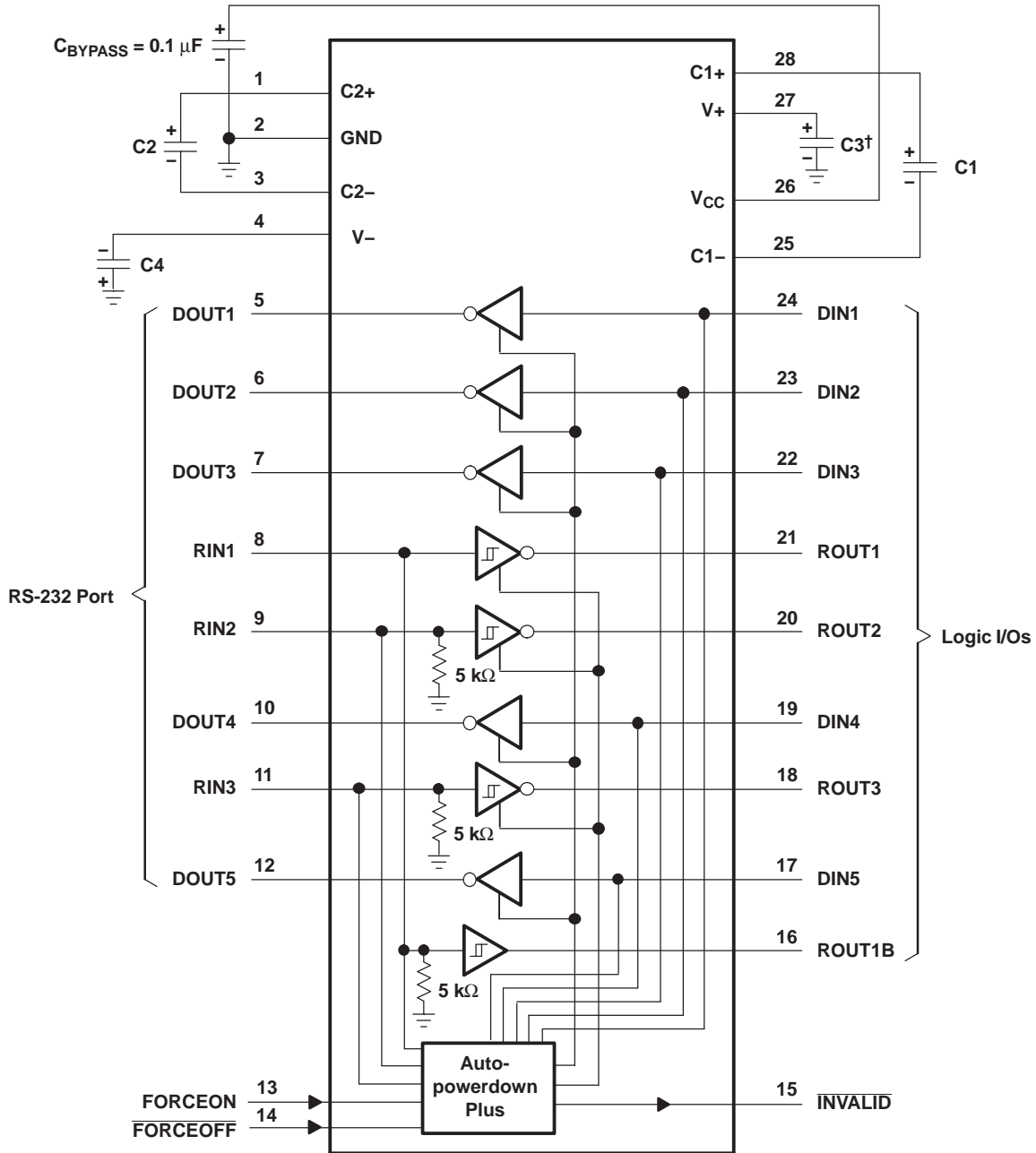


Figure 5.  $\overline{INVALID}$  Propagation-Delay Times and Supply-Enabling Time

PRODUCT PREVIEW

APPLICATION INFORMATION



V<sub>CC</sub> vs CAPACITOR VALUES

V <sub>CC</sub>	C1	C2, C3, and C4
3.3 V $\pm$ 0.15 V	0.1 $\mu$ F	0.1 $\mu$ F
3.3 V $\pm$ 0.3 V	0.22 $\mu$ F	0.22 $\mu$ F
5 V $\pm$ 0.5 V	0.047 $\mu$ F	0.33 $\mu$ F
3 V to 5.5 V	0.22 $\mu$ F	1 $\mu$ F

† C3 can be connected to V<sub>CC</sub> or GND.

NOTES: A. Resistor values shown are nominal.

B. Nonpolarized ceramic capacitors are acceptable. If polarized tantalum or electrolytic capacitors are used, they should be connected as shown.

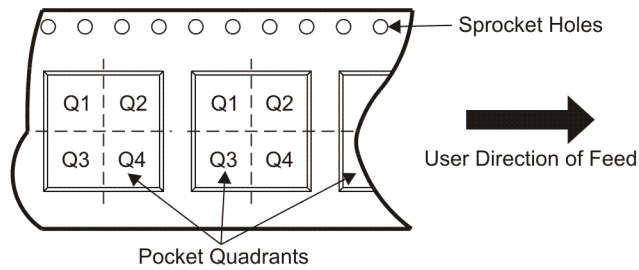
Figure 6. Typical Operating Circuit and Capacitor Values

PRODUCT PREVIEW

TAPE AND REEL INFORMATION



QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN65C3238EDBR	SSOP	DB	28	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1
SN65C3238EDWR	SOIC	DW	28	1000	330.0	32.4	11.35	18.67	3.1	16.0	32.0	Q1
SN75C3238EDBR	SSOP	DB	28	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1
SN75C3238EDWR	SOIC	DW	28	1000	330.0	32.4	11.35	18.67	3.1	16.0	32.0	Q1

**TAPE AND REEL BOX DIMENSIONS**



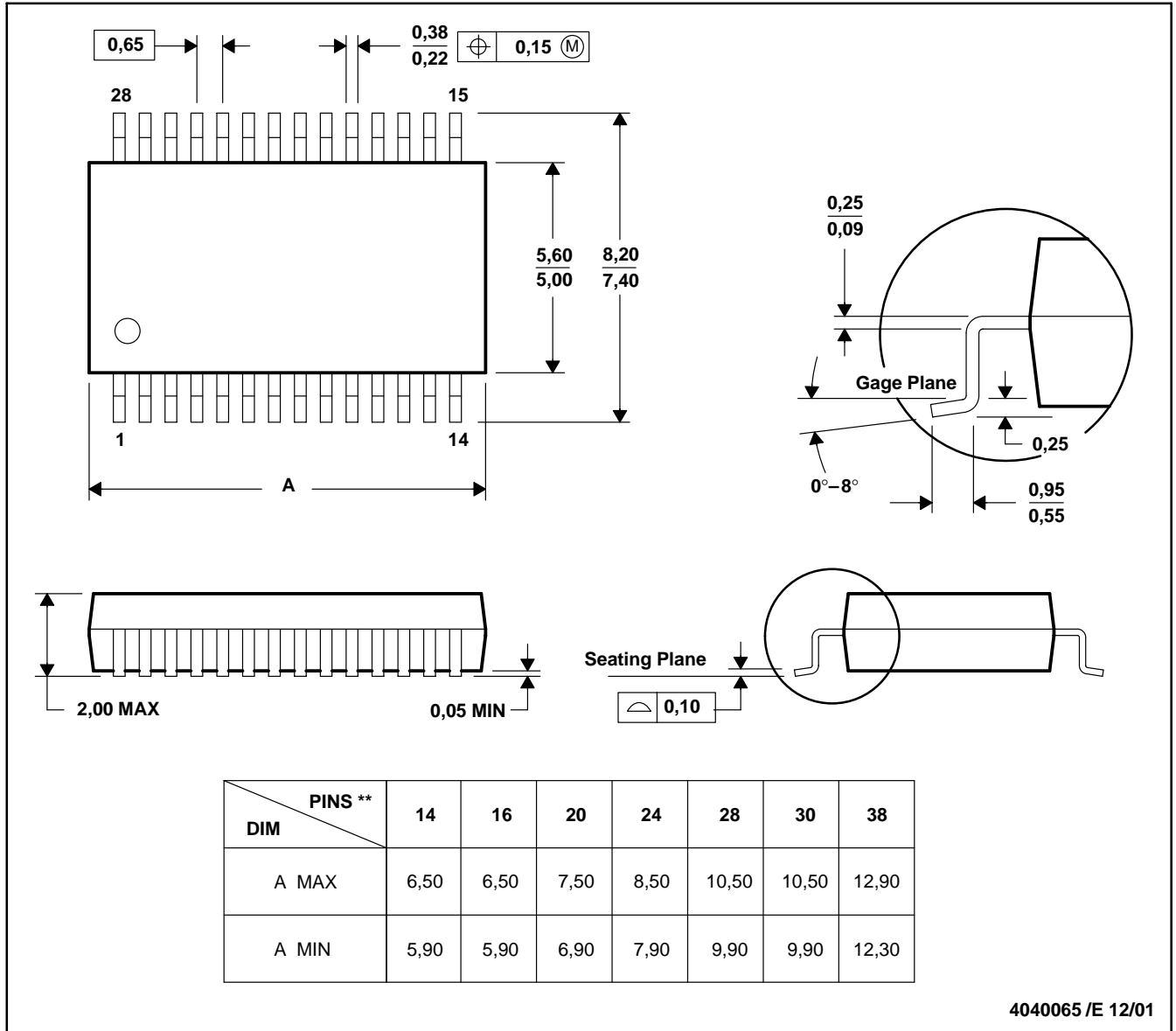
\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN65C3238EDBR	SSOP	DB	28	2000	346.0	346.0	33.0
SN65C3238EDWR	SOIC	DW	28	1000	346.0	346.0	49.0
SN75C3238EDBR	SSOP	DB	28	2000	346.0	346.0	33.0
SN75C3238EDWR	SOIC	DW	28	1000	346.0	346.0	49.0

DB (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE

28 PINS SHOWN

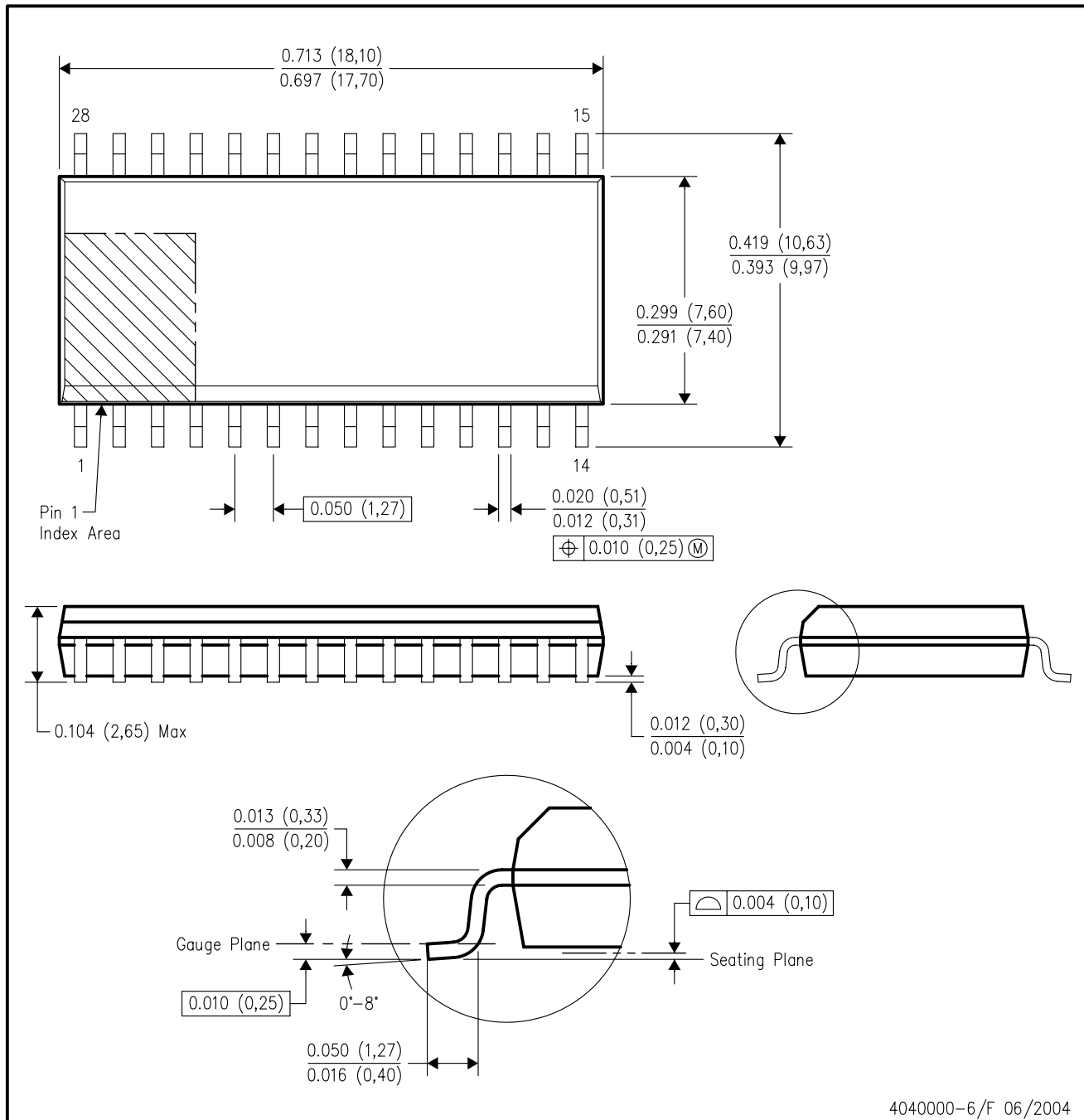


- NOTES: A. All linear dimensions are in millimeters.  
 B. This drawing is subject to change without notice.  
 C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.  
 D. Falls within JEDEC MO-150

查询"SN65C3238E"供应商

DW (R-PDSO-G28)

PLASTIC SMALL-OUTLINE PACKAGE



4040000-6/F 06/2004

- NOTES:
- A. All linear dimensions are in inches (millimeters).
  - B. This drawing is subject to change without notice.
  - C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
  - D. Falls within JEDEC MS-013 variation AE.



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