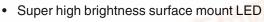
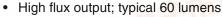
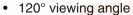
Vishay Semiconductors

Little Star® 1 Watt Power SMD LED Warm White

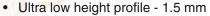
FEATURES







Compact package outline
 (L x W x H) of 6 x 6 x 1.5 mm



- Designed for high current drive; up to 350 mA
- Low thermal resistance; R_{th,IP} = 18 K/W
- Qualified according to JEDEC moisture sensitivity level 2a
- Compatible to IR reflow soldering
- Little Star[®] are class 1M LED products. Do not view directly with optical instrument
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- AEC-Q101 qualified
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B



The VLMW71.. is one of the most robust and light efficient LEDs in the market. With its extremely high level of brightness and the ultra low high profile, which is only 1.5 mm are highly suitable for both conventional lighting and specialized application such as automotive signal lights, traffic lights, channel lights, tube lights and garden lights among others.

PRODUCT GROUP AND PACKAGE DATA

Product group: LED

Package: SMD Little StarProduct series: power

Angle of half intensity: ± 60°

APPLICATIONS

- Automotive: exterior applications, e.g.: fog-lamp, rear mirror lighting, etc.
- Communication: FlashLED
- Industry: white goods (e.g.: oven, microwave, etc.)
- Lighting: garden light, architecture lighting, general lighting, etc.

PARTS TABLE		THE LIE AM	48.000
PART	LUMINOUS FLUX CORRELATION BETWEEN LUM. FLUX/LUM. INTENSITY (at I _F = 350 mA)	COLOR, LUMINOUS INTENSITY (at I _F = 350 mA)	TECHNOLOGY
VLMW71S2S3QN-GS08	Warm white, φ = (51 700 to 67 200) mlm	$I_{V \text{ typ.}} = 19\ 000\ \text{mcd}$	InGaN



Vi**含泡沙SelmicGASOOttloc**S08"供应商



ABSOLUTE MAXIMUM RAT	'INGS ¹⁾ VLMW71S2S3Q	N		
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Forward current		I _F	350	mA
Power dissipation		P _{tot}	1.4	W
Junction temperature		T _j	+ 120	°C
Surge current t < 10 µs, d = 0.1		I _{FM}	1000	mA
Operating temperature range		T _{amb}	- 40 to + 100	°C
Storage temperature range		T _{stg}	- 40 to + 100	°C
Thermal resistance junction/pin		R _{thJP}	18	K/W

Note:

Not designed for reverse operation

 $^{^{1)}}$ T_{amb} = 25 °C, unless otherwise specified

OPTICAL AND ELECTRICAL	AND ELECTRICAL CHARACTERISTICS 1) VLMW71S2S3QN, WARM WHITE					
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous flux/luminous intensity	1 - 250 mA	ф	51 700		67 200	mlm
Luminous mux/minious intensity	I _F = 350 mA	I _V		19 000		mcd
Chromaticity coordinate x acc. to CIE 1931	I _F = 350 mA	х		0.44		
Chromaticity coordinate y acc. to CIE 1931	I _F = 350 mA	у		0.41		
Angle of half intensity	I _F = 350 mA	φ		± 60		deg
Forward voltage ²⁾	I _F = 350 mA	V _F		3.6	4	V
Temperature coefficient of V _F	I _F = 350 mA	TC _{VF}		- 3		mV/K
Temperature coefficient of I _V	I _F = 350 mA	TC _{IV}		- 0.4		%/K

Note:

²⁾ Forward voltages are tested at a current pulse duration of 1 ms and a tolerance of \pm 0.05 V

LUMINOUS INTENSITY/FLUX CLASSIFICATION WARM WHITE			
GROUP	LUMINOUS FLUX ϕ_V (mlm) CORRELATION TABLE		
STANDARD	MIN.	MAX.	
S2	51 700	59 000	
S3	59 000	67 200	

Note:

Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of \pm 11 %.

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel).

In order to ensure availability, single brightness groups will not be orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped in any one reel. In order to ensure availability, single wavelength groups will not be orderable.

 $^{^{1)}}$ T_{amb} = 25 °C, unless otherwise specified

Vishay Semiconductors

QM	Bin	Сх	Су
OM			
OM			
0.415	QM		
0.421			
QN			
QN			
ON			
PN O.415 O.409 O.409 O.400 O.423 O.400 O.416 O.337 O.402 O.382 O.409 O.409 O.402 O.382 O.416 O.397 O.397 O.397 O.397 O.437 O.437 O.437 O.437 O.437 O.437 O.437 O.430 O.4414 O.426 O.430 O.421 O.430 O.421 O.4444 O.426 O.430 O.423 O.430 O.421 O.430 O.423 O.430 O.421 O.430 O.423 O.430 O.421 O.430 O.423 O.430 O.421 O.430 O.423 O.405 O.409 O.423 O.405 O.409 O.423 O.406 O.409 O.423 O.406 O.409 O.424 O.4444 O.426 O.446 O.426 O.446 O.436 O.409 O.427 O.446 O.428 O.392 O.446 O.446 O.428 O.392 O.4416 O.387 O.428 O.4428 O.392 O.4444 O.428 O.392 O.4444 O.428 O.392 O.446 O.446 O.446 O.446 O.446 O.446 O.446 O.444 O.426 O.446 O.446 O.446 O.446 O.444 O.426 O.4444 O.426 O.4443 O.436 O.409 O.436 O.409 O.436 O.409 O.4433 O.397 NO O.428 O.392 O.428 O.392 O.428 O.392 O.428 O.392 O.428 O.392 O.428 O.392 O.428 O.393 O.428 O.392 O.421 O.377	QN		
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QO			
0.402			
PP	QO		
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0.421 0.377			
	NP		
0.428 0.392			
		0.428	0.392

Note:

Chromaticity coordinate groups are tested at a current pulse duration of 25 ms and a tolerance of \pm 0.01.

Vi**含haly//Sleht/i7c/6Act/Qctle/rs**08"供应商

VISHAY.

TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

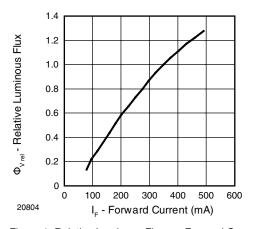


Figure 1. Relative Luminous Flux vs. Forward Current

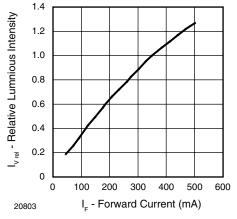


Figure 2. Relative Luminous Intensity vs. Forward Current

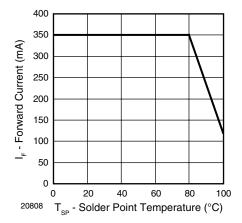


Figure 3. Forward Current vs. Solder Point Temperature

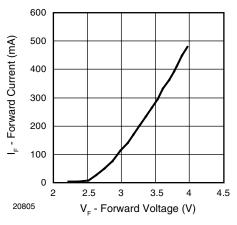


Figure 4. Forward Current vs. Forward Voltage

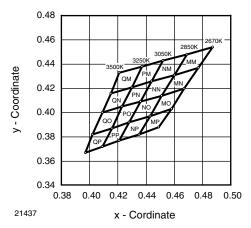


Figure 5. Coordinates of Color Groups

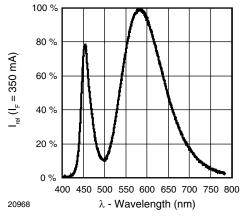
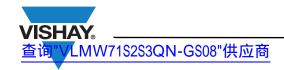


Figure 6. Relative Spectrale Emission



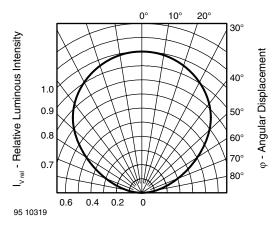
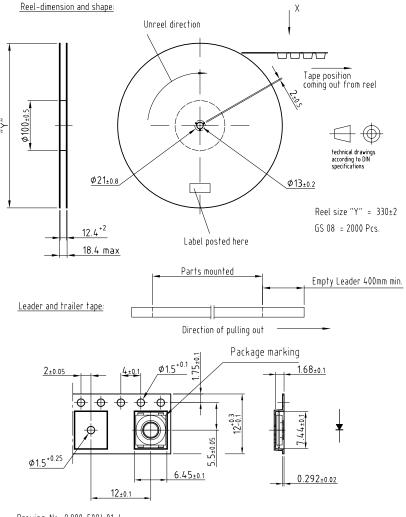


Figure 7. Relative Luminous Intensity vs. Angular Displacement

TAPING DIMENSIONS in millimeters

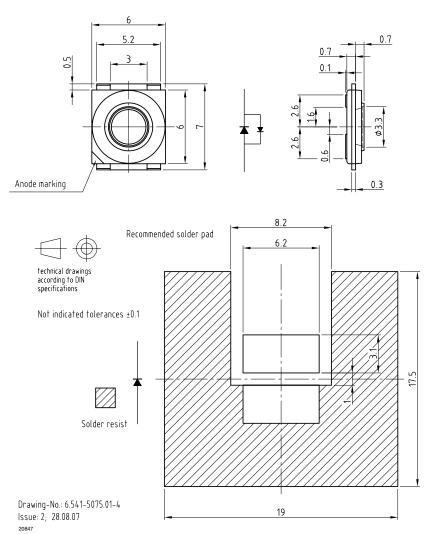


Drawing-No.: 9.800-5094.01-4 Issue: 3; 22.01.08

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VISHAY.

PACKAGE DIMENSIONS/SOLDERING PADS DIMENSIONS in millimeters



SOLDERING PROFILE



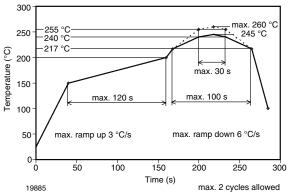
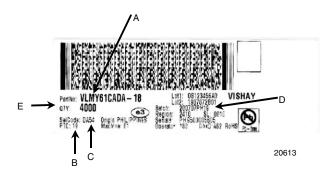


Figure 8. Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

Vishay Semiconductors

BAR CODE PRODUCT LABEL EXAMPLE:



A) Type of component

B) Manufacturing plant

C) SEL - selection code (bin):

e.g.: DA = code for luminous intensity group

5 = code for color group

4 = code for forward voltage

D) Batch:

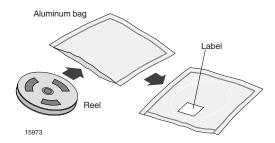
200707 = year 2007, week 07

PH19 = plant code

E) Total quantity

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

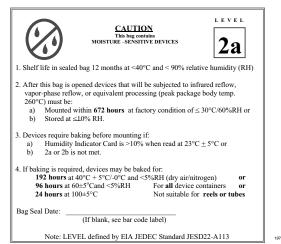
In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at $40 \,^{\circ}\text{C} + 5 \,^{\circ}\text{C/-} \, 0 \,^{\circ}\text{C}$ and $< 5 \,^{\circ}\text{KH}$ (dry air/nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



Vishay

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