

June 2009

FDB120N10

N-Channel PowerTrench $^{\circledR}$ MOSFET 100V, 74A, 12m Ω

Features

- $R_{DS(on)} = 9.7 \text{m}\Omega$ (Typ.)@ $V_{GS} = 10 \text{V}$, $I_D = 74 \text{A}$
- · Fast Switching Speed
- · Low Gate Charge
- High Performance Trench Technology for Extremely Low $R_{DS(\text{on})}$
- High Power and Current Handling Capability
- RoHS Compliant



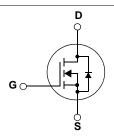
Description

This N-Channel MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench process that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance.

Application

• DC to DC Converters / Synchronous Rectification





$\textbf{MOSFET Maximum Ratings} \ \, \textbf{T}_{C} = 25^{o} \text{C unless otherwise noted}$

Symbol	Parameter			Ratings	Units
V _{DSS}	Drain to Source Voltage			100	V
V _{GSS}	Gate to Source Voltage			±20	V
	- Continuous ($T_C = 25^{\circ}C$)			74	^
^I D	Drain Current	- Continuous (T _C = 100°C)		52	A
I _{DM}	Drain Current	- Pulsed	(Note 1)	296	Α
E _{AS}	Single Pulsed Avalanche	Energy	(Note 2)	198	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		(Note 3)	5.8	V/ns
D	Davier Dissipation	$(T_C = 25^{\circ}C)$		170	W
P_{D}	Power Dissipation - Derate above 25°C			1.14	W/°C
T _J , T _{STG}	Operating and Storage Te	mperature Range		-55 to +175	°C
T _L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds			300	°C

Thermal Characteristics

Symbol	Parameter Ratings		Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.88	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	62.5	

Units

Max.

Package Marking and Ordering Information

De	vice Marking	Device	Package	Reel Size	Tape Width	Quantity
F	DB120N10	FDB120N10	D2-PAK	330mm	24mm	800

Test Conditions

Min.

Тур.

Electrical Characteristics T_C = 25°C unless otherwise noted Parameter

Off Chara	acteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A$, $V_{GS} = 0V$, $T_C = 25^{\circ}C$	100	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 250μA, Referenced to 25°C	-	0.1	-	V/°C
ı	Zero Gate Voltage Drain Current	V _{DS} = 100V, V _{GS} = 0V	-	-	1	μА
DSS	Zero Gate Voltage Drain Gurrent	$V_{DS} = 100V, V_{GS} = 0V, T_{C} = 150^{\circ}C$	-	-	500	μΑ
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	±100	nA

On Characteristics

Symbol

$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250\mu A$	2.5	-	4.5	V
R _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 10V, I_D = 74A$	-	9.7	12	mΩ
9 _{FS}	Forward Transconductance	$V_{DS} = 10V, I_D = 74A$ (Not	e 4) -	105	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V _{DS} = 25V, V _{GS} = 0V		4215	5605	pF
C _{oss}	Output Capacitance			405	540	pF
C _{rss}	Reverse Transfer Capacitance	1 - 11/11/2	-	170	255	pF
Q _{g(tot)}	Total Gate Charge at 10V		-	66	86	nC
Q _{gs}	Gate to Source Gate Charge	$V_{DS} = 80V I_{D} = 74A$	-	26	-	nC
Q _{gd}	Gate to Drain "Miller" Charge	V _{GS} = 10V (Note 4, 5)	-	20	-	nC

Switching Characteristics

t _{d(on)}	Turn-On Delay Time		-	27	64	ns
t _r	Turn-On Rise Time	$V_{DD} = 50V, I_{D} = 74A$	-	105	220	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = 10V, R_{GEN} = 4.7\Omega$	-	39	88	ns
t _f	Turn-Off Fall Time	(Note 4, 5)	-	15	40	ns

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diode Forward Current			-	-	74	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current			-	-	296	Α
V_{SD}	Drain to Source Diode Forward Voltage V _{GS} = 0V, I _{SD} = 74A		-	-	1.3	V	
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _{SD} = 74A		-	44	-	ns
Q _{rr}	Reverse Recovery Charge $dI_F/dt = 100A/\mu s$ (Note 4)		-	67	-	nC	

- Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 0.11mH, I $_{AS}$ = 60A, V $_{DD}$ = 50V, R $_{G}$ = 25 $\!\Omega$, Starting T $_{J}$ = 25 $^{\circ}C$
- 3. $I_{SD} \le 74 A$, di/dt $\le 200 A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$
- 4. Pulse Test: Pulse width $\leq 300 \mu s, \ Duty \ Cycle \leq 2\%$
- 5. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

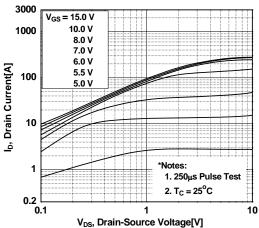


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

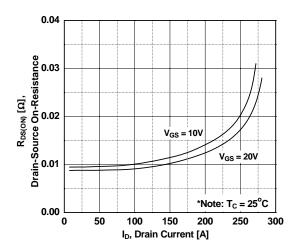


Figure 5. Capacitance Characteristics

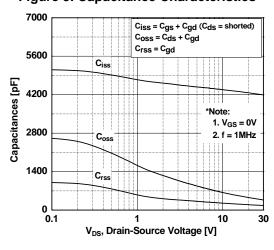


Figure 2. Transfer Characteristics

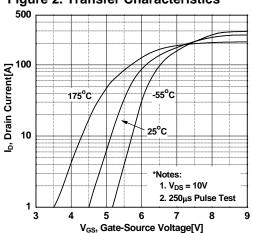


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

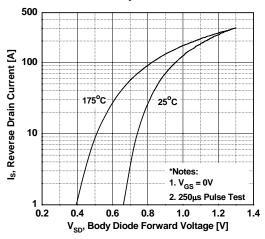
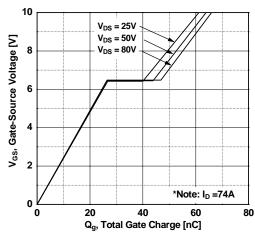


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

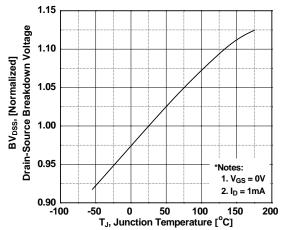


Figure 9. Maximum Safe Operating Area

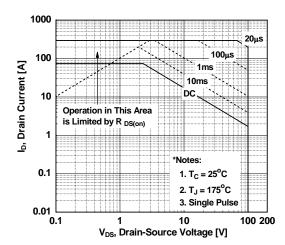


Figure 8. On-Resistance Variation vs. Temperature

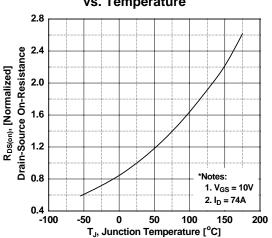


Figure 10. Maximum Drain Current vs. Case Temperature

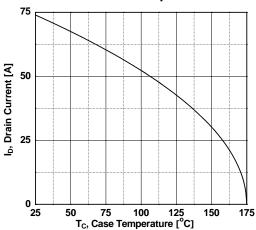
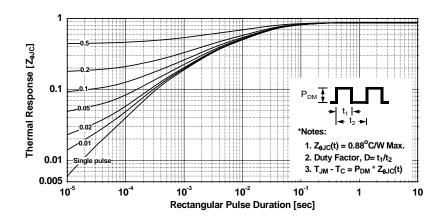
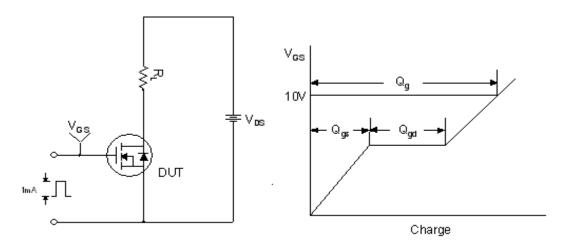


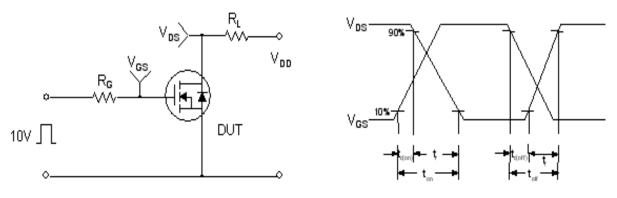
Figure 11. Transient Thermal Response Curve



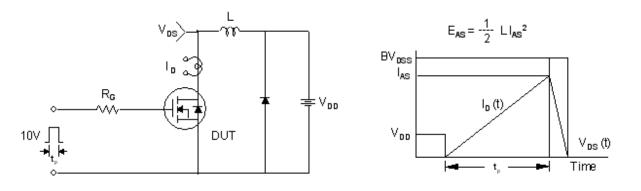
Gate Charge Test Circuit & Waveform



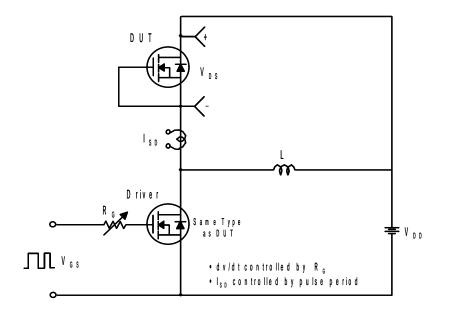
Resistive Switching Test Circuit & Waveforms

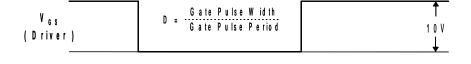


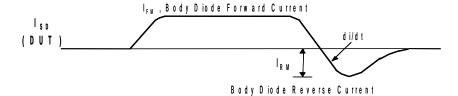
Unclamped Inductive Switching Test Circuit & Waveforms

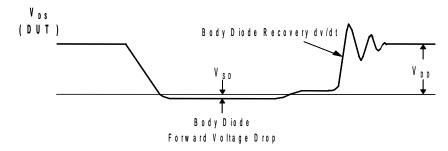


Peak Diode Recovery dv/dt Test Circuit & Waveforms



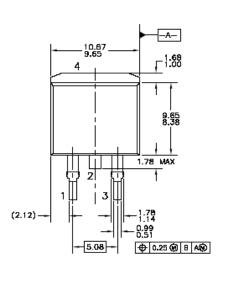


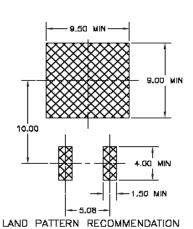


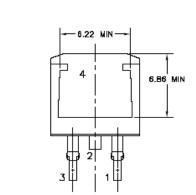


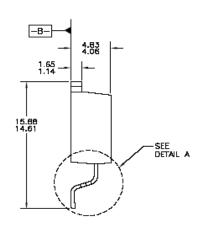
Mechanical Dimensions

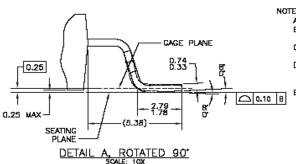
D2-PAK











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 B) REFERENCE JEDEC, TO—263, ISSUE D,
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 AND CENTER OF THE PACKAGE).

 B E) PRESENCE OF TRIMMED CENTER LEAD
 IS OPTIONAL.

TO283AD2REVD Dimensions in Millimeters





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