NPN TRIPLE DIFFUSED PLANAR SILICON TRANSISTOR

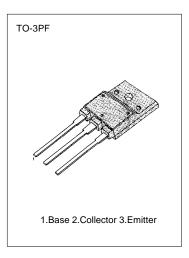
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COLOR TV HORIZONTAL OUTPUT APPLICATION (DAMPER DIODE BUILT IN)

- High Collector-Base Voltage (V_{CBO}=1500V)
- \bullet High Switching Speed (t_F. max=0.4 μ s)

ABSOLUTE MIXIMUM RATING

Characteristic	Symbol	Rating	Unit
Collector Base Voltage	V_{CBO}	1500	V
Collector Emitter Voltage	V_{CEO}	800	V
Emitter Base Voltage	V_{EBO}	6	V
Collector Current	Ic	5	Α
Collector Current (Peak)	Ic	16	Α
Collector Dissipation (T _C =250°C)	Pc	60	W
Junction Temperature	TJ	150	°C
Storage Temperature	T _{STG}	-55 ~ 150	°C

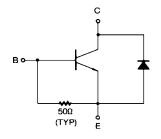


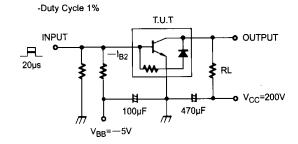
ELECTRICAL CHARACTERISTICS (T_C=25°C)

Characteristic	Symbol	Test Condition	Min	Тур	Max	Unit
Collector Cutoff Current	I _{CBO}	$V_{CB} = 800V, I_{E} = 0$			10	μΑ
Emitter Cutoff Current	I _{EBO}	$V_{EB} = 4V, I_{C} = 0$	40		200	mA
DC Current Gain	h _{FE}	$V_{CE} = 5V, I_{C} = 1A$	8			-
Collector Emitter Saturation Voltage	V _{CE} (sat)	$I_C = 4A, I_B = 0.8A$		3	5	V
Base Emitter Saturation Voltage	V _{BE} (sat)	$I_C = 4A, I_B = 0.8A$			1.5	V
Current Gain Bandwidth Product	f⊤	$V_{CE} = 10V, I_{C} = 1A$		3		MHz
Damper Diode Turn On Voltage	V _F	$I_F = 5A$			2	V
Fall Time	t _F	$I_C = 4A, I_B 1 = 0.8A$			0.4	μs
		$I_B 2 = -1.6A, V_{CC} = 200V$				-
		$R_L = 50\Omega$				

-EQUIVALENT CIRCUIT

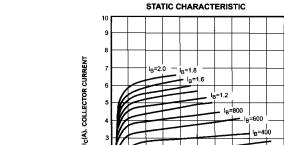
-SWITCHING TIME TEST CIRCUIT

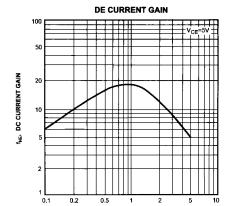




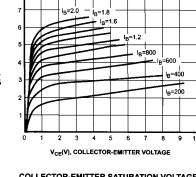


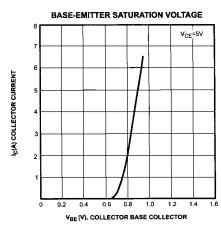
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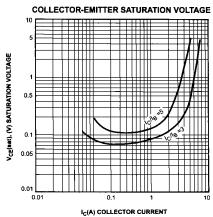
I_C(A) COLLECTOR CURRENT

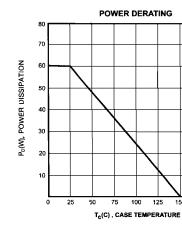


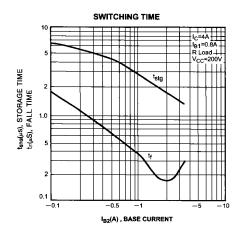


150

175 200



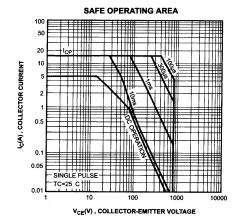


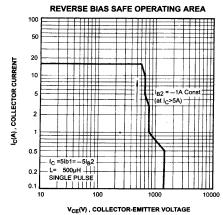


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Definition of Terms

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.	
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.	