

GENERAL DESCRIPTION

Glass passivated triacs in a plastic envelope suitable for surface mounting, intended for use in applications requiring high bidirectional transient and blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

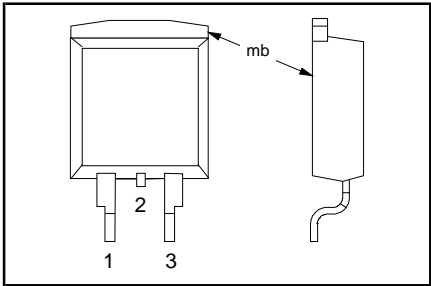
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
		<b>BT138B-500</b>	<b>600</b>	<b>800</b>	
		<b>500F</b>	<b>600F</b>	<b>800F</b>	
		<b>500G</b>	<b>600G</b>	<b>800G</b>	
$V_{DRM}$	Repetitive peak off-state voltages	500	600	800	V
$I_{T(RMS)}$	RMS on-state current	12	12	12	A
$I_{TSM}$	Non-repetitive peak on-state current	95	95	95	A

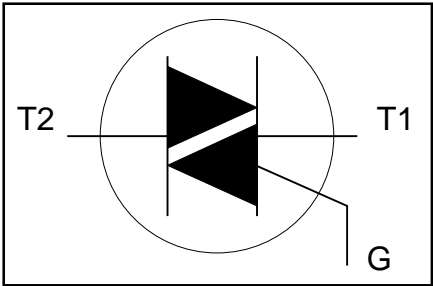
PINNING - SOT404

PIN	DESCRIPTION
1	main terminal 1
2	main terminal 2
3	gate
mb	main terminal 2

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.			UNIT
$V_{DRM}$	Repetitive peak off-state voltages		-	<b>-500</b> 500 <sup>1</sup>	<b>-600</b> 600 <sup>1</sup>	<b>-800</b> 800	V
$I_{T(RMS)}$	RMS on-state current	full sine wave; $T_{mb} \leq 99\text{ }^{\circ}\text{C}$	-	12			A
$I_{TSM}$	Non-repetitive peak on-state current	full sine wave; $T_j = 25\text{ }^{\circ}\text{C}$ prior to surge $t = 20\text{ ms}$ $t = 16.7\text{ ms}$ $t = 10\text{ ms}$	- - - -	95 105 45			A A A <sup>2</sup> s
$I^2t$	$I^2t$ for fusing	$I_{TM} = 20\text{ A}$ ; $I_G = 0.2\text{ A}$ ; $dI_G/dt = 0.2\text{ A}/\mu\text{s}$	-	50			A/ $\mu\text{s}$
$dI_T/dt$	Repetitive rate of rise of on-state current after triggering	T2+ G+ T2+ G- T2- G- T2- G+	- - - -	50 50 50 10			A/ $\mu\text{s}$ A/ $\mu\text{s}$ A/ $\mu\text{s}$ A/ $\mu\text{s}$
$I_{GM}$	Peak gate current		-	2			A
$V_{GM}$	Peak gate voltage		-	5			V
$P_{GM}$	Peak gate power		-	5			W
$P_{G(AV)}$	Average gate power	over any 20 ms period	-	0.5			W
$T_{stg}$	Storage temperature		-40	150			$^{\circ}\text{C}$
$T_j$	Operating junction temperature		-	125			$^{\circ}\text{C}$

1 Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15 A/ $\mu\text{s}$ .

[查询"BT138B"供应商](#)  
[Tlacs](#)

BT138B series

## THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{th\ j-mb}$	Thermal resistance junction to mounting base	full cycle	-	-	1.5	K/W
$R_{th\ j-a}$	Thermal resistance junction to ambient	half cycle minimum footprint, FR4 board	-	-	2.0	K/W
			-	55	-	K/W

## STATIC CHARACTERISTICS

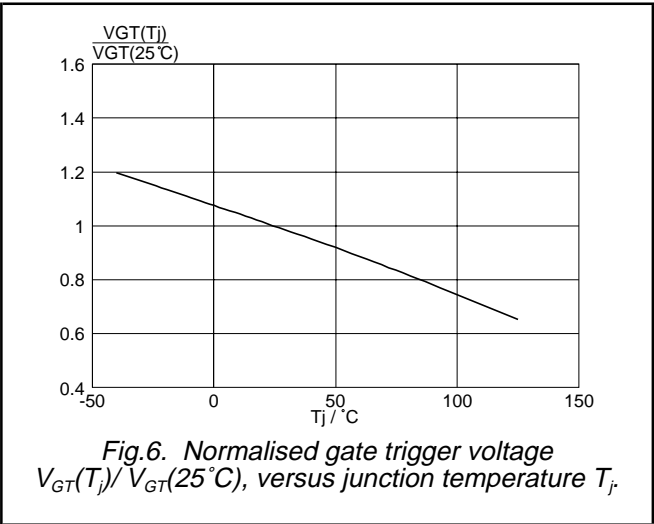
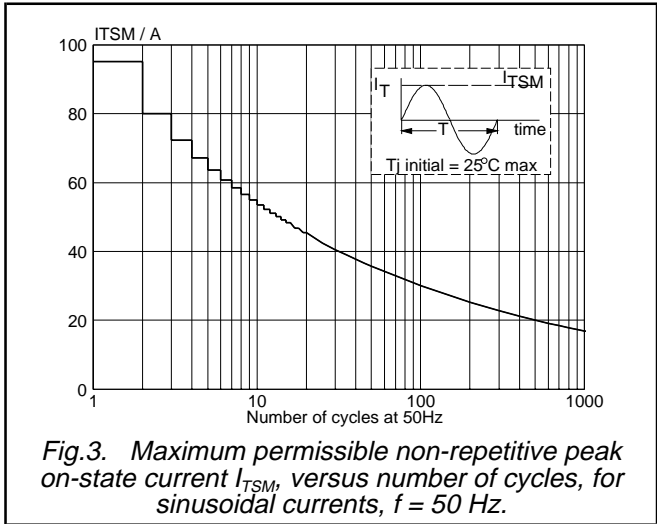
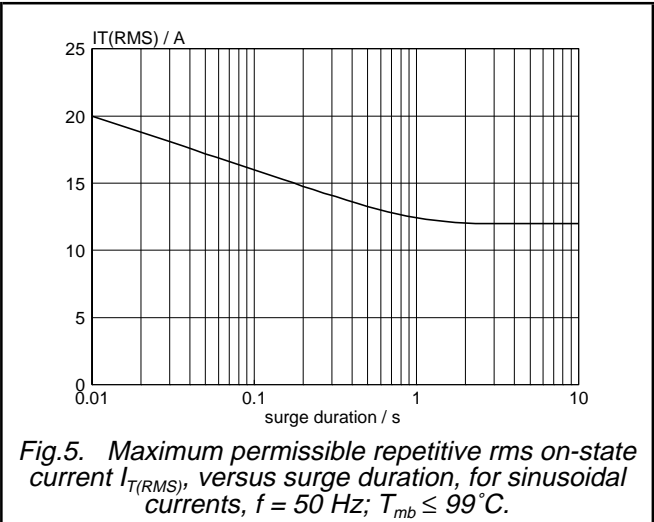
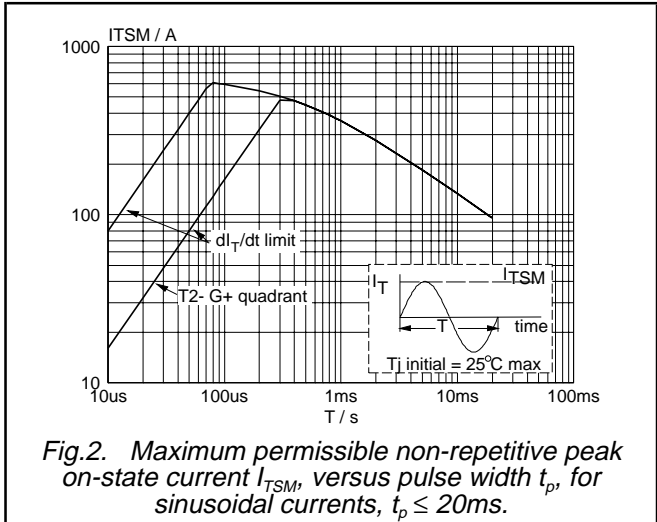
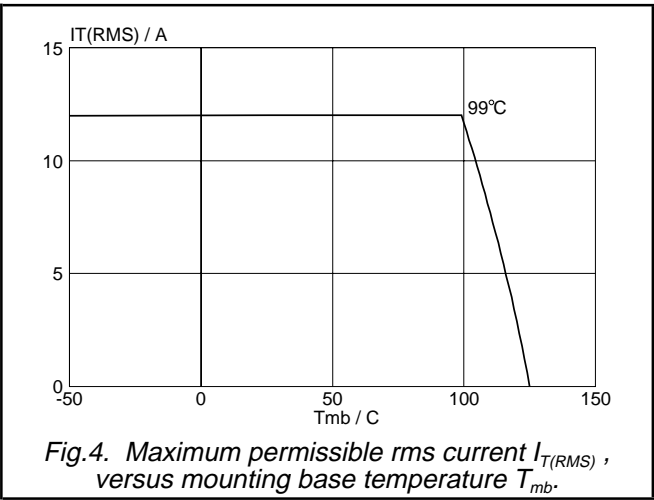
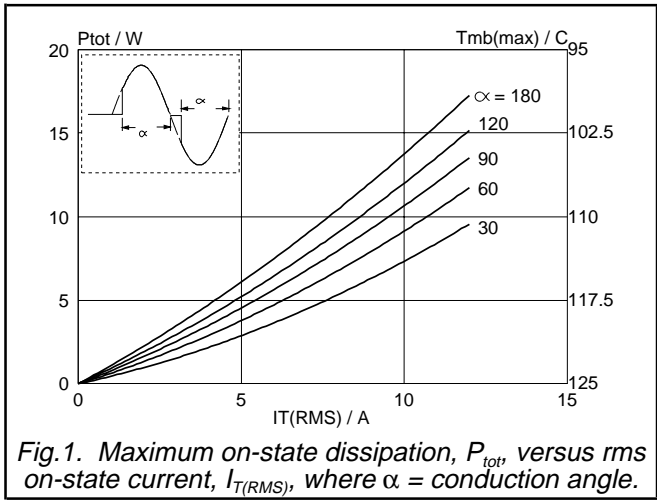
 $T_j = 25\ ^\circ\text{C}$  unless otherwise stated

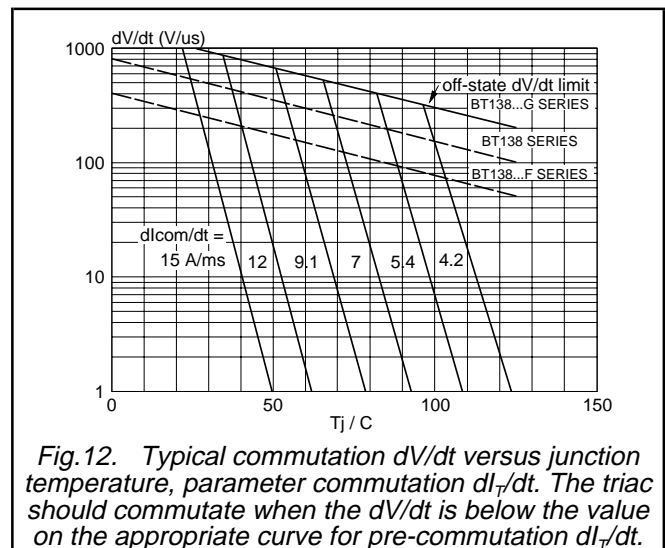
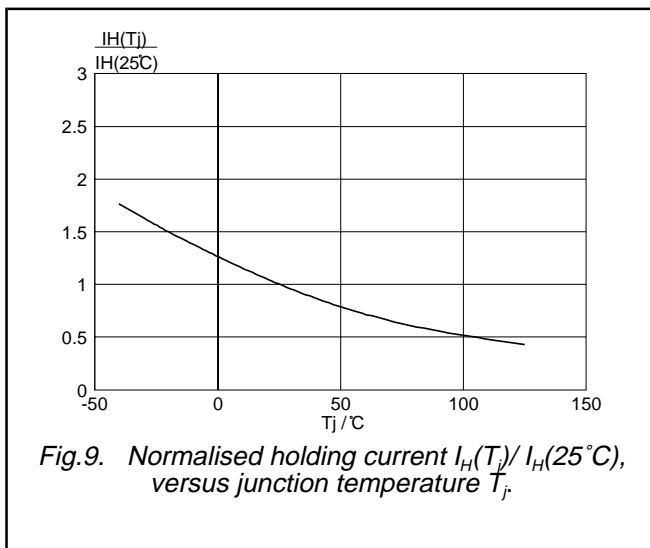
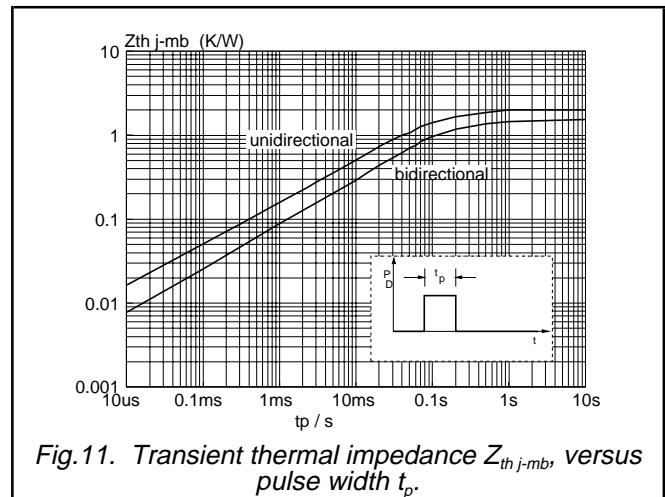
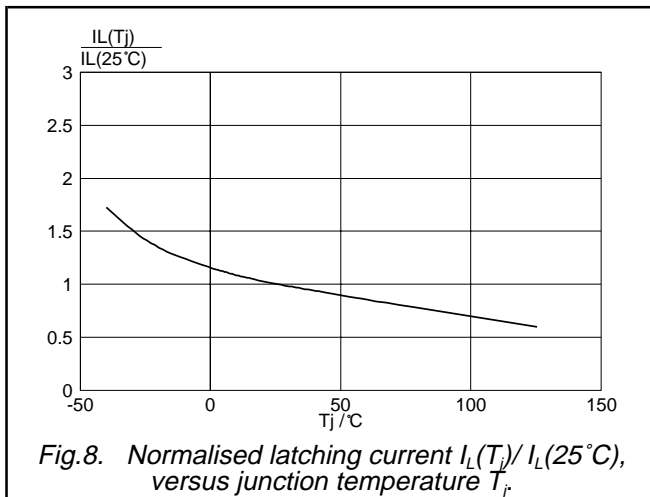
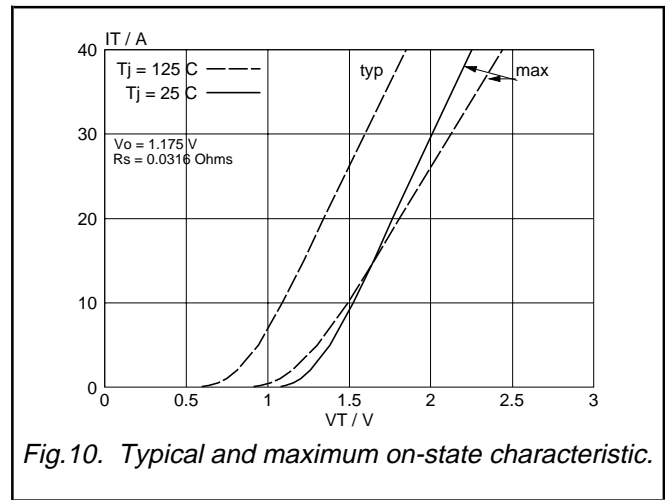
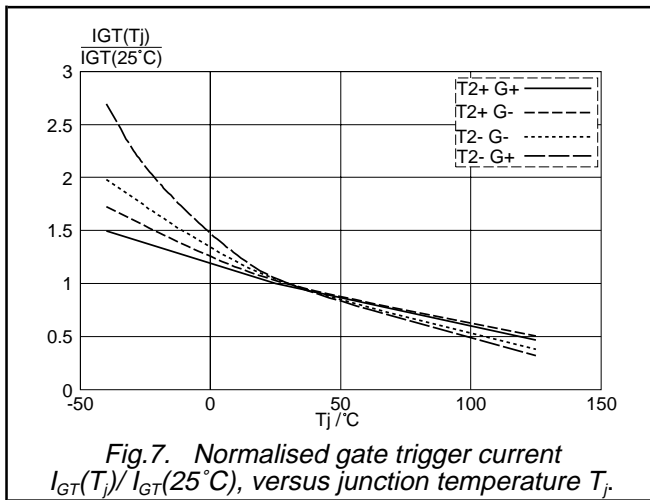
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.			UNIT
$I_{GT}$	Gate trigger current	<b>BT138B-</b> $V_D = 12\ \text{V}; I_T = 0.1\ \text{A}$			...	...F	...G	
		T2+ G+	-	5	35	25	50	mA
		T2+ G-	-	8	35	25	50	mA
		T2- G-	-	10	35	25	50	mA
		T2- G+	-	22	70	70	100	mA
$I_L$	Latching current	$V_D = 12\ \text{V}; I_{GT} = 0.1\ \text{A}$						
		T2+ G+	-	7	40	40	60	mA
		T2+ G-	-	20	60	60	90	mA
		T2- G-	-	8	40	40	60	mA
		T2- G+	-	10	60	60	90	mA
$I_H$	Holding current	$V_D = 12\ \text{V}; I_{GT} = 0.1\ \text{A}$	-	6	30	30	60	mA
$V_T$	On-state voltage	$I_T = 15\ \text{A}$	-	1.4	1.65			V
$V_{GT}$	Gate trigger voltage	$V_D = 12\ \text{V}; I_T = 0.1\ \text{A}$	-	0.7	1.5			V
		$V_D = 400\ \text{V}; I_T = 0.1\ \text{A};$ $T_j = 125\ ^\circ\text{C}$	0.25	0.4	-			V
$I_D$	Off-state leakage current	$V_D = V_{DRM(max)};$ $T_j = 125\ ^\circ\text{C}$	-	0.1	0.5			mA

## DYNAMIC CHARACTERISTICS

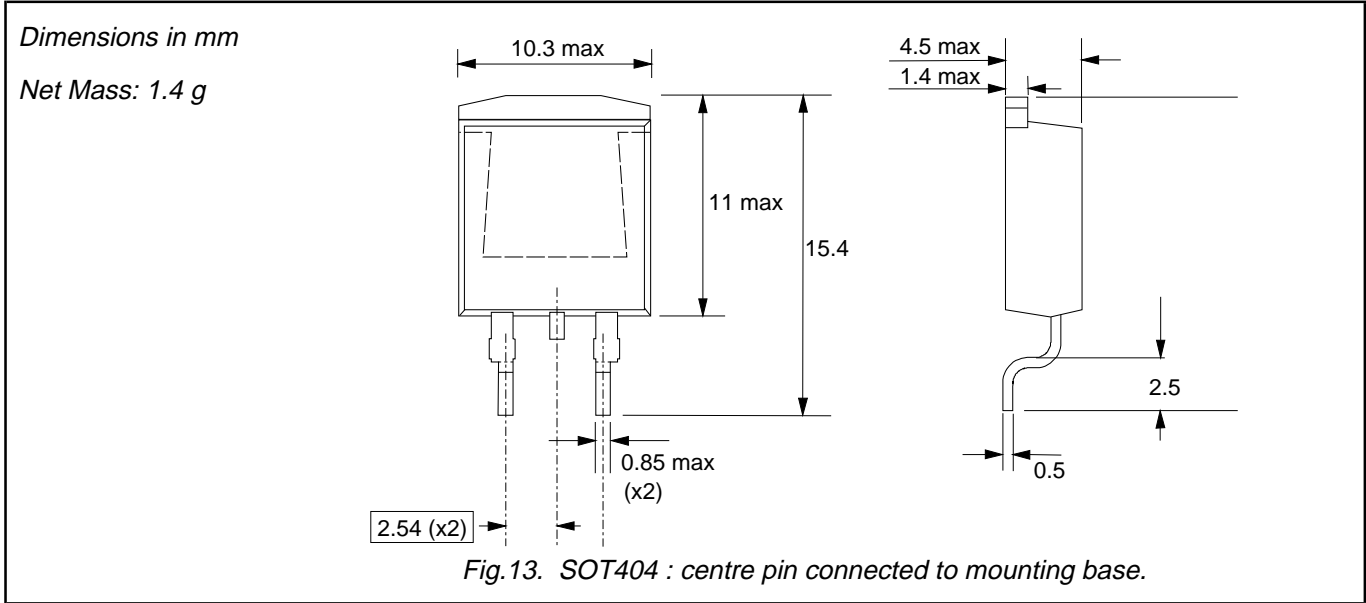
 $T_j = 25\ ^\circ\text{C}$  unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.			TYP.	MAX.	UNIT
$dV_D/dt$	Critical rate of rise of off-state voltage	<b>BT138B-</b> $V_{DM} = 67\% V_{DRM(max)};$ $T_j = 125\ ^\circ\text{C};$ exponential waveform; gate open circuit	...	...F	...G	250	-	V/ $\mu\text{s}$
$dV_{com}/dt$	Critical rate of change of commutating voltage	$V_{DM} = 400\ \text{V}; T_j = 95\ ^\circ\text{C};$ $I_{T(RMS)} = 12\ \text{A};$ $dl_{com}/dt = 5.4\ \text{A/ms};$ gate open circuit	-	-	10	20	-	V/ $\mu\text{s}$
$t_{gt}$	Gate controlled turn-on time	$I_{TM} = 16\ \text{A}; V_D = V_{DRM(max)};$ $I_G = 0.1\ \text{A}; dl_G/dt = 5\ \text{A}/\mu\text{s}$	-	-	-	2	-	$\mu\text{s}$





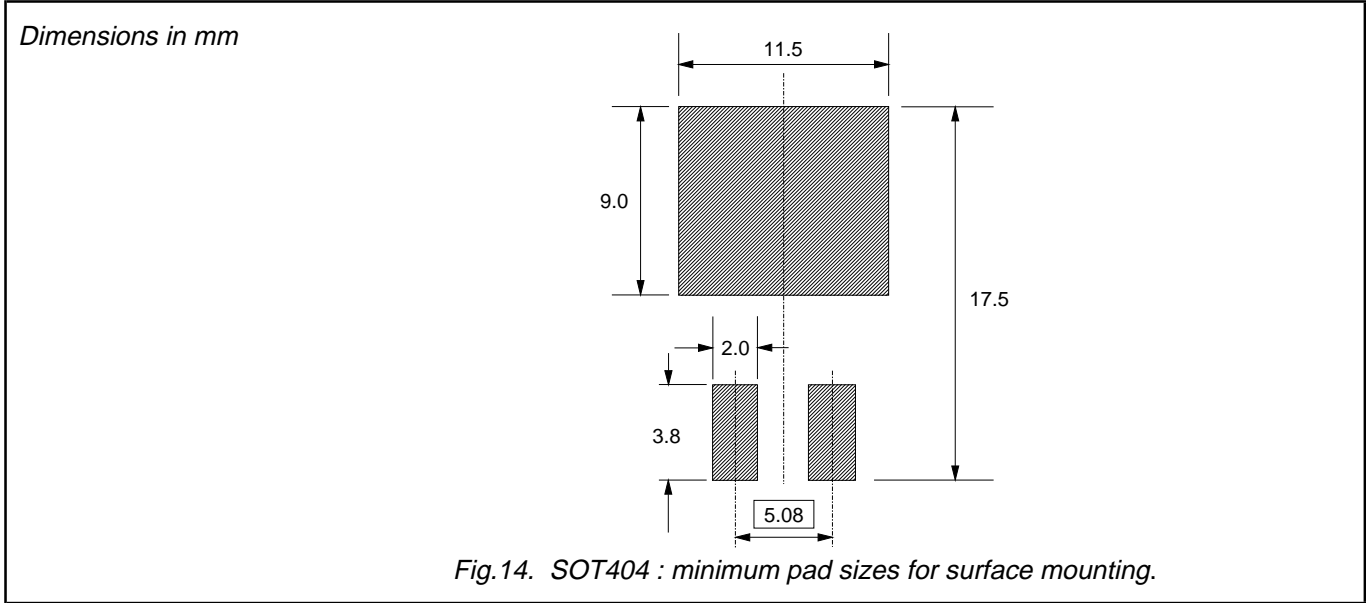
MECHANICAL DATA



Notes

1. Epoxy meets UL94 V0 at 1/8".

MOUNTING INSTRUCTIONS



Notes

1. Plastic meets UL94 V0 at 1/8".

## DEFINITIONS

<b>Data sheet status</b>	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
<b>Limiting values</b>	
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
<b>Application information</b>	
Where application information is given, it is advisory and does not form part of the specification.	
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## LIFE SUPPORT APPLICATIONS

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