



## Programmable Gamma-Voltage Generator and High Slew Rate $V_{COM}$ with Integrated Two-Bank Memory plus 1/2 AVDD Topology

### FEATURES

- 10-Bit Resolution
- 8-Channel P-Gamma
- 1-Channel P- $V_{COM}$
- High Slew Rate  $V_{COM}$ : 45V/ $\mu$ s
- 16x Rewritable Nonvolatile Memory
- Two Independent Pin-Selectable Memory Banks
- Rail-to-Rail Output:
  - 300mV Min Swing-to-Rail (10mA)
  - > 300mA Max  $I_{OUT}$
- Low Supply Current
- Supply Voltage: 9V to 20V
- Digital Supply: 2V to 5.5V
- Two-Wire Interface:
  - Supports 400kHz and 3.4MHz
- 1/2 AVDD Capability

### APPLICATIONS

- TFT-LCD Reference Drivers

### DESCRIPTION

The BUF08630 offers eight programmable gamma channels and one programmable  $V_{COM}$  channel.

The final gamma and  $V_{COM}$  values can be stored in the on-chip, nonvolatile memory. To allow for programming errors or liquid crystal display (LCD) panel rework, the BUF08630 supports up to 16 write operations to the on-chip memory.

The BUF08630 has two separate memory banks, allowing simultaneous storage of two different gamma curves to facilitate switching between gamma curves.

All gamma and  $V_{COM}$  channels offer a rail-to-rail output that typically swings to within 150mV of either supply rail with a 10mA load. All channels are programmed using a two-wire interface that supports standard operations up to 400kHz and high-speed data transfers up to 3.4MHz.

For the complete BUF08630 data sheet, contact your TI representative.

### RELATED PRODUCTS

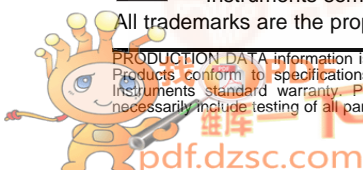
FEATURES	PRODUCT
22-channel gamma correction buffer	<a href="#">BUF22821</a>
16-channel gamma correction buffer	<a href="#">BUF16821</a>
12-channel gamma correction buffer	<a href="#">BUF12800</a>
18-/20-channel programmable buffer, 10-Bit, $V_{COM}$	<a href="#">BUF20800</a>
18-/20-Channel programmable buffer with memory	<a href="#">BUF20820</a>
Programmable $V_{COM}$ driver	<a href="#">BUF01900</a>
22V supply, traditional gamma buffers	<a href="#">BUF11705</a>
High-resolution, fully-programmable LCD bias IC for TV	<a href="#">TPS65168</a>



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PACKAG

## PACKAGING INFORMATION

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/ Ball Finish	MSL Peak Temp
BUF08630RGWR	ACTIVE	VQFN	RGW	20	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-2600
BUF08630RGWT	ACTIVE	VQFN	RGW	20	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-2600

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com> for more information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all RoHS materials, with the exception of lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in high temperature applications.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based eutectic solder used between the leadframe and die. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

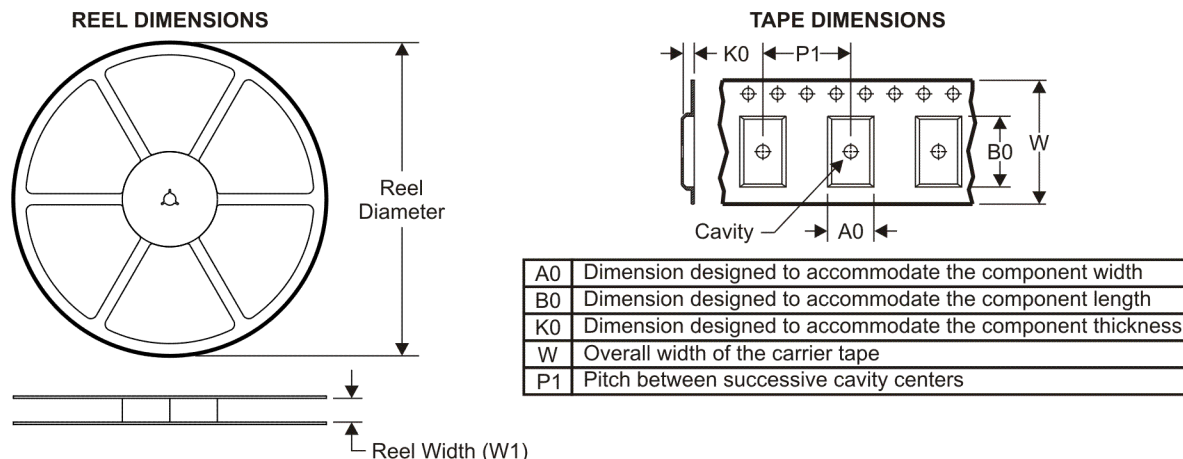
**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (both of which are RoHS prohibited materials) in homogeneous material.

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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TAPE AND REEL INFORMATION



QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
BUF08630RGWR	VQFN	RGW	20	3000	330.0	12.4	5.3	5.3	1.5	8.0	12.0	Q2
BUF08630RGWT	VQFN	RGW	20	250	180.0	12.4	5.3	5.3	1.5	8.0	12.0	Q2

**TAPE AND REEL BOX DIMENSIONS**

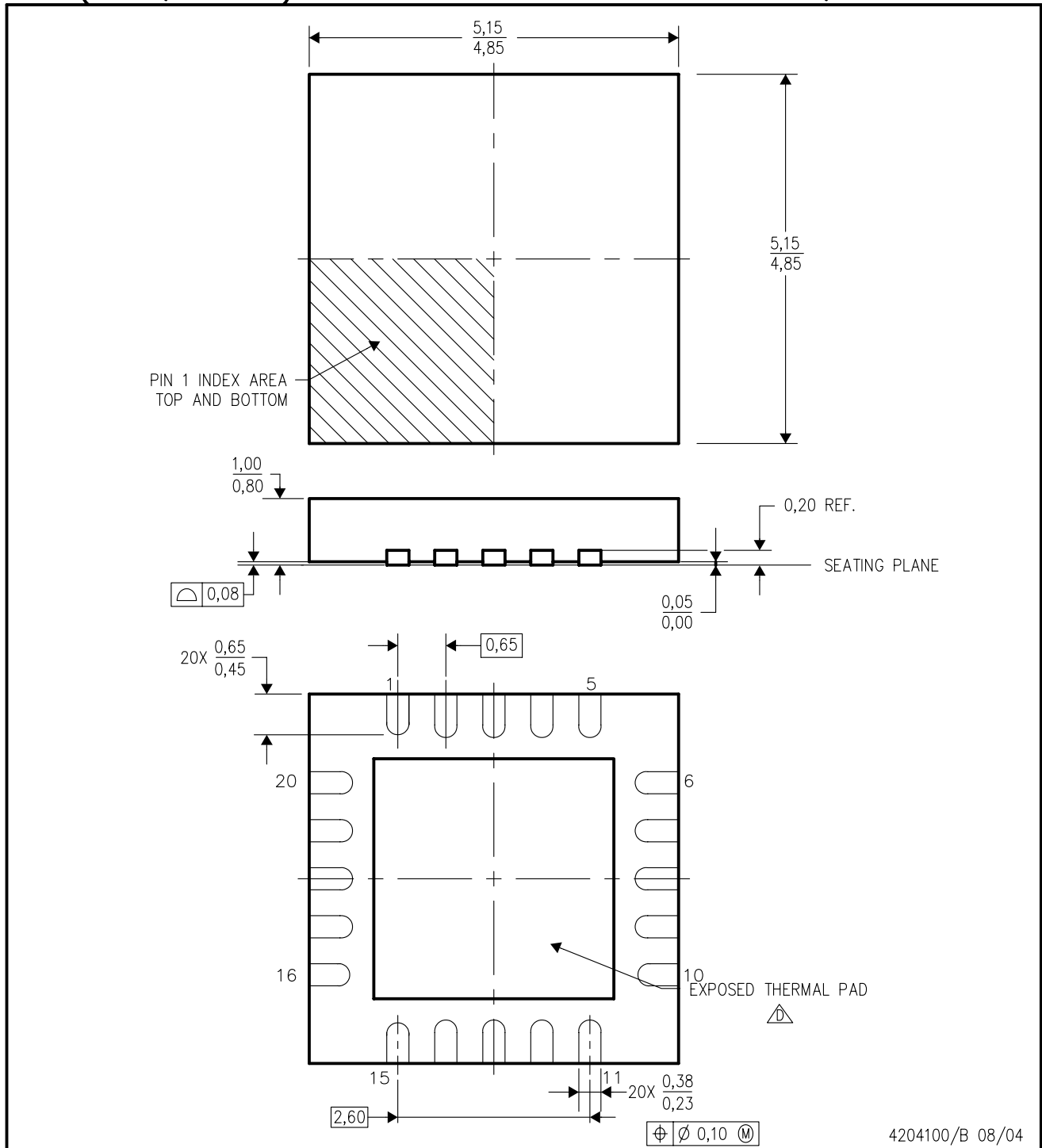



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
BUF08630RGWR	VQFN	RGW	20	3000	346.0	346.0	29.0
BUF08630RGWT	VQFN	RGW	20	250	190.5	212.7	31.8

[查询"BUF08630"供应商](#)

RGW (S-PQFP-N20) PLASTIC QUAD FLATPACK



- NOTES:
- A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5-1994.
  - B. This drawing is subject to change without notice.
  - C. Quad Flat pack, No-leads (QFN) package configuration
  -  The package thermal pad must be soldered to the board for thermal and mechanical performance.. See the Product Data Sheet for details regarding the exposed thermal pad dimensions.
  - E. Falls within JEDEC MO-220.

## THERMAL PAD MECHANICAL DATA

[查询"BUF08630"供应商](#)

RGW (S=PVQFN=N20)

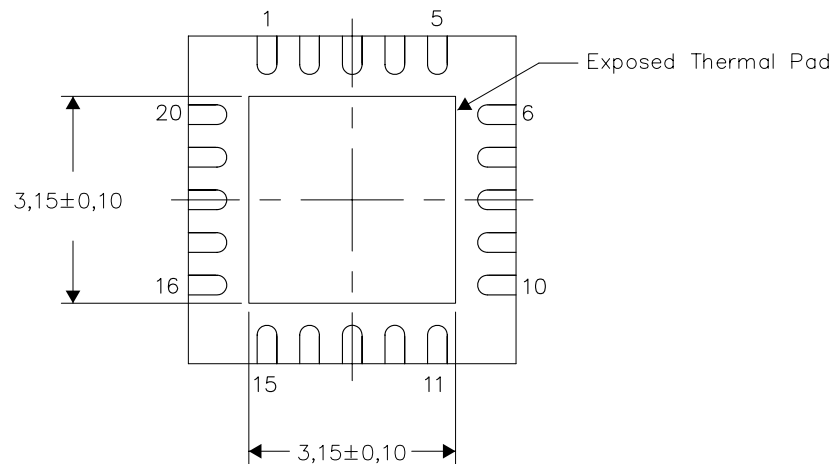
PLASTIC QUAD FLATPACK NO-LEAD

### THERMAL INFORMATION

This package incorporates an exposed thermal pad that is designed to be attached directly to an external heatsink. The thermal pad must be soldered directly to the printed circuit board (PCB). After soldering, the PCB can be used as a heatsink. In addition, through the use of thermal vias, the thermal pad can be attached directly to the appropriate copper plane shown in the electrical schematic for the device, or alternatively, can be attached to a special heatsink structure designed into the PCB. This design optimizes the heat transfer from the integrated circuit (IC).

For information on the Quad Flatpack No-Lead (QFN) package and its advantages, refer to Application Report, QFN/SON PCB Attachment, Texas Instruments Literature No. SLUA271. This document is available at [www.ti.com](http://www.ti.com).

The exposed thermal pad dimensions for this package are shown in the following illustration.

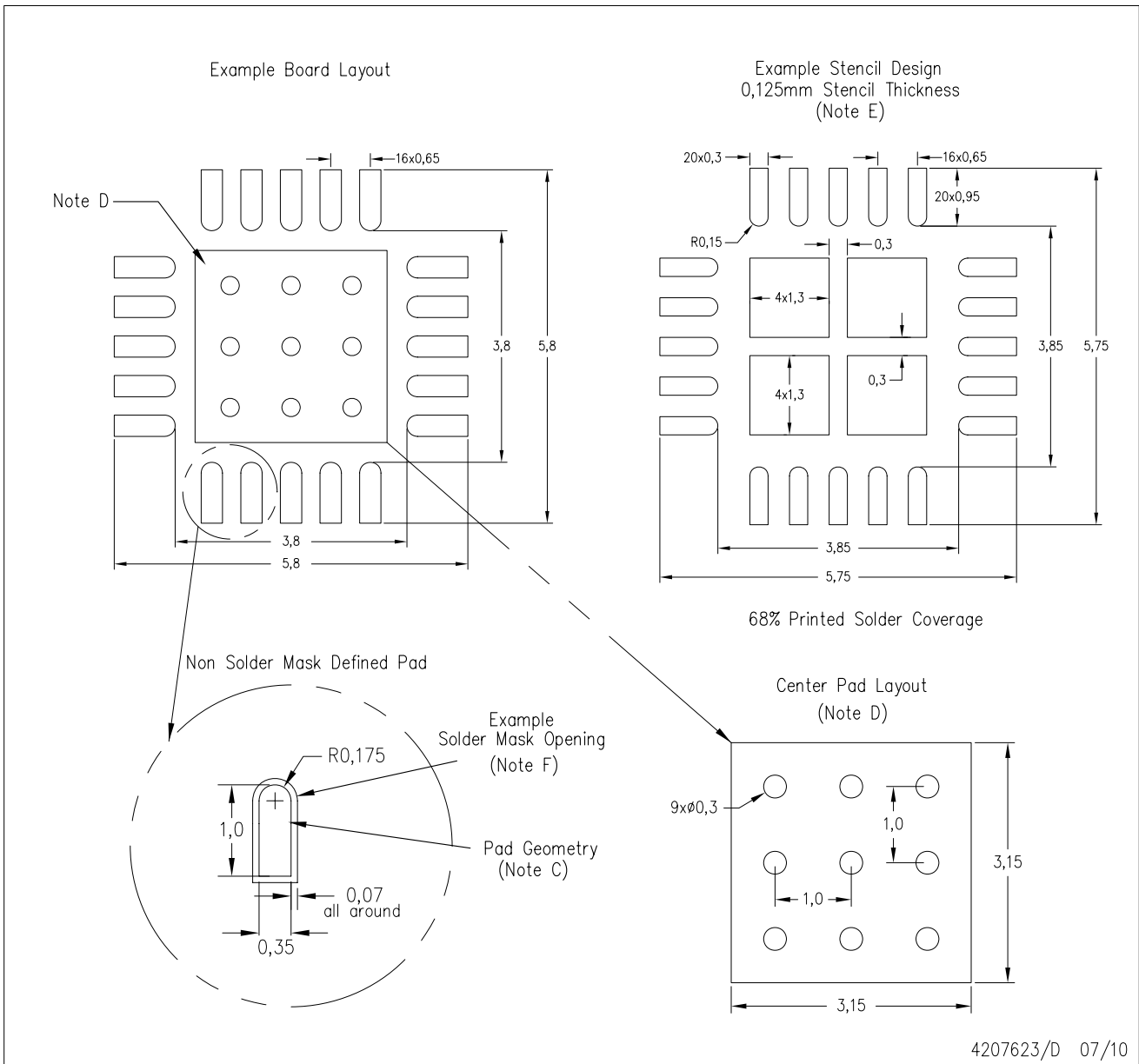


Bottom View

NOTE: All linear dimensions are in millimeters

Exposed Thermal Pad Dimensions

4206352-2/H 07/10



- NOTES:
- All linear dimensions are in millimeters.
  - This drawing is subject to change without notice.
  - Publication IPC-7351 is recommended for alternate designs.
  - This package is designed to be soldered to a thermal pad on the board. Refer to Application Note, QFN Packages, Texas Instruments Literature No. SLUA271, and also the Product Data Sheets for specific thermal information, via requirements, and recommended board layout. These documents are available at [www.ti.com](http://www.ti.com) <<http://www.ti.com>>.
  - Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC 7525 for stencil design considerations.
  - Customers should contact their board fabrication site for solder mask tolerances.

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