DATA SHEET



SILICON POWER TRANSISTOR 2SA1742

PNP SILICON EPITAXIAL TRANSISTOR FOR HIGH-SPEED SWITCHING

The 2SA1742 is a power transistor developed for high-speed switching and features a high hre at low $V_{CE(sat)}$. This transistor is ideal for use as a driver in DC/DC converters and actuators.

In addition, a small resin-molded insulation type package contributes to high-density mounting and reduction of mounting cost.

FEATURES

- High hFE and low VCE(sat): hFE ≥ 100 MIN. @VCE = -2.0 V, IC = -1.5 A VCE(sat) ≥ -0.3 V MAX. @IC = -4.0 V, IB = -0.2 A
- Full-mold package that does not require an insulating board or bushing

Parameter Symbol Conditions Ratings Unit -100 ٧ Collector to base voltage Vсво -60 v Collector to emitter voltage VCEO -7.0 V Emitter to base voltage Vево -7.0 Collector current (DC) IC(DC) А $\overline{PW} \le 300 \ \mu s$, Collector current (pulse) -14 А C(pulse) duty cycle $\leq 10\%$ -3.5 A Base current (DC) B(DC) Total power dissipation Pτ Tc = 25°C 30 W T_A = 25°C 2.0 W °C Junction temperature Tj 150 Storage temperature Tstg -55 to +150 °C

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

ORDERING INFORMATION

Part No.	Package
2SA1742	Isolated TO-220

(Isolated TO-220)



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ELECTRICAL420 HARAGTERISTICS (TA = 25°C)

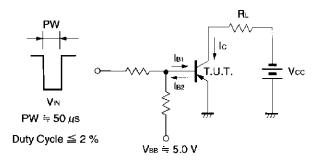
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Collector to emitter voltage	VCEO(SUS)	Ic = −4.0 V, I _B = −0.4 A, L = 1 mH	-60			V
	VCEX(SUS)	Ic = -4.0 A, I _{B1} = -I _{B2} = -0.4 A, V _{BE(OFF)} = 1.5 V, L = 180 μ H, clamped	-60			V
Collector cutoff current	Ісво	$V_{CB} = -60 \text{ V}, \text{ I}_{E} = 0 \text{ A}$			-10	μA
	ICER	$V_{CE} = -60 \text{ V}, \text{ Rbe} = 50 \Omega, \text{ Ta} = 125^{\circ}\text{C}$			-1.0	mA
	ICEX1	$V_{CE} = -60 \text{ V}, \text{ V}_{BE(OFF)} = 1.5 \text{ V}$			-10	μA
	ICEX2	$\label{eq:Vce} \begin{split} V_{\text{CE}} &= -60 \text{ V}, \text{ V}_{\text{BE(OFF)}} = 1.5 \text{ V}, \\ T_{\text{A}} &= 125^{\circ}\text{C} \end{split}$			-1.0	mA
Emitter cutoff current	Іево	$V_{EB} = -5.0 \text{ V}, \text{ Ic} = 0 \text{ A}$			-10	μΑ
DC current gain	hfe1	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.7 \text{ A}^{Note}$	100			
	hFE2	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -1.5 \text{ A}^{Note}$	100		400	
	hfe3	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -4.0 \text{ A}^{Note}$	60			
Collector saturation voltage	VCE(sat)1	$I_{C} = -4.0 \text{ A}, I_{B} = -0.2 \text{ A}^{Note}$			-0.3	V
	VCE(sat)2	$I_{C} = -6.0 \text{ A}, I_{B} = -0.3 \text{ A}^{Note}$			-0.5	V
Base saturation voltage	VBE(sat)1	$I_{C} = -4.0 \text{ A}, I_{B} = -0.2 \text{ A}^{Note}$			-1.2	V
	VBE(sat)2	$I_{C} = -6.0 \text{ A}, I_{B} = -0.3 \text{ A}^{Note}$			-1.5	V
Collector capacitance	Cob	$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0 \text{ A}, \text{ f} = 1.0 \text{ MHz}$		180		pF
Gain bandwidth product	f⊤	$V_{CB} = -10 \text{ V}, \text{ Ic} = -1.0 \text{ A}$		40		MHz
Turn-on time	ton	lc = −4.0 A, RL = 12.5 Ω,			0.3	μs
Storage time	tstg	$I_{B1} = -I_{B2} = -0.2 \text{ A}, \text{ Vcc} \cong -50 \text{ V}$			1.5	μs
Fall time	tr	Refer to the test circuit.			0.3	μs

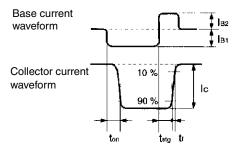
Note Pulse test PW \leq 350 μ s, duty cycle \leq 2%

hfe CLASSIFICATION

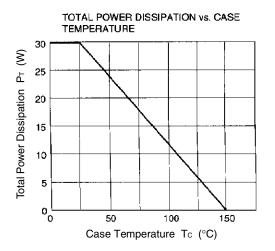
Marking	М	L	К
hfe2	100 to 200	150 to 300	200 to 400

SWITCHING TIME (ton, tstg, tf) TEST CIRCUIT

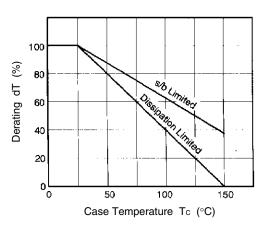




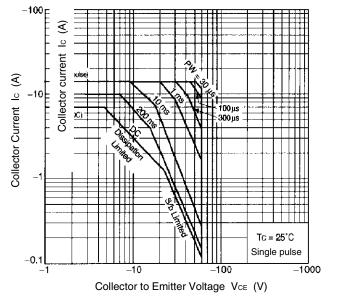
查询"2004/CA12"供应商CTERISTICS (TA = 25°C)



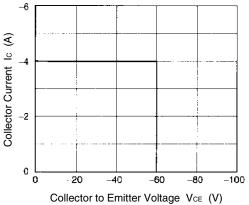
DERATING CURVE OF SAFE OPERATING AREA

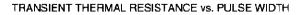


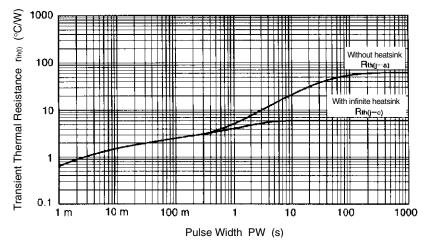
FORWARD BIAS SAFE OPERATING AREA

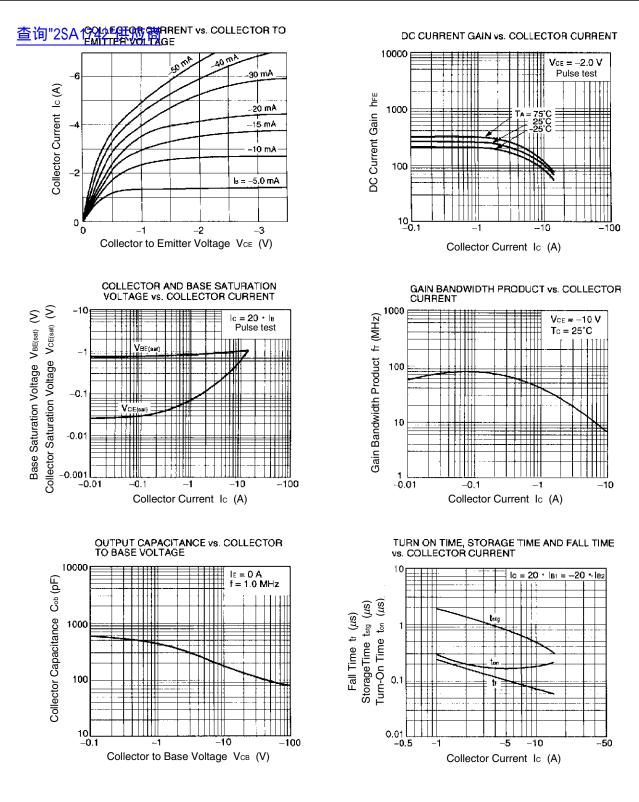


REVERSE BIAS SAFE OPERATING AREA



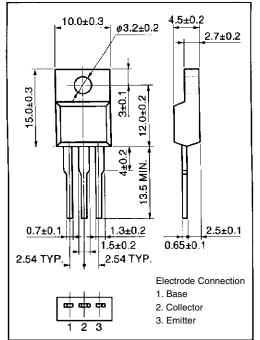






查询论ACKAGE供应ATVING (UNIT: mm)

Isolated TO-220



查询"2SA1742"供应商

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